EUROPEAN PATENT OFFICE
U.S. PATENT AND TRADEMARK OFFICE

CPC NOTICE OF CHANGES 1812

DATE: JANUARY 1, 2026

PROJECT MP12708

The following classification changes will be effected by this Notice of Changes:

Action	Subclass	Group(s)
SCHEME:		
Titles Changed:	A61M	16/207
	B05B	12/1445
	B60G	2600/71
	B66B	1/3438
	B67C	3/288
	E05B	27/0053
	E05B	29/0046
	F16D	2025/081
	F16D	2048/0212, 2048/0251
	F16D	66/025
	F16D	2500/5016
	G03B	2215/0557
	G05B	2219/1201, 2219/1215, 2219/1216, 2219/15006, 2219/21005, 2219/21039, 2219/21074, 2219/21075, 2219/21079, 2219/2221, 2219/2228, 2219/2229, 2219/2231, 2219/2232, 2219/2233, 2219/2234, 2219/2235, 2219/2236, 2219/2237, 2219/2238, 2219/23465, 2219/24183, 2219/25048, 2219/25049, 2219/25085, 2219/25212, 2219/25223, 2219/25477, 2219/31179, 2219/31207, 2219/31209, 2219/31229, 2219/31248, 2219/31379, 2219/31448, 2219/32051, 2219/32052, 2219/33063, 2219/33149, 2219/33163, 2219/33168, 2219/33241, 2219/33339, 2219/33341, 2219/33342, 2219/33343, 2219/33344, 2219/33345, 2219/33346, 2219/33347, 2219/34446, 2219/36454, 2219/39122, 2219/39139, 2219/39141, 2219/39142, 2219/39145, 2219/39312, 2219/40133, 2219/40134, 2219/40135, 2219/40138, 2219/40139, 2219/40144, 2219/40145, 2219/40146, 2219/40182, 2219/40186, 2219/40187, 2219/40188, 2219/40268, 2219/40399, 2219/40401, 2219/40402, 2219/40403, 2219/40404, 2219/40405, 2219/40406, 2219/40407, 2219/41407, 2219/42184, 2219/42185, 2219/42186, 2219/42188,

Action	Subclass	Group(s)
		2219/42191, 2219/42335, 2219/49379, 2219/50219
	G06F	9/3877, 2009/3883
	G06F	11/2242
	G07F	17/3223
	G08B	13/19643
	H03H	2210/046
	H03J	2200/18
	H04B	7/269
	H04N	23/662
	H04N	2201/0031, 2201/0032
DEFINITIONS:		
Definitions New:	E05B	27/0053
	E05B	29/0046
Definitions Modified:	F02D	25/00
	F15B	7/00, 7/06
	F16D	2048/0212
	F16D	65/28
	G06F	9/3877, 9/3879, 9/3881, 9/3885, 9/4416
	G06F	11/00, 11/1637, 11/1654
	G06F	21/1087
	H04J	3/0638, 3/0679
	H04L	67/12
	H04M	1/72412
	H04M	15/7652
	H04N	21/23, 21/4751

No other subclasses/groups are impacted by this Notice of Changes.

This Notice of Changes includes the following *[Check the ones included]:*

1. CLASSIFICATION SCHEME CHANGES

    ☒ A. New, Modified or Deleted Group(s)

    ☐ B. New, Modified or Deleted Warning(s)

    ☐ C. New, Modified or Deleted Note(s)

    ☐ D. New, Modified or Deleted Guidance Heading(s)

2. DEFINITIONS

    ☒ A. New or Modified Definitions (Full definition template)

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       ☒   B. Modified or Deleted Definitions (Definitions Quick Fix)

3. ☐ REVISION CONCORDANCE LIST (RCL)

4. ☐ CHANGES TO THE CPC-TO-IPC CONCORDANCE LIST (CICL)

5. ☐ CHANGES TO THE CROSS-REFERENCE LIST (CRL)

1. CLASSIFICATION SCHEME CHANGES

A. New, Modified or Deleted Group(s)

SUBCLASS A61M - DEVICES FOR INTRODUCING MEDIA INTO, OR ONTO, THE BODY; DEVICES FOR TRANSDUCING BODY MEDIA OR FOR TAKING MEDIA FROM THE BODY; DEVICES FOR PRODUCING OR ENDING SLEEP OR STUPOR

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	A61M16/207	3	{Membrane valves with pneumatic amplification stage, i.e. having leader and follower membranes}	

SUBCLASS B05B - SPRAYING APPARATUS; ATOMISING APPARATUS; NOZZLES

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	B05B12/1445	3	{pumping means for the liquids or other fluent materials being mechanically linked, e.g. leader-follower pumps}	

SUBCLASS B60G - VEHICLE SUSPENSION ARRANGEMENTS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	B60G2600/71	1	Distributed control; Hierarchical control structure, e.g. with one unit steering and other units following; Remote control units	

SUBCLASS B66B - ELEVATORS; ESCALATORS OR MOVING WALKWAYS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	B66B1/3438	4	{Controlling-dependent control system configuration}	

SUBCLASS B67C - CLEANING, FILLING WITH LIQUIDS OR SEMILIQUIDS, OR EMPTYING, OF BOTTLES, JARS, CANS, CASKS, BARRELS, OR SIMILAR CONTAINERS, NOT OTHERWISE PROVIDED FOR; FUNNELS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	B67C3/288	5	{using leader-follower controls}	

SUBCLASS E05B - LOCKS; ACCESSORIES THEREFOR; HANDCUFFS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	E05B27/0053	1	{for use with more than one key, e.g. master-secondary key}	
M	E05B29/0046	1	{for use with more than one key, e.g. master-secondary key}	

SUBCLASS F16D - COUPLINGS FOR TRANSMITTING ROTATION; CLUTCHES; BRAKES

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	F16D2025/081	2	{Hydraulic devices that initiate movement of pistons in secondary cylinders for actuating clutches, i.e. primary cylinders}	
M	F16D2048/0212	2	{Details of pistons for primary or secondary cylinders especially adapted for fluid control}	
M	F16D2048/0251	4	{Electric motor driving a piston, e.g. for actuating the primary cylinder}	
M	F16D66/025	4	{sensing the position of parts of the brake system other than the braking members, e.g. limit switches mounted on primary cylinders}	
M	F16D2500/5016	3	Shifting operation, i.e. volume compensation of the primary cylinder due to wear, temperature changes or leaks in the cylinder	

SUBCLASS G03B - APPARATUS OR ARRANGEMENTS FOR TAKING PHOTOGRAPHS OR FOR PROJECTING OR VIEWING THEM; APPARATUS OR ARRANGEMENTS EMPLOYING ANALOGOUS TECHNIQUES USING WAVES OTHER THAN OPTICAL WAVES; ACCESSORIES THEREFOR

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	G03B2215/0557	3	Multiple units, e.g. leader-follower systems	

SUBCLASS G05B - CONTROL OR REGULATING SYSTEMS IN GENERAL; FUNCTIONAL ELEMENTS OF SUCH SYSTEMS; MONITORING OR TESTING ARRANGEMENTS FOR SUCH SYSTEMS OR ELEMENTS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	G05B2219/1201	3	Each plc can act as leader, flying leader	
M	G05B2219/1215	3	Leader-follower system	
M	G05B2219/1216	3	Interlock problem, avoid sending data to follower when follower processes data	
M	G05B2219/15006	3	Set configuration from leader control station	
M	G05B2219/21005	3	Several follower modules connected to same I-O of leader, multiplexed by leader	
M	G05B2219/21039	3	Followers, modules in daisy chain, each handles control data, transmits to next	
M	G05B2219/21074	3	Leader has keyboard to enter address of called follower	
M	G05B2219/21075	3	Initialise each module random, count down, if zero leader sets address	
M	G05B2219/21079	3	Allocate at start up also to each controlled device a code for the leader	
M	G05B2219/2221	3	Only common memory in host, leader, no local memory in follower, local controller	
M	G05B2219/2228	3	Leader detects and configures followers	
M	G05B2219/2229	3	Multiprocessing, change over from leader-follower to peer-to-peer, no leader	
M	G05B2219/2231	3	Leader-follower	
M	G05B2219/2232	3	Leader executes modified program on follower demand	
M	G05B2219/2233	3	Each follower can control several other followers	
M	G05B2219/2234	3	Each follower can function in standalone if leader fails	
M	G05B2219/2235	3	Each follower has library of states during which operation is permitted to start	
M	G05B2219/2236	3	Leader determines critical time when each of followers must be controlled	
M	G05B2219/2237	3	Selection of leader or follower	
M	G05B2219/2238	3	Several leaders at same time	
M	G05B2219/23465	3	Leader processor blocks input of data to followers	
M	G05B2219/24183	3	If error, spare unit takes over, message to leader, confirm new configuration	
M	G05B2219/25048	3	Leader clock and several frequency dividers, for motion and sequence control	

M	G05B2219/25049	3	Leader processor gives timing information to followers	
M	G05B2219/25085	3	Several function expansion units for leader, main unit, universal system	
M	G05B2219/25212	3	Leader address node, node answers ready, leader sends command, node executes it	
M	G05B2219/25223	3	Follower has registers to indicate leader, acknowledge, transfer address, read write	
M	G05B2219/25477	3	Leader waits for signal from follower, follower active thereafter, during limited time	
M	G05B2219/31179	3	Leader sends message with address of follower to all followers, follower answers, interrupt	
M	G05B2219/31207	3	Leader sends global files to autonomous controllers, feedback of process status	
M	G05B2219/31209	3	Leader actuator sensor interface has priority over host, build into host	
M	G05B2219/31229	3	Supervisor, leader, workstation controller, automation, machine control	
M	G05B2219/31248	3	Multiple data link layer leaders, if one fails, other takes over	
M	G05B2219/31379	3	Leader monitors controllers, updates production progress, allocates resources	
M	G05B2219/31448	3	Display at central computer, follower displays for each machine unit	
M	G05B2219/32051	3	Central control, modify program follower computers as function of production demand from host	
M	G05B2219/32052	3	Lookup table, identify job to be executed by leader or follower	
M	G05B2219/33063	3	Generic coordination, leader agent to data manager agent to tasks to active agent	
M	G05B2219/33149	3	Publisher subscriber, publisher, leader broadcasts data to followers, subscriber	
M	G05B2219/33163	3	Multichannel main bus	
M	G05B2219/33168	3	Two buses, main bus and local servo bus	
M	G05B2219/33241	3	Compare results from two leaders on two busses, if not equal shut down machines	
M	G05B2219/33339	3	Controller with lowest operation rate is selected as leader	
M	G05B2219/33341	3	Peer-to-peer, change leader if overloaded	
M	G05B2219/33342	3	Leader-follower, supervisor, front end and follower processor, hierarchical structure	
M	G05B2219/33343	3	Each follower stores communication program to be used by leader, exchangeability	
M	G05B2219/33344	3	Each follower has several processors operating in parallel	
M	G05B2219/33345	3	Several leader modules, connection modules and follower modules	

M	G05B2219/33346	3	Only memory of leader module stores all position programs of followers	
M	G05B2219/33347	3	Leader sends servo address, speed, kind of interpolation to follower	
M	G05B2219/34446	3	No change of operation mode when follower axis is out of synchronisation	
M	G05B2219/36454	3	Leader-follower, director agent, operator replication	
M	G05B2219/39122	3	Follower, follower mirrors leader, leader	
M	G05B2219/39139	3	Produce program of follower from path of leader and desired relative position	
M	G05B2219/39141	3	Follower program has no taught positions, receives position from leader, convert from leader	
M	G05B2219/39142	3	Moving time between positions in follower program coordinated online with leader	
M	G05B2219/39145	3	Follower path is the same as leader path and superposed desired relative movement	
M	G05B2219/39312	3	Double neural network for tracking, follower microprocessor for servo control	
M	G05B2219/40133	3	Force sensation of follower converted to movement of chair for operator	
M	G05B2219/40134	3	Force sensation of follower converted to vibration for operator	
M	G05B2219/40135	3	Follower force converted to shape display, actuated by fingers, surface is force image	
M	G05B2219/40138	3	Scaled feedback of forces from follower to leader and leader to follower	
M	G05B2219/40139	3	Force from follower converted to a digital display-like fingers and object	
M	G05B2219/40144	3	Force sensation feedback from follower	
M	G05B2219/40145	3	Force sensation of follower converted to audio signal for operator	
M	G05B2219/40146	3	Telepresence, teletaction, sensor feedback from follower to operator	
M	G05B2219/40182	3	Leader has different configuration than follower manipulator	
M	G05B2219/40186	3	Reachability control, permits follower to reach commanded position	
M	G05B2219/40187	3	Indexed position control, leader controls only small part of follower space	
M	G05B2219/40188	3	Position control with scaling, leader small movement, follower large movement	
M	G05B2219/40268	3	Leader attached to tip of macro manipulator, controls follower micromanipulator	
M	G05B2219/40399	3	Selection of leader-follower operation mode	
M	G05B2219/40401	3	Convert workspace of leader to workspace of follower	

M	G05B2219/40402	3	Control button on leader for quick movement, for fine slow movement	
M	G05B2219/40403	3	Leader for walk through, follower uses data for motion control and simulation	
M	G05B2219/40404	3	Separate leader controls macro and microfollower manipulator	
M	G05B2219/40405	3	Leader-follower position control	
M	G05B2219/40406	3	Leader-follower rate control	
M	G05B2219/40407	3	Leader-follower, leader is replica of follower	
M	G05B2219/41407	3	Leader changes resistor, follower restores value in order to follow leader	
M	G05B2219/42184	3	Leader-follower with feedforward for compensation of contour error	
M	G05B2219/42185	3	Leader-follower with contour controller	
M	G05B2219/42186	3	Leader-follower, motion proportional to axis	
M	G05B2219/42188	3	Follower controlled as function of reference and actual position and derived speed of leader	
M	G05B2219/42191	3	Adjust proportionality factor to optimize follower axis movement	
M	G05B2219/42335	3	If one follower axis out of synchronisation, synchronise all other axes to that one	
M	G05B2219/49379	3	Key input path, move one axis manually, other axis follower controlled by program	
M	G05B2219/50219	3	Follower spindle is driven at half the torque of main spindle for synchronism	

SUBCLASS G06F - ELECTRIC DIGITAL DATA PROCESSING

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	G06F9/3877	4	{using a secondary processor, e.g. coprocessor (peripheral processor G06F13/12)}	
M	G06F2009/3883	5	{Two-engine architectures, i.e. stand-alone processor acting as a secondary processor}	
M	G06F11/2242	4	{in multi-processor systems, e.g. one processor becoming the primary tester (G06F11/2736 takes precedence)}	

SUBCLASS G07F - COIN-FREED OR LIKE APPARATUS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	G07F17/3223	3	{Architectural aspects of a gaming system, e.g. internal configuration, leader-follower, wireless communication}	

SUBCLASS G08B - SIGNALLING OR CALLING SYSTEMS; ORDER TELEGRAPHS; ALARM SYSTEMS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	G08B13/19643	7	{wherein the cameras play different roles, e.g. different resolution, different camera type, different control type}	

SUBCLASS H03H - IMPEDANCE NETWORKS, e.g. RESONANT CIRCUITS; RESONATORS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	H03H2210/046	2	Leader-follower	

SUBCLASS H03J - TUNING RESONANT CIRCUITS; SELECTING RESONANT CIRCUITS

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	H03J2200/18	1	Tuning of a leader filter in order to tune its follower filter	

SUBCLASS H04B - TRANSMISSION

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	H04B7/269	7	{Leader-follower synchronisation}	

SUBCLASS H04N - PICTORIAL COMMUNICATION, e.g. TELEVISION

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to#
M	H04N23/662	4	{by using camera arrangements where one camera controls another camera to affect the control of camera image capture, e.g. placing the camera in a desirable condition to capture a desired image}	
M	H04N2201/0031	3	where the still picture apparatus controls another apparatus	
M	H04N2201/0032	3	where the still picture apparatus is controlled by another apparatus	

*N = new entries where reclassification into entries is involved; C = entries with modified file scope where reclassification of documents from the entries is involved; Q = new entries which are firstly populated with documents via administrative transfers from deleted (D) entries. Afterwards, the transferred documents into the Q entry will either stay or be moved to more appropriate entries, as determined by intellectual reclassification; T = existing entries with enlarged file scope, which receive documents from C or D entries, e.g. when a limiting reference is removed from the entry title; M = entries with no change to the file scope (no reclassification); D = deleted entries; F = frozen entries will be deleted once reclassification of documents from the entries is completed; U = entries that are unchanged.

NOTES:

- **No {curly brackets} are used for titles in CPC only subclasses, e.g. C12Y, A23Y; 2000 series symbol titles of groups found at the end of schemes (orthogonal codes); or the Y section titles. The {curly brackets} are used for 2000 series symbol titles found interspersed throughout the main trunk schemes (breakdown codes).

- U groups: it is obligatory to display the required "anchor" symbol (U group), i.e. the entry immediately preceding a new group or an array of new groups to be created (in case new groups are not clearly subgroups of C-type groups). Always include the symbol, indent level and title of the U group in the table above.

- All entry types should be included in the scheme changes table above for better understanding of the overall scheme change picture. Symbol, indent level, and title are required for all types.

- "Transferred to" column <u>must</u> be completed for all C, D, F, and Q type entries. F groups will be deleted once reclassification is completed.

- When multiple symbols are included in the "Transferred to" column, avoid using ranges of symbols in order to be as precise as possible.

- For administrative transfer of documents, the following text should be used: "<administrative transfer to XX>", "<administrative transfer to XX and YY simultaneously>", or "<administrative transfer to XX, YY, ...and ZZ simultaneously>" when administrative transfer of the same documents is to more than one place.

- Administrative transfer to main trunk groups is assumed to be the source allocation type, unless otherwise indicated.

- Administrative transfer to 2000/Y series groups is assumed to be "additional information".

- If needed, instructions for allocation type should be indicated within the angle brackets using the abbreviations "ADD" or "INV": <administrative transfer to XX ADD>, <administrative transfer to XX INV>, or <administrative transfer to XX ADD, YY INV, ... and ZZ ADD simultaneously>.

- In certain situations, the "D" entries of 2000-series or Y-series groups may not require a destination ("Transferred to") symbol, however it is required to specify "<no transfer>" in the "Transferred to" column for such cases.

- For finalization projects, the deleted "F" symbols should have <no transfer> in the "Transferred to" column.

- For more details about the types of scheme change, see CPC Guide.

2. A. DEFINITIONS (new)

E05B 27/0053

References

Informative references

Attention is drawn to the following places, which may be of interest for search:

Master key systems in general	E05B 35/10

E05B 29/0046

References

Informative references

Attention is drawn to the following places, which may be of interest for search:

Master key systems in general	E05B 35/10

2. A. DEFINITIONS (modified)

G06F 9/3877

Definition statement

Replace: The Definition statement text with the revised text below.

- Concurrent instruction execution using secondary processor or coprocessor which controls its own execution, i.e. has a decode unit or sequencer;
- Means and protocol to transfer instructions and data to a secondary processor, and to receive results in return;
- Detection of presence or absence of a secondary processor;
- Reconfigurable coprocessors, i.e. not special purpose.

Relationships with other classification places

Replace: The Relationships text with the revised text below.

Vector processors: G06F, G06F 15/8053.

Cryptographic processors: G06F 21/123.

I/O or DMA processors: G06F 13/12.

Image or graphics processors: G06T 1/20.

Digital data processing: G06F 17/00, G06F.

References

Insert: The following new Limiting references section.

Limiting references

This place does not cover:

Peripheral processor	G06F 13/12

Informative references

Replace: The Informative references table with the revised table below.

Execution units executing under control of a primary decoder	G06F 9/3885
Vector processor	G06F 15/8053

Glossary of terms

Replace: The Glossary of terms table with the revised table below.

host	primary processor to which the coprocessor is a secondary processor

G06F 11/00

Glossary of terms

Replace: The "master-checker setup" row in the Glossary of terms table with the following revised table row.

primary-checker setup	a redundant configuration in which a primary CPU drives the system. The checker CPU is synchronized (often at clock level) with the primary. It processes the input data stream as the primary (and often also the very same program). Whenever the primary drives an output signal, the checker compares its own value with the data written by the primary. A mismatch triggers an error signal. The primary-checker mode is supported in many modern microprocessors by a comparator integrated into the pin driver circuitry, thus reducing the external logic to a few chips for interfacing the error signals. The primary-checker system generally gives more accurate answers by ensuring that the answer is correct before passing it on to the application requesting the algorithm

	being completed. It also allows for error handling if the results are inconsistent. Depending on the merit of a correct answer, a checker-CPU may or may not be warranted. In order to alleviate some of the cost in these situations, the checker-CPU may be used to calculate something else in the same algorithm, increasing the speed and processing output of the CPU system. There are two possible configurations: Primary-Listener and Cross-Coupled. The Primary-Listener lock step configuration pairs two processors, with one as a complete Primary and the other as a complete Listener, the latter having disabled output drivers. In the Cross-Coupled configuration, one of the processors, the SI-Primary, drives the system interface bus, while the other processor, the SC-Primary, drives the secondary cache bus. The SI-Primary has disabled output drivers for the secondary cache interface bus while the SC-Primary has disabled output drivers for the system interface bus.

G06F 11/1637

Definition statement

<u>Replace:</u>   The Definition statement text with the revised text below.

- Systems often denoted as primary/checker in which there is no separate comparator unit. Instead, one or some of the redundant unit(s) (the checker(s)) do(es) additional work to perform the comparison, thereby detecting erroneous behaviour and checking the system for correct operation. The additional compare functionality may be implemented in hardware on the corresponding processing component(s) or in software executed by the corresponding processing component(s).
- Primary-checker-type architectures in which two processors operate in clock lockstep, where the checker compares the values driven by the primary with its own corresponding internally present by otherwise disabled outputs. If a discrepancy is detected, the checker produces an error signal. See the

glossary for citations of example primary-checker structures. Note that this type of architectures also falls in the G06F 11/1654.

- Primary-checker architectures where the checker is limited in processing functionality with respect to the primary, even though this is not absolutely in line with our definition of hardware redundancy.

G06F 11/1654

Definition statement

Replace: The Definition statement text with the revised text below.

- Here one of the processing components is usually called the "primary", the other(s) may be denoted as secondary, checker(s), or shadow(s). The primary's: outputs drive the system. The secondary's: outputs are used for comparison but are otherwise disabled from reaching other components of the system.
- Architectures in which both processors have a primary and a secondary role but for distinct parts of the system, i.e. for different types of output.

Relationships with other classification places

Replace: The Relationships text with the revised text below.

There may be an overlap of this group with G06F 11/1637 when the secondary itself also performs the comparison, thereby making it a checker as well.

2. B. DEFINITIONS QUICK FIX

Symbol	Location of change (e.g., section title)	Existing reference symbol or text	Action; New symbol; New text
F02D 25/00	Glossary	Master and slave engine | Primary and secondary engine	Delete entire row
F15B 7/00	Definition Statement	slave cylinder	Replace with: subordinate cylinder
F15B 7/00	Glossary	slave cylinder	Replace with: subordinate cylinder
F15B 7/06	Special rules	slave cylinders	Replace with: subordinate cylinders
F16D 2048/0212	Informative references	Details of pistons in master or slave cylinders F16D 2025/081, F16D 25/082	Replace with: Details of pistons in primary or secondary cylinders F16D 2025/081, F16D 25/082
F16D 65/28	Definition statement	slave cylinders	Replace with: secondary cylinders
G06F 9/3879	Definition statement	Slave processors	Replace two instances with: Secondary processors
G06F 9/3879	Glossary	master processor	Replace with: primary processor
G06F 9/3881	Definition statement	slave processor	Replace with: secondary processor
G06F 9/3885	Informative references	Concurrent execution using a slave processor G06F 9/3877	Replace with: Concurrent execution using a secondary processor G06F 9/3877
G06F 9/4416	Informative references	Booting of multiprocessor systems, e.g. where one processor (the master) sends the boot or initialisation code to the other processors (slaves)	Replace with: Booting of multiprocessor systems, e.g. where one processor (the primary processor) sends the boot or

		G06F 15/177	initialisation code to the other processors (secondary processors) G06F 15/177
G06F 21/1087	Definition statement	master/slave	Replace with: controller-subordinate
H04J 3/0638	Definition statement	master/slave	Replace with: leader-follower
H04J 3/0679	Synonyms and Keywords	HMS | Hierarchical Master Slave	Delete entire row
H04J 3/0679	Synonyms and Keywords	PAMS | Pre-assigned Master Slave	Delete entire row
H04L 67/12	Special rules	master/slave	Replace with: leader/follower
H04M 1/72412	Definition statement	master	Replace with: leader
H04M 15/7652	Definition statement	master-slave	Replace with: primary-secondary
H04N 21/23	Definition statement	master clock	Replace with: dedicated reference clock
H04N 21/4751	Definition statement	Master users	Replace with: Administrator users

Notes:
Use this Definitions Quick Fix (DQF) table to:
- Delete an entire definition
- Delete an entire section
- Change a reference symbol
- Delete a reference symbol
- Delete text in a References section
- Correct one error in spelling, article use, or verb tense

Otherwise, use the standard template.

Reminder: Never delete F symbol definitions.