CPC COOPERATIVE PATENT CLASSIFICATION

G PHYSICS

(NOTES omitted)

INSTRUMENTS

G06 COMPUTING; CALCULATING OR COUNTING

(NOTES omitted)

G06F ELECTRIC DIGITAL DATA PROCESSING (computer systems based on specific

computational models G06N)

NOTE

In this subclass, the following terms or expressions are used with the meaning indicated:

· "handling" includes processing or transporting of data;

e.g. by using an adder-accumulator}

• "data processing equipment" means an association of an electric digital data processor classifiable under group G06F 7/00, with one or more arrangements classifiable under groups G06F 1/00 - G06F 5/00 and G06F 9/00 - G06F 13/00.

WARNINGS

1. The following IPC groups are not in the CPC scheme. The subject matter for these IPC groups is classified in the following CPC groups:

G06F 3/18 covered by <u>G06F 3/00</u>, <u>G06K 11/00</u>

G06F 7/04 covered by <u>G06F 7/02</u> G06F 9/302 - G06F 9/318 covered by <u>G06F 9/30</u>

2. In this subclass non-limiting references (in the sense of paragraph 39 of the Guide to the IPC) may still be displayed in the scheme.

1/00	Details not covered by groups	1/0335	{the phase increment itself being a
1/00	G06F 3/00 - G06F 13/00 and G06F 21/00	1/0333	composed function of two or more
	(architectures of general purpose stored program		variables, e.g. frequency and phase}
	computers G06F 15/76)	1/0342	{for generating simultaneously two or more
1/02	Digital function generators	-, -, -	related waveforms, e.g. with different phase
1/022	• • {Waveform generators, i.e. devices for generating		angles only}
-,	periodical functions of time, e.g. direct digital	1/035	Reduction of table size {(G06F 1/0314 takes
	synthesizers (<u>G06F 1/025</u> , <u>G06F 1/03</u> take		precedence)}
	precedence)}	1/0353	• • • {by using symmetrical properties of the
1/025	for functions having two-valued amplitude, e.g.		function, e.g. using most significant bits for
	Walsh functions		quadrant control}
1/0255	• • {Walsh or analogous functions}	1/0356	• • • {by using two or more smaller tables, e.g.
1/03	• • working, at least partly, by table look-up		addressed by parts of the argument}
	(G06F 1/025 takes precedence)	1/04	 Generating or distributing clock signals or signals
	NOTE		derived directly therefrom
		1/06	Clock generators producing several clock signals
	In order to be classified in this group, the table must contain function values of the		$\{(\underline{\text{G06F 1/08}} - \underline{\text{G06F 1/14}} \text{ take precedence})\}$
	desired or an intermediate function, not merely	1/08	Clock generators with changeable or
	coefficients.	1/10	programmable clock frequency
	Coefficients.	1/10	• Distribution of clock signals {, e.g. skew}
1/0307	• • • {Logarithmic or exponential functions	1/105	• • • {in which the distribution is at least partially
	(<u>G06F 1/0314</u> , <u>G06F 1/035</u> take precedence)}	1/10	optical}
1/0314	• • • {the table being stored on a peripheral device,	1/12	• Synchronisation of different clock signals {provided by a plurality of clock generators}
	e.g. papertape, drum}	1/14	Time supervision arrangements, e.g. real time
1/0321	{Waveform generators, i.e. devices for	1/14	clock
	generating periodical functions of time, e.g.	1/16	Constructional details or arrangements
	direct digital synthesizers (<u>G06F 1/0314</u> ,	1/10	• Constructional details of affairgements
1/0328	G06F 1/035 take precedence)} {in which the phase increment is adjustable,		
1/114:78			

1/1601	• • {Constructional details related to the housing of computer displays, e.g. of CRT monitors, of flat displays (constructional details related to	1/1628 • • • {Enclosures for carrying portable computers with peripheral devices, e.g. cases for a laptop and a printer}
	flat displays integrated in a portable computer,	WARNING
1/1602	e.g. laptop, handheld computer <u>G06F 1/1637</u> ; constructional details related to television receivers <u>H04N 5/64</u>)}	Group <u>G06F 1/1628</u> is impacted by reclassification into groups <u>A45C 11/003</u> and <u>G06F 1/1629</u> .
1/1603	 . • {Arrangements to protect the display from incident light, e.g. hoods} 	Groups G06F 1/1628, A45C 11/003 and
1/1605	• • • {Multimedia displays, e.g. with integrated or attached speakers, cameras, microphones}	G06F 1/1629 should be considered in order to perform a complete search.
1/1607	 {Arrangements to support accessories mechanically attached to the display housing (G06F 1/1603, G06F 1/1605 take precedence)} {to support filters or lenses} 	1/1629 • • • {Protective covers or auxiliary enclosures for portable computers (for carrying with peripheral (2022))
1/1611	• • • {to support document holders}	<u>A45C 11/003</u>)}
1/1613	• • {for portable computers (cooling arrangements	WARNING
	therefor <u>G06F 1/203</u> ; constructional details or arrangements for pocket calculators, electronic agendas or books <u>G06F 15/0216</u> ; constructional details of portable telephone sets: with several bodies <u>H04M 1/0202</u>)}	Group G06F 1/1629 is incomplete pending reclassification of documents from groups A45C 11/00, A45C 11/001, A45C 11/003, G06F 1/1613, G06F 1/1628, G06F 2200/1633 and H04B 1/3888.
	WARNING	All groups listed in this Warning should be
	Group <u>G06F 1/1613</u> is impacted by reclassification into group <u>G06F 1/1629</u> .	considered in order to perform a complete search.
	Groups G06F 1/1613 and G06F 1/1629 should	1/163 • • • {Wearable computers, e.g. on a belt}
	be considered in order to perform a complete search.	1/1632 {External expansion units, e.g. docking stations}
1/1615	• • • {with several enclosures having relative motions, each enclosure supporting at least one I/O or computing function (constructional	1/1633 {Constructional details or arrangements of portable computers not specific to the type of enclosures covered by groups
	details of portable telephones comprising a plurality of mechanically joined movable body	1/1635 {Details related to the integration of battery packs and other power supplies such as fuel
1/1616	parts <u>H04M 1/0206</u>)} { with folding flat displays, e.g. laptop	cells or integrated AC adapter}
1,1010	computers or notebooks having a clamshell	1/1637 {Details related to the display arrangement,
	configuration, with body parts pivoting to an	including those related to the mounting of the display in the housing}
	open position around an axis parallel to the plane they define in closed position}	1/1639 {the display being based on projection}
1/1618	{the display being foldable up to the back	1/1641 {the display being formed by a plurality
	of the other housing with a single degree of freedom, e.g. by 360° rotation over the	of foldable display components (G06F 1/1647 takes precedence)}
	axis defined by the rear edge of the base enclosure}	1/1643 {the display being associated to a digitizer, e.g. laptops that can be used as penpads
1/162	• • • • {changing, e.g. reversing, the face orientation of the screen with a two degrees of freedom mechanism, e.g. for	(details related to the relative motion of the display enclosure with respect to the body enclosure, e.g. to move between laptop and
	folding into tablet PC like position or	tablet PC configuration G06F 1/1615)}
	orienting towards the direction opposite to the user to show to a second user}	1/1645 {the display being suitable to be used in combination with an external overhead
1/1622	• • • { with enclosures rotating around an axis	projector}
	perpendicular to the plane they define or with ball-joint coupling, e.g. PDA with	1/1647 {including at least an additional display (G06F 1/1692 takes precedence)}
	display enclosure orientation changeable between portrait and landscape by rotation	1/1649 {the additional display being independently orientable, e.g. for presenting information to a second user}
1/1624	with respect to a coplanar body enclosure} { with sliding enclosures, e.g. sliding	1/165 {the additional display being small, e.g.
1,1027	keyboard or display}	for presenting status information}
1/1626	• • • {with a single-body enclosure integrating a flat display, e.g. Personal Digital Assistants	1/1652 {the display being flexible, e.g. mimicking a sheet of paper, or rollable}
	[PDAs]}	1/1654 {the display being detachable, e.g. for
		remote use}

of ag de	Details related to functional adaptations the enclosure, e.g. to provide protection ainst EMI, shock, water, or to host tachable peripherals like a mouse or movable expansions units like PCMCIA	1/1679	• • • • •	{for locking or maintaining the movable parts of the enclosure in a fixed position, e.g. latching mechanism at the edge of the display in a laptop or for the screen protective cover of a PDA (G06F 1/1681
	rds, or to provide access to internal			takes precedence)}
sto me	mponents for maintenance or to removable orage supports like CDs or DVDs, or to echanically mount accessories (mounting	1/1681	• • • • •	{Details related solely to hinges (hinge details related to the transmission of signals or power are classified in
	accessories to a computer display 06F 1/1607; display hoods G06F 1/1603;	1/1/02		<u>G06F 1/1683</u>)}
CO	oling arrangements for portable computers 06F 1/203)}	1/1683	• • • • •	{for the transmission of signal or power between the different housings, e.g. details of wired or wireless communication,
<u>W</u>	ARNING	1/1/04		passage of cabling}
	Group G06F 1/1656 is incomplete pending reclassification of documents from groups A45C 11/003 and	1/1684	a	Constructional details or rrangements related to integrated I/) peripherals not covered by groups 606F 1/1635 - G06F 1/1675}
	<u>G06F 2200/1633</u> .	1/1686		{the I/O peripheral being an integrated
	Groups A45C 11/003, G06F 2200/1633			camera}
	and G06F 1/1656 should be considered in order to perform a complete search.	1/1688	• • • • •	{the I/O peripheral being integrated loudspeakers}
1/1658	{related to the mounting of internal	1/169		{the I/O peripheral being an integrated
	components, e.g. disc drive or any other functional module}			pointing device, e.g. trackball in the palm rest area, mini-joystick integrated between
1/166	{related to integrated arrangements for			keyboard keys, touch pads or touch
	adjusting the position of the main body			stripes (<u>G06F 1/1643</u> takes precedence; constructional details of pointing devices
	with respect to the supporting surface, e.g.			G06F 3/033)}
	legs for adjusting the tilt angle}	1/1692		• {the I/O peripheral being a secondary
	Details related to the integrated keyboard} { Arrangements for ergonomically			touch screen used as control interface,
	adjusting the disposition of keys of the			e.g. virtual buttons or sliders}
	integrated keyboard}	1/1694	• • • • •	(the I/O peripheral being a single or a
	{Arrangements for reducing the size of			set of motion sensors for pointer control or gesture input obtained by sensing
	the integrated keyboard for transport,			movements of the portable computer}
	e.g. foldable keyboards, keyboards with collapsible keys (<u>G06F 1/1664</u> takes	1/1696		{the I/O peripheral being a printing or
	precedence)}			scanning device}
	{Arrangements for adjusting the tilt angle	1/1698		{the I/O peripheral being a sending/
1	of the integrated keyboard independently			receiving arrangement to establish a cordless communication link, e.g. radio
	from the main body (adjusting the tilt			or infrared link, integrated cellular phone
	angle integrally with the main body			(details of antennas disposed inside a
	G06F 1/166)} {Detachable keyboards}			computer <u>H01Q 1/2266</u>)}
	Special purpose buttons or auxiliary	1/18		ging or power distribution
	keyboards, e.g. retractable mini keypads,	1/181		aclosures (for portable computers
	keypads or buttons that remain accessible	1/182		6F 1/1613) with special features, e.g. for use in
	at closed laptop (G06F 1/1666 takes	1/102		ndustrial environments; grounding
	precedence)}			r shielding against radio frequency
	{Arrangements for projecting a virtual keyboard}			nterference [RFI] or electromagnetical
	Aiscellaneous details related to the relative			nterference [EMI]}
mo	ovement between the different enclosures	1/183		ternal mounting support structures, e.g. printed circuit boards, internal connecting
	enclosure parts} {for detecting open or closed state or		mea	ans (for buses <u>G06F 13/409</u>)}
	particular intermediate positions assumed	1/184		Mounting of motherboards}
	by movable parts of the enclosure,	1/185		Mounting of expansion boards}
	e.g. detection of display lid position	1/186		Securing of expansion boards in
	with respect to main body in a laptop,			orrespondence to slots provided at the omputer enclosure}
	detection of opening of the cover of battery compartment}	1/187		Mounting of fixed and removable disk
	outery compartment;		d	rives}
		1/188		Mounting of power supply units}
		1/189		ower distribution}
		1/20	Cooli	ng means

1/202		1/220	1 4 1 1 1 2
1/203	• • • {for portable computers, e.g. for laptops}	1/329	by task scheduling
1/206	• • • {comprising thermal management}	1/3293	• • • • by switching to a less power-consuming
1/22	 Means for limiting or controlling the pin/gate ratio 		processor, e.g. sub-CPU
1/24	Resetting means	1/3296	by lowering the supply or operating
1/26	• Power supply means, e.g. regulation thereof (for		voltage
	memories G11C)	2100	
1/263	• • {Arrangements for using multiple switchable	3/00	Input arrangements for transferring data to be
1,203	power supplies, e.g. battery and AC (G06F 1/30		processed into a form capable of being handled
	takes precedence)}		by the computer; Output arrangements for
1/2//			transferring data from processing unit to output
1/266	• • {Arrangements to supply power to external		unit, e.g. interface arrangements
	peripherals either directly from the computer or	3/002	 {Specific input/output arrangements not covered
	under computer control, e.g. supply of power		by <u>G06F 3/01</u> - <u>G06F 3/16</u> (other optical apparatus
	through the communication port, computer		<u>G02B 27/00</u>)}
	controlled power-strips}	3/005	• • {Input arrangements through a video camera}
1/28	• • Supervision thereof, e.g. detecting power-supply	3/007	• {Digital input from or digital output to memories of
	failure by out of limits supervision	2, 3 3 1	the shift register type}
1/30	 Means for acting in the event of power-supply 	3/01	Input arrangements or combined input and output
	failure or interruption, e.g. power-supply	3/01	arrangements for interaction between user and
	fluctuations (for resetting only G06F 1/24)		computer (G06F 3/16 takes precedence)
1/305	• • • {in the event of power-supply fluctuations}	3/011	• • {Arrangements for interaction with the human
1/32	Means for saving power	3/011	
1/3203	Power management, i.e. event-based initiation		body, e.g. for user immersion in virtual reality
1,3203	of a power-saving mode	2/042	(blind teaching <u>G09B 21/00</u>)}
1/3206	Monitoring of events, devices or parameters	3/012	• • • {Head tracking input arrangements}
1/3200	that trigger a change in power modality	3/013	• • • {Eye tracking input arrangements (<u>G06F 3/015</u>
1/2200	Monitoring remote activity, e.g. over		takes precedence)}
1/3209		3/014	• • • {Hand-worn input/output arrangements, e.g.
1/2212	telephone lines or network connections		data gloves}
1/3212	Monitoring battery levels, e.g. power	3/015	{Input arrangements based on nervous system
	saving mode being initiated when battery		activity detection, e.g. brain waves [EEG]
	voltage goes below a certain level		detection, electromyograms [EMG] detection,
1/3215	Monitoring of peripheral devices		electrodermal response detection}
1/3218	of display devices	3/016	• • {Input arrangements with force or tactile
1/3221	• • • • of disk drive devices		feedback as computer generated output to the
1/3225	• • • • of memory devices		user}
1/3228	Monitoring task completion, e.g. by use	3/017	• • {Gesture based interaction, e.g. based on a set of
	of idle timers, stop commands or wait		recognized hand gestures (interaction based on
	commands		gestures traced on a digitiser G06F 3/04883)}
1/3231	Monitoring the presence, absence or	3/018	• • {Input/output arrangements for oriental
	movement of users		characters}
1/3234	Power saving characterised by the action	3/02	Input arrangements using manually operated
	undertaken		switches, e.g. using keyboards or dials
1/3237	by disabling clock generation or	3/0202	• • • {Constructional details or processes of
	distribution		manufacture of the input device}
1/324	by lowering clock frequency	3/0205	{Lever arrangements for operating keyboard
1/3243	• • • • • {Power saving in microcontroller unit}		cursor control keys in a joystick-like
1/3246	by software initiated power-off		manner}
1/325	{Power saving in peripheral device}	3/0208	• • • • {Arrangements for adjusting the tilt angle
	{Power saving in peripheral device} {Power saving in bus}	5/0200	of a keyboard, e.g. pivoting legs (for
1/3253			keyboards integrated in a laptop computer
1/3256	• • • • • {Power saving in optical drive}		G06F 1/1667)}
1/3259	• • • • • {Power saving in cursor control device,	3/021	
	e.g. mouse, joystick, trackball}	3/021	peripherals in a keyboard, e.g. card or
1/3262	• • • • • {Power saving in digitizer or tablet}		barcode reader, optical scanner}
1/3265	• • • • • {Power saving in display device}	2/0212	
1/3268	• • • • • {Power saving in hard disk drive}	3/0213	{Arrangements providing an integrated
1/3271	{Power saving in keyboard}		pointing device in a keyboard, e.g.
1/3275	• • • • • {Power saving in memory, e.g. RAM,		trackball, mini-joystick (for pointing
0210	cache}		devices integrated in a laptop computer
1/3278	• • • • • {Power saving in modem or I/O		G06F 1/169; joysticks G05G 9/047;
1,5210	interface}		constructional details of pointing devices
1/3281	{Power saving in PCMCIA card}	A 10 = + +	G06F 3/033)}
	{Power saving in PCMCIA card} {Power saving in printer}	3/0216	{Arrangements for ergonomically adjusting
1/3284	• • • • • FOWER SAVING IN DEHILLER		the disposition of keys of a keyboard (for
1/2207			
1/3287	by switching off individual functional units in the computer system		keyboards integrated in a laptop computer G06F 1/1664)}

3/0219 3/0221 3/0224	 {Special purpose keyboards} {Arrangements for reducing keyboard size for transport or storage, e.g. foldable keyboards, keyboards with collapsible keys (G06F 3/0216 takes precedence; for keyboards integrated in a laptop computer G06F 1/1666)} {Key guide holders} 	 3/0317 {in co-operation with a patterned surface, e.g. absolute position or relative movement detection for an optical mouse or pen positioned with respect to a coded surface} 3/0321 {by optically sensing the absolute position with respect to a regularly patterned surface forming a passive digitiser, e.g. pen optically detecting
3/0227	 • {Rey gattle holders} • {Cooperation and interconnection of the input arrangement with other functional units of a computer (G06F 3/023 - G06F 3/037 take precedence)} 	position indicative tags printed on a paper sheet (constructional details of penshaped pointing devices <u>G06F 3/03545</u> , <u>G06F 3/03542</u> , <u>G06F 3/037</u>)}
3/023	Arrangements for converting discrete items of information into a coded form, e.g. arrangements for interpreting keyboard generated codes as alphanumeric codes, operand codes or instruction codes	3/0325 {using a plurality of light emitters or reflectors or a plurality of detectors forming a reference frame from which to derive the orientation of the object, e.g. by triangulation or on the basis of reference deformation in the picked up image}
3/0231	{Cordless keyboards}	3/033 • • • Pointing devices displaced or positioned by the
3/0232	{Manual direct entries, e.g. key to main memory}	user {, e.g. mice, trackballs, pens or joysticks};
3/0233	{Character input methods}	Accessories therefor (digitisers characterised
3/0234	• • • {using switches operable in different	by the transducing means <u>G06F 3/041</u>)
.,	directions}	3/0334 {Foot operated pointing devices}
3/0235	• • • • {using chord techniques (<u>G06F 3/0234</u> takes precedence)}	3/0338 with detection of limited linear or angular displacement of an operating part of the device from a neutral position, e.g. isotonic
3/0236	{using selection techniques to select from	or isometric joysticks
3/0237	displayed items} {using prediction or retrieval techniques}	3/0346 with detection of the device orientation or
3/0237	{Programmable keyboards (key guide	free movement in a 3D space, e.g. 3D mice,
3/0230	holders <u>G06F 3/0224</u>)}	6-DOF [six degrees of freedom] pointers
3/027	for insertion of the decimal point	using gyroscopes, accelerometers or tilt- sensors
3/03	Arrangements for converting the position or the	3/0354 with detection of 2D relative movements
	displacement of a member into a coded form	between the device, or an operating part
	NOTE	thereof, and a plane or surface, e.g. 2D mice,
		thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks
	In this group, the first place priority rule is	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in
		thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement}
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary,	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving
3/0304	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement}
3/0304	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g.
3/0304	 In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; 	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)}	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device}
3/0304	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} {comprising a plurality of distinctive and	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus}
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member}
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface}
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen}	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move
	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • • {for tracking the rotation of a spherical}	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • • {for tracking the rotation of a spherical or circular member, e.g. optical rotary	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)}
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • • {for tracking the rotation of a spherical or circular member, e.g. optical rotary encoders used in mice or trackballs using	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • • {for tracking the rotation of a spherical or circular member, e.g. optical rotary	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations of an operating part of the device, e.g. scroll
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • **Obtection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • **Comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • ***Off tracking the rotation of a spherical or circular member, e.g. optical rotary encoders used in mice or trackballs using a tracking ball or in mouse scroll wheels (tracking relative movement in co-operation with a regularly or irregularly patterned	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations of an operating part of the device, e.g. scroll wheels, sliders, knobs, rollers or belts
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • • {for tracking the rotation of a spherical or circular member, e.g. optical rotary encoders used in mice or trackballs using a tracking ball or in mouse scroll wheels (tracking relative movement in co-operation with a regularly or irregularly patterned surface, e.g. as in optical mice G06F 3/0317;	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations of an operating part of the device, e.g. scroll
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • • {for tracking the rotation of a spherical or circular member, e.g. optical rotary encoders used in mice or trackballs using a tracking ball or in mouse scroll wheels (tracking relative movement in co-operation with a regularly or irregularly patterned surface, e.g. as in optical mice G06F 3/0317; constructional details of scroll or thumb-	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations of an operating part of the device, e.g. scroll wheels, sliders, knobs, rollers or belts 3/037 using the raster scan of a cathode-ray tube
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • **Obtection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • **Comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • **Off tracking the rotation of a spherical or circular member, e.g. optical rotary encoders used in mice or trackballs using a tracking ball or in mouse scroll wheels (tracking relative movement in co-operation with a regularly or irregularly patterned surface, e.g. as in optical mice G06F 3/0317; constructional details of scroll or thumbwheels G06F 3/0362; optical rotary encoders	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations of an operating part of the device, e.g. scroll wheels, sliders, knobs, rollers or belts 3/037 using the raster scan of a cathode-ray tube [CRT] for detecting the position of the member, e.g. light pens cooperating with CRT monitors
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • • {Detection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • • {comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • • {for tracking the rotation of a spherical or circular member, e.g. optical rotary encoders used in mice or trackballs using a tracking ball or in mouse scroll wheels (tracking relative movement in co-operation with a regularly or irregularly patterned surface, e.g. as in optical mice G06F 3/0317; constructional details of scroll or thumb-	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations of an operating part of the device, e.g. scroll wheels, sliders, knobs, rollers or belts 3/037 using the raster scan of a cathode-ray tube [CRT] for detecting the position of the member, e.g. light pens cooperating with CRT monitors 3/038 Control and interface arrangements therefor,
3/0308	In this group, the first place priority rule is applied, i.e. at each hierarchical level, in the absence of an indication to the contrary, classification is made in the first appropriate place. • • **Obtection arrangements using opto-electronic means (constructional details of pointing devices not related to the detection arrangement using opto-electronic means G06F 3/033; optical digitisers G06F 3/042)} • • **Comprising a plurality of distinctive and separately oriented light emitters or reflectors associated to the pointing device, e.g. remote cursor controller with distinct and separately oriented LEDs at the tip whose radiations are captured by a photo-detector associated to the screen} • • **Off tracking the rotation of a spherical or circular member, e.g. optical rotary encoders used in mice or trackballs using a tracking ball or in mouse scroll wheels (tracking relative movement in co-operation with a regularly or irregularly patterned surface, e.g. as in optical mice G06F 3/0317; constructional details of scroll or thumbwheels G06F 3/0362; optical rotary encoders	thereof, and a plane or surface, e.g. 2D mice, trackballs, pens or pucks 3/03541 {Mouse/trackball convertible devices, in which the same ball is used to track the 2D relative movement} 3/03542 {Light pens for emitting or receiving light} 3/03543 {Mice or pucks (G06F 3/03541 takes precedence)} 3/03544 {having dual sensing arrangement, e.g. two balls or two coils used to track rotation of the pointing device} 3/03545 {Pens or stylus} 3/03546 {using a rotatable ball at the tip as position detecting member} 3/03547 {Touch pads, in which fingers can move on a surface} 3/03548 {Sliders, in which the moving part moves in a plane} 3/03549 {Trackballs (G06F 3/03541 takes precedence)} 3/0362 with detection of 1D translations or rotations of an operating part of the device, e.g. scroll wheels, sliders, knobs, rollers or belts 3/037 using the raster scan of a cathode-ray tube [CRT] for detecting the position of the member, e.g. light pens cooperating with CRT monitors

3/0383	• • • • { Signal control means within the pointing	3/0425 {using a single imaging device like a video
	device}	camera for tracking the absolute position
3/0386	• • • • {for light pen}	of a single or a plurality of objects with
3/039	Accessories therefor, e.g. mouse pads	respect to an imaged reference surface,
3/0393	{Accessories for touch pads or touch	e.g. video camera imaging a display or
	screens, e.g. mechanical guides added to	a projection screen, a table or a wall
	touch screens for drawing straight lines,	surface, on which a computer generated
	hard keys overlaying touch screens or	image is displayed or projected (tracking a
	touch pads}	projected light spot to determine a position
3/0395	{Mouse pads}	on a display surface G06F 3/0386)
3/03/3		3/0426 {tracking fingers with respect to a
3/041	Digitisers, e.g. for touch screens or touch pads,	virtual keyboard projected or printed on
0/0/40	characterised by the transducing means	the surface (virtual keyboards on touch
3/0412	{Digitisers structurally integrated in a	screens G06F 3/04886)}
	display }	3/0428 {by sensing at the edges of the touch
3/0414	• • • { using force sensing means to determine a	surface the interruption of optical paths,
	position}	e.g. an illumination plane, parallel to
3/04142	• • • • {the force sensing means being located	the touch surface which may be virtual
	peripherally, e.g. disposed at the corners or	(sensing beam interruptions in a planar
	at the side of a touch sensing plate}	beam grid of an optical touch-screen
3/04144	• • • • {using an array of force sensing means	
	(position sensing using the local	<u>G06F 3/0421</u>)}
	deformation of sensor cells G06F 3/0447)}	3/043 using propagating acoustic waves
3/04146	• • • • {using pressure sensitive conductive	3/0433 • • • • {in which the acoustic waves are either
3/04140	elements delivering a boolean signal and	generated by a movable member and
	located between crossing sensing lines,	propagated within a surface layer or
	e.g. located between X and Y sensing line	propagated within a surface layer and
		captured by a movable member}
2/0416	layers}	3/0436 {in which generating transducers and
3/0416	{Control or interface arrangements specially	detecting transducers are attached to
	adapted for digitisers}	a single acoustic waves transmission
3/04162	• • • • { for exchanging data with external	substrate}
	devices, e.g. smart pens, via the digitiser	3/044 by capacitive means
	sensing hardware}	3/0441 {using active external devices, e.g. active
3/04164	• • • • {Connections between sensors and	pens, for receiving changes in electrical
	controllers, e.g. routing lines between	potential transmitted by the digitiser, e.g.
	electrodes and connection pads}	tablet driving signals}
3/04166	• • • • {Details of scanning methods, e.g.	3/0442 {using active external devices, e.g. active
	sampling time, grouping of sub areas	pens, for transmitting changes in electrical
	or time sharing with display driving	potential to be received by the digitiser}
	(Synchronisation with the driving of	3/0443 {using a single layer of sensing
	the display or the backlighting unit to	electrodes}
	avoid interferences generated internally	
	<u>G06F 3/04184</u>)}	3/0444 { using a single conductive element
3/041661	• • • • • { using detection at multiple resolutions,	covering the whole sensing surface, e.g. by
	e.g. coarse and fine scanning; using	sensing the electrical current flowing at the
	detection within a limited area, e.g.	corners}
	object tracking window}	3/0445 {using two or more layers of sensing
3/041662	{using alternate mutual and self-	electrodes, e.g. using two layers of
	capacitive scanning}	electrodes separated by a dielectric layer}
3/0418	• • • • • { for error correction or compensation,	3/0446 { using a grid-like structure of electrodes in
3/0410	e.g. based on parallax, calibration or	at least two directions, e.g. using row and
	alignment}	column electrodes}
2/04192		3/0447 {Position sensing using the local
3/04182	• • • • • {Filtering of noise external to the	deformation of sensor cells}
	device and not generated by digitiser	3/0448 {Details of the electrode shape, e.g. for
0/0/10/1	components}	enhancing the detection of touches, for
3/04184	• • • • • {Synchronisation with the driving of the	generating specific electric field shapes,
	display or the backlighting unit to avoid	for enhancing display quality}
	interferences generated internally}	3/045 using resistive elements, e.g. a single
3/04186	• • • • {Touch location disambiguation}	continuous surface or two parallel surfaces
3/042	by opto-electronic means	
3/0421	• • • • {by interrupting or reflecting a light beam,	put in contact
	e.g. optical touch-screen}	3/046 by electromagnetic means
3/0423	• • • • • {using sweeping light beams, e.g. using	3/047 using sets of wires, e.g. crossed wires
U . - U	rotating or vibrating mirror}	
	Towning or Totaling limitor;	

3/048 . Interaction techniques based on graphical user interfaces [GUI]

NOTE

This group <u>covers</u> subject matter where the focus is placed on the way the user can interact with the displayed data. The mere presence of a standard GUI in the context of the disclosure of a specific software application or a specific device capable of processing data related to its specific function, should be in general classified in the appropriate subclasses related to those software applications or specific devices.

3/0481 . . . based on specific properties of the displayed interaction object or a metaphor-based environment, e.g. interaction with desktop elements like windows or icons, or assisted by a cursor's changing behaviour or appearance

3/04812 . . . Interaction techniques based on cursor appearance or behaviour, e.g. being affected by the presence of displayed objects

3/04815 . . . Interaction with a metaphor-based environment or interaction object displayed as three-dimensional, e.g. changing the user viewpoint with respect to the environment or object

3/04817 . . . using icons (graphical or visual programming using iconic symbols G06F 8/34)

3/0482 . . . Interaction with lists of selectable items, e.g.

3/0483 . . . Interaction with page-structured environments, e.g. book metaphor

3/0484 . . . for the control of specific functions or operations, e.g. selecting or manipulating an object, an image or a displayed text element, setting a parameter value or selecting a range

3/04842 Selection of displayed objects or displayed text elements (<u>G06F 3/0482</u> takes precedence)

3/04845 . . . for image manipulation, e.g. dragging, rotation, expansion or change of colour

3/04847 . . . Interaction techniques to control parameter settings, e.g. interaction with sliders or dials

3/0485 . . . Scrolling or panning

3/04855 Interaction with scrollbars

3/0486 . . . Drag-and-drop

3/0487 . . . using specific features provided by the input device, e.g. functions controlled by the rotation of a mouse with dual sensing arrangements, or of the nature of the input device, e.g. tap gestures based on pressure sensed by a digitiser

3/0488 using a touch-screen or digitiser, e.g. input of commands through traced gestures

3/04883 for inputting data by handwriting, e.g. gesture or text

3/04886 by partitioning the display area of the touch-screen or the surface of the digitising tablet into independently controllable areas, e.g. virtual keyboards or menus

3/0489 using dedicated keyboard keys or combinations thereof

3/04892 Arrangements for controlling cursor position based on codes indicative of cursor displacements from one discrete location to another, e.g. using cursor control keys associated to different directions or using the tab key (arrangements for controlling cursor position based on coordinate signals G06F 3/038)

3/04895 Guidance during keyboard input operation, e.g. prompting

3/04897 {Special input arrangements or commands for improving display capability}

 Jogital input using the sampling of an analogue quantity at regular intervals of time {, input from a/d converter or output to d/a converter}

 Digital input from, or digital output to, record carriers {, e.g. RAID, emulated record carriers or networked record carriers}

WARNING

Groups G06F 3/06, G06F 3/0601, G06F 3/0602, G06F 3/0604, G06F 3/0605, G06F 3/0607, G06F 3/0608, G06F 3/061, G06F 3/0611, G06F 3/0613, G06F 3/0614, G06F 3/0616, G06F 3/0617, G06F 3/0619, G06F 3/062, G06F 3/0622, G06F 3/0623, G06F 3/0625, G06F 3/0626, G06F 3/0628, G06F 3/0629, G06F 3/0631, G06F 3/0632, G06F 3/0634, G06F 3/0635, G06F 3/0637, G06F 3/0638, G06F 3/064, G06F 3/0641, G06F 3/0643, G06F 3/0644, G06F 3/0646, G06F 3/0647, G06F 3/0649, G06F 3/065, G06F 3/0652, G06F 3/0653, G06F 3/0655, G06F 3/0656, G06F 3/0658, G06F 3/0659, G06F 3/0661, G06F 3/0662, G06F 3/0664, G06F 3/0665, G06F 3/0667, G06F 3/0668, G06F 3/067, G06F 3/0671, G06F 3/0673, G06F 3/0674, G06F 3/0676, G06F 3/0677, G06F 3/0679, G06F 3/068, G06F 3/0682, G06F 3/0683, G06F 3/0685, G06F 3/0686, G06F 3/0688, G06F 3/0689 and G06F 3/08 are incomplete pending reclassification of documents from group G06F 2003/0697.

All groups listed in this Warning should be considered in order to perform a complete search.

3/0601 . . {Interfaces specially adapted for storage systems}

NOTE

{In this subgroup the following classification rules must be observed:

For a complete classification in the field of G06F 3/0601 documents should receive classification symbols for "invention information" as follows:

- at least one symbol in <u>G06F 3/0602</u> <u>G06F 3/0626</u> for the technical effect achieved and
- at least one symbol in <u>G06F 3/0628</u> - <u>G06F 3/0667</u> for the technique used and
- at least one symbol in G06F 3/0668 - G06F 3/0689 for the infrastructure involved.

G06F 3/0601		
(continued)	The classification of	3/0655 {Vertical data movement, i.e. input-output
	"additional information" is	transfer; data movement between one or
	optional. CPC symbols in the range G06F 2206/1004 - G06F 2206/101	more hosts and one or more storage devices}
	should be used for classifying	3/0656 {Data buffering arrangements}
	"additional information".}	3/0658 {Controller construction arrangements} 3/0659 {Command handling arrangements, e.g.
		3/0659 {Command handling arrangements, e.g. command buffers, queues, command
3/0602	• • { specifically adapted to achieve a particular	scheduling}
	effect}	3/0661 {Format or protocol conversion
3/0604	• • • {Improving or facilitating administration,	arrangements}
	e.g. storage management}	3/0662 {Virtualisation aspects}
3/0605	• • • • {by facilitating the interaction with a user	3/0664 {at device level, e.g. emulation of a
2 10 40 =	or administrator}	storage device or system}
3/0607	• • • • {by facilitating the process of upgrading	3/0665 {at area level, e.g. provisioning of virtual
	existing storage systems, e.g. for improving compatibility between host and	or logical volumes}
	storage device}	3/0667 {at data level, e.g. file, record or object
3/0608	• • • • {Saving storage space on storage systems}	virtualisation}
3/061	{Improving I/O performance}	3/0668 {adopting a particular infrastructure}
3/0611	• • • • (Improving to performance) • • • • {in relation to response time}	3/067 {Distributed or networked storage systems,
3/0613	• • • • {in relation to response time} • • • • {in relation to throughput}	e.g. storage area networks [SAN], network
3/0614	{Improving the reliability of storage	attached storage [NAS]}
3/0011	systems}	3/0671 {In-line storage system}
3/0616	• • • • {in relation to life time, e.g. increasing	3/0673 {Single storage device}
	Mean Time Between Failures [MTBF]}	3/0674 {Disk device}
3/0617	• • • • {in relation to availability}	3/0676 {Magnetic disk device}
3/0619	• • • • {in relation to data integrity, e.g. data	3/0677 {Optical disk device, e.g. CD-ROM,
	losses, bit errors}	DVD}
3/062	• • • {Securing storage systems}	3/0679 {Non-volatile semiconductor memory
3/0622	• • • • {in relation to access}	device, e.g. flash memory, one time programmable memory [OTP]}
3/0623	• • • • {in relation to content}	3/068 {Hybrid storage device}
3/0625	• • • {Power saving in storage systems}	3/0682 {Tape device}
3/0626	• • • {Reducing size or complexity of storage	3/0683 {Plurality of storage devices}
	systems}	3/0685 {Hybrid storage combining
3/0628	• • • {making use of a particular technique}	heterogeneous device types, e.g.
3/0629	{Configuration or reconfiguration of storage	hierarchical storage, hybrid arrays}
2/0/21	systems}	3/0686 {Libraries, e.g. tape libraries, jukebox}
3/0631	 {by allocating resources to storage systems}	3/0688 {Non-volatile semiconductor memory
3/0632	•	arrays}
3/0032	• • • • {by initialisation or re-initialisation of storage systems}	3/0689 {Disk arrays, e.g. RAID, JBOD}
3/0634	• • • • {by changing the state or mode of one or	
3/0031	more devices}	
3/0635	• • • • {by changing the path, e.g. traffic	
	rerouting, path reconfiguration}	
3/0637	{Permissions}	
3/0638	• • • {Organizing or formatting or addressing of	
	data}	
3/064	• • • • {Management of blocks}	
3/0641	• • • • • {De-duplication techniques}	
3/0643	• • • • {Management of files}	
3/0644	• • • • {Management of space entities, e.g.	
	partitions, extents, pools}	
3/0646	{Horizontal data movement in storage	
	systems, i.e. moving data in between storage	
3/0647	devices or systems} {Migration mechanisms}	
3/0647	{Migration mechanisms} {Lifecycle management}	
3/0649	{Effective management} {Replication mechanisms}	
3/0652	• • • • {Replication mechanisms} • • • • {Erasing, e.g. deleting, data cleaning,	
5/0032	moving of data to a wastebasket	
3/0653	• • • {Monitoring storage devices or systems}	
	(

2003/0697 (Frozen)	• • {device management, e.g. handlers, drivers, I/O schedulers}	3/1212	• • • • {achieving reduced delay between job submission and print start}
	WARNING	3/1213	• • • • • {at an intermediate node or at the final node}
	Group G06F 2003/0697 is no longer used	3/1214	{at the submitting node}
	for the classification of documents as of	3/1215	• • • • • {achieving increased printing speed, i.e.
	May 1, 2021.	3,1213	reducing the time between printing start
	The content of this group is being reclassified		and printing end}
	into groups <u>G06F 3/06</u> , <u>G06F 3/0601</u> ,	3/1217	• • • • {achieving reduced idle time at the output
	G06F 3/0602, G06F 3/0604, G06F 3/0605,	0.722.	device or increased asset utilization}
	G06F 3/0607, G06F 3/0608, G06F 3/061,	3/1218	{Reducing or saving of used resources, e.g.
	G06F 3/0611, G06F 3/0613, G06F 3/0614,		avoiding waste of consumables or improving
	G06F 3/0616, G06F 3/0617, G06F 3/0619,		usage of hardware resources}
	G06F 3/062, G06F 3/0622, G06F 3/0623,	3/1219	• • • • { with regard to consumables, e.g. ink,
	G06F 3/0625, G06F 3/0626, G06F 3/0628,		toner, paper}
	G06F 3/0629, G06F 3/0631, G06F 3/0632, G06F 3/0634, G06F 3/0635, G06F 3/0637,	3/122	• • • • { with regard to computing resources, e.g.
	G06F 3/0638, G06F 3/064, G06F 3/0641,		memory, CPU}
	G06F 3/0643, G06F 3/0644, G06F 3/0646,	3/1221	• • • • { with regard to power consumption }
	G06F 3/0647, G06F 3/0649, G06F 3/065,	3/1222	• • • {Increasing security of the print job}
	G06F 3/0652, G06F 3/0653, G06F 3/0655,	3/1223	• • • {specifically adapted to use a particular
	G06F 3/0656, G06F 3/0658, G06F 3/0659,		technique}
	G06F 3/0661, G06F 3/0662, G06F 3/0664,	3/1224	{Client or server resources management}
	G06F 3/0665, G06F 3/0667, G06F 3/0668,	3/1225	• • • • {Software update, e.g. print driver,
	G06F 3/067, G06F 3/0671, G06F 3/0673,		modules, plug-ins, fonts}
	G06F 3/0674, G06F 3/0676, G06F 3/0677,	3/1226	• • • • {Discovery of devices having required
	G06F 3/0679, G06F 3/068, G06F 3/0682,	2/1227	properties}
	G06F 3/0683, G06F 3/0685, G06F 3/0686, G06F 3/0688, G06F 3/0689 and G06F 3/08.	3/1227	{Printer definition files}
		3/1228	• • • • {Printing driverless or using generic
	All groups listed in this Warning should be considered in order to perform a complete	2/1220	drivers}
	search.	3/1229	{Printer resources management or printer maintenance, e.g. device status, power
	Scarcii.		levels}
3/08	• • from or to individual record carriers, e.g. punched card {, memory card, integrated circuit [IC] card	3/123	• • • • {Software or firmware update, e.g. device firmware management}
	or smart card}	3/1231	{Device related settings, e.g. IP address,
3/09	 Digital output to typewriters 	3/1231	Name, Identification}
3/12	• Digital output to print unit {, e.g. line printer, chain	3/1232	• • • • {Transmitting printer device capabilities,
	printer}		e.g. upon request or periodically}
3/1201	• • {Dedicated interfaces to print systems}	3/1234	{Errors handling and recovery,
3/1202	• • • {specifically adapted to achieve a particular		e.g. reprinting (G06F 3/1261 takes
	effect}		precedence)}
3/1203	{Improving or facilitating administration,	3/1235	• • • • {caused by end of consumables, e.g.
2/1204	e.g. print management}		paper, ink, toner}
3/1204	{resulting in reduced user or operator actions, e.g. presetting, automatic actions,	3/1236	(Connection management)
	using hardware token storing data}	3/1237	• • • {Print job management}
3/1205	• • • • • {resulting in increased flexibility in print	3/1238	• • • • {Secure printing, e.g. user identification,
3/1203	job configuration, e.g. job settings, print		user rights for device usage, unallowed
	requirements, job tickets}		content, blanking portions or fields of a
3/1206	• • • • {resulting in increased flexibility in input	2/1222	page, releasing held jobs}
	data format or job format or job type}	3/1239	{Restricting the usage of resources,
3/1207	• • • • {resulting in the user being informed about		e.g. usage or user levels, credit limit, consumables, special fonts}
	print result after a job submission}	3/124	(5, 11, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1
3/1208	{resulting in improved quality of the	3/124	{Parallel printing or parallel ripping} {Dividing a job according to job
	output result, e.g. print layout, colours,	3/1241	requirements, e.g. black/white and colour
	workflows, print preview}		pages, covers and body of books, tabs}
3/1209	{resulting in adapted or bridged legacy	3/1242	{Image or content composition onto a
	communication protocols, e.g. emulation,	5,1272	page}
	protocol extension}	3/1243	• • • • {Variable data printing, e.g. document
3/121	• • • {Facilitating exception or error detection and	0,1210	forms, templates, labels, coupons,
	recovery, e.g. fault, media or consumables		advertisements, logos, watermarks,
3/1211	<pre>depleted} {Improving printing performance}</pre>		transactional printing, fixed content

3/1244	• • • • { Job translation or job parsing, e.g. page banding }	3/1278 • • • {specifically adapted to adopt a particular infrastructure}
3/1245	• • • • • {by conversion to intermediate or common format}	3/1279 {Controller construction, e.g. aspects of the interface hardware}
3/1246	{by handling markup languages, e.g. XSL, XML, HTML}	3/128 {Direct printing, e.g. sending document file, using memory stick, printing from a camera}
3/1247	• • • • • {by conversion to printer ready format}	3/1281 {Multi engine printer devices, e.g. one entity
3/1248	• • • • • {by printer language recognition, e.g.	having multiple output engines}
2/125	PDL, PCL, PDF}	3/1282 {High volume printer device} 3/1284 {Local printer device}
3/125	• • • • {Page layout or assigning input pages onto output media, e.g. imposition}	3/1284 {Local printer device} 3/1285 {Remote printer device, e.g. being remote
3/1251	• • • • • { for continuous media, e.g. web media,	from client or server}
	rolls}	3/1286 {via local network}
3/1252	{for sheet based media}	3/1287 {via internet}
3/1253	• • • • {Configuration of print job parameters, e.g. using UI at the client}	3/1288 {in client-server-printer device configuration}
3/1254	• • • • {Automatic configuration, e.g. by driver}	3/1289 {in server-client-printer device configuration, e.g. the server does not see
3/1255	• • • • • {Settings incompatibility, e.g.	the printer}
	constraints, user requirements vs. device capabilities}	3/129 {in server-printer device-client configuration, e.g. print flow goes from
3/1256	• • • • • {User feedback, e.g. print preview, test print, proofing, pre-flight checks}	server to printer and then bidirectional from printer to client, i.e. the client does
3/1257	{by using pre-stored settings, e.g. job	not communicate with the server}
	templates, presets, print styles}	3/1291 {Pool of printer devices: self-managing
3/1258	• • • • {by updating job settings at the printer}	printing devices in a network, e.g. without a
3/1259	• • • • {Print job monitoring, e.g. job status}	server} 3/1292 {Mobile client, e.g. wireless printing}
3/126	{Job scheduling, e.g. queuing, determine appropriate device}	3/1292 {Mobile client, e.g. wireless printing} 3/1293 {Printer information exchange with computer}
3/1261	{by using alternate printing}	3/1294 {Status or feedback related to information
3/1262	{by grouping or ganging jobs}	exchange}
3/1263	{based on job priority, e.g. re-arranging	3/1295 {Buffering means}
	the order of jobs, e.g. the printing sequence}	3/1296 • • {Printer job scheduling or printer resource handling}
3/1264	• • • • • {by assigning post-processing resources}	3/1297 • • {Printer code translation, conversion, emulation, compression; Configuration of printer
3/1265	• • • • Printing by reference, e.g. retrieving	parameters } 3/1298 • • • {Printer language recognition, e.g. programme
	document/image data for a job from a source mentioned in the job}	control language, page description language}
3/1267	 {Job repository, e.g. non-scheduled jobs, delay printing} 	 3/13 Digital output to plotter {; Cooperation and interconnection of the plotter with other functional
3/1268	• • • • {Job submission, e.g. submitting print job	units}
	order or request not the print data itself}	3/14 • Digital output to display device {; Cooperation and
3/1269	• • • • • {by broadcasting server}	interconnection of the display device with other functional units}
3/127	• • • • • {by using hot folders, e.g. folder for which print settings or print data	3/1407 • General aspects irrespective of display type, e.g.
	management rules are set in advance}	determination of decimal point position, display
3/1271	{Job submission at the printing node,	with fixed or driving decimal point, suppression
	e.g. creating a job from a data stored	of non-significant zeros}
	locally or remotely (<u>G06F 3/1238</u> takes precedence)}	 3/1415 • { with means for detecting differences between the image stored in the host and the images displayed
3/1272	{Digital storefront, e.g. e-ordering,	on the displays}
	web2print, submitting a job from a remote submission screen}	3/1423 • • {controlling a plurality of local displays, e.g. CRT and flat panel display}
3/1273	Print job history, e.g. logging,	3/1431 {using a single graphics controller}
	accounting, tracking}	3/1438 • • • {using more than one graphics controller}
3/1274	• • • • {Deleting of print job}	3/1446 {display composed of modules, e.g. video
3/1275	• • • {Print workflow management, e.g. defining	walls}
2/1076	or changing a workflow, cross publishing}	3/1454 • • {involving copying of the display data of a local workstation or window to a remote workstation
3/1276	• • • • { within a printer driver, e.g. driver resides either on a server or on a client }	or window so that an actual copy of the data
3/1277	• • • • { using filter pipeline, e.g. outside the	is displayed simultaneously on two or more
	driver, adding traps}	displays, e.g. teledisplay}

3/1462	• • { with means for detecting differences between the image stored in the host and the images	7/06	 Arrangements for sorting, selecting, merging, or comparing data on individual record carriers
	displayed on the remote displays}	7/08	• • Sorting, i.e. grouping record carriers in numerical
3/147 3/1475	 using display panels {with conversion of CRT control signals to flat panel control signals, e.g. adapting the palette memory} 		or other ordered sequence according to the classification of at least some of the information they carry (by merging two or more sets of carriers in ordered sequence G06F 7/16)
3/153	using cathode-ray tubes	7/10	Selecting, i.e. obtaining data of one kind from
3/16	• Sound input; Sound output (speech processing G10L)		those record carriers which are identifiable by data of a second kind from a mass of ordered or randomly- distributed record carriers
3/162	 {Interface to dedicated audio devices, e.g. audio drivers, interface to CODECs} 	7/12	• • • with provision for printing-out a list of selected
3/165	• • {Management of the audio stream, e.g. setting of volume, audio stream path}	7/14	items . Merging, i.e. combining at least two sets of
3/167	• • {Audio in a user interface, e.g. using voice commands for navigating, audio feedback}		record carriers each arranged in the same ordered sequence to produce a single set having the same ordered sequence
5/00	Methods or arrangements for data conversion	7/16	Combined merging and sorting
	without changing the order or content of the data	7/20	Comparing separate sets of record carriers
5/01	handledfor shifting, e.g. justifying, scaling, normalising {(digital stores in which the information is		arranged in the same sequence to determine whether at least some of the data in one set is identical with that in the other set or sets
	moved stepwise, e.g. shift-registers G11C 19/00;	7/22	Arrangements for sorting or merging computer data
	digital stores in which the information circulates G11C 21/00)}	7/24	on continuous record carriers, e.g. tape, drum, disc Sorting, i.e. extracting data from one or more
5/012	• • {in floating-point computations}	7721	carriers, rearranging the data in numerical or
5/015	• • {having at least two separately controlled shifting levels, e.g. using shifting matrices (G06F 5/012 takes precedence)}		other ordered sequence, and rerecording the sorted data on the original carrier or on a different carrier or set of carriers {sorting methods in
5/017	• • {using recirculating storage elements}		general (G06F 7/36 takes precedence)
5/06	• for changing the speed of data flow, i.e. speed regularising {or timing, e.g. delay lines, FIFO buffers; over- or underrun control therefor (G06F 7/78 takes precedence)}	7/26	 the sorted data being recorded on the original record carrier within the same space in which the data had been recorded prior to their sorting, without using intermediate storage
5/065	• • {Partitioned buffers, e.g. allowing multiple independent queues, bidirectional FIFO's}	7/32	Merging, i.e. combining data contained in ordered sequence on at least two record carriers to
5/08	• having a sequence of storage locations, the intermediate ones not being accessible for either enqueue or dequeue operations, e.g. using a shift register {(G06F 5/065 takes precedence; shift	- 10.6	produce a single carrier or set of carriers having all the original data in the ordered sequence {merging methods in general}(G06F 7/36 takes precedence)
5/00 <i>5</i>	registers per se G11C 19/00)}	7/36	Combined merging and sorting
5/085 5/10	 {in which the data is recirculated}. having a sequence of storage locations each being individually accessible for both enqueue and	7/38	 Methods or arrangements for performing computations using exclusively denominational number representation, e.g. using binary, ternary,
	dequeue operations, e.g. using random access		decimal representation
5/12	memory {(G06F 5/065 takes precedence)} Means for monitoring the fill level; Means for	7/381	• {using cryogenic components, e.g. Josephson gates}
3,12	resolving contention, i.e. conflicts between simultaneous enqueue and dequeue operations	7/383	 • {using magnetic or similar elements (parametric and other resonant circuits G06F 7/388)}
5/14	• • • • for overflow or underflow handling, e.g. full	7/385	• • • {magnetic bubbles}
5/16	or empty flags • Multiplexed systems, i.e. using two or more	7/386	• • {decimal, radix 20 or 12 (<u>G06F 7/385</u> takes precedence)}
3/10	similar devices which are alternately accessed for enqueue and dequeue operations, e.g. ping-pong buffers	7/388	• • {using other various devices such as electro- chemical, microwave, surface acoustic wave, neuristor, electron beam switching, resonant, e.g.
7/00	Methods or arrangements for processing data by operating upon the order or content of the data handled (logic circuits <u>H03K 19/00</u>)	7/40	parametric, ferro-resonant} using contact-making devices, e.g. electromagnetic relay (G06F 7/46 takes
7/02	• Comparing digital values (<u>G06F 7/06</u> , { <u>G06F 7/22</u> ,} <u>G06F 7/38</u> take precedence)	7/405	precedence) {binary}
7/023	• {adaptive, e.g. self learning}	7/42	• • • Adding; Subtracting {(G06F 7/405 takes
7/026	Magnitude comparison, i.e. determining		precedence)}
- 10	the relative order of operands based on their numerical value, e.g. window comparator}	7/44	• • • Multiplying; Dividing {(G06F 7/405 takes precedence)}
	, ,	7/443	• • • {by successive additions or subtractions}

7/446	• • • { by partial product forming (with electric multiplication table) }	7/4986 {by successive multiplication or division by 2}
7/46	using electromechanical counter-type	7/4988 {by table look-up}
7/461	accumulators	7/499 Denomination or exception handling, e.g.
7/461 7/462	{Adding; subtracting}	rounding or overflow
	{Multiplying; dividing}	7/49905 {Exception handling}
7/463	• • • {by successive additions or subtractions}	7/4991 {Overflow or underflow}
7/465	 { by partial product forming (with electric multiplication table)} 	7/49915 {Mantissa overflow or underflow in handling floating-point numbers}
7/466	• • • {by successive multiplication or division by	7/49921 {Saturation, i.e. clipping the result to a
7/400	2}	minimum or maximum value
7/467	• • • {by using preset multiples of the	7/49926 {Division by zero}
77 107	multiplicand or the divisor}	7/49931 {Modulo N reduction of final result}
7/468	• • • {for evaluating functions by calculation}	7/49936 • • • • {Normalisation mentioned as feature only}
7/48	• using non-contact-making devices, e.g. tube, solid	7/49942 {Significance control}
	state device; using unspecified devices	7/49947 {Rounding}
7/4806	{Computations with complex numbers}	7/49952 {Sticky bit}
7/4812	{Complex multiplication}	7/49957 {Implementation of IEEE-754
7/4818	• • • {using coordinate rotation digital computer	Standard}
	[CORDIC]}	7/49963 {Rounding to nearest (<u>G06F 7/49957</u>
7/4824	• • • {using signed-digit representation}	takes precedence)}
7/483	Computations with numbers represented by	7/49968 {Rounding towards positive infinity
	a non-linear combination of denominational	$(\underline{\text{G06F 7/49957}} \text{ takes precedence})$
	numbers, e.g. rational numbers, logarithmic	7/49973 {Rounding towards negative infinity,
	number system or floating-point numbers {(G06F 7/4806, G06F 7/4824, G06F 7/49,	e.g. truncation of two's complement
	G06F 7/491, G06F 7/544 take precedence)	numbers (<u>G06F 7/49957</u> takes
7/4833	{Logarithmic number system}	precedence)}
7/4836	{Computations with rational numbers}	7/49978 {Rounding towards zero (<u>G06F 7/49957</u> takes precedence)}
7/485	Adding; Subtracting {(G06F 7/4833,	7/49984 {Rounding away from zero}
,,	G06F 7/4836 take precedence)}	7/49989 {Interval arithmetic}
7/487	Multiplying; Dividing {(G06F 7/4833,	7/49994 {Sign extension}
	<u>G06F 7/4836</u> take precedence)}	7/50 Adding; Subtracting
7/4873	{Dividing}	(G06F 7/483 - G06F 7/491,
7/4876	{Multiplying}	G06F 7/544 - G06F 7/556 take precedence)
7/49	Computations with a radix, other than binary,	7/501 Half or full adders, i.e. basic adder cells for
	8, 16 or decimal, e.g. ternary, negative or	one denomination
	imaginary radices, mixed radix {non-linear	7/5013 {using algebraic addition of the input
= //6/	PCM (G06F 7/4824 takes precedence)}	signals, e.g. Kirchhoff adders}
7/491	or 20. (G06F 7/4824 takes precedence)	7/5016 {forming at least one of the output signals
7/4912	• • • {Adding; Subtracting (<u>G06F 7/492</u> ,	directly from the minterms of the input
1/4912	G06F 7/498 take precedence)	signals, i.e. with a minimum number of gate levels}
7/4915	• • • {Multiplying; Dividing (<u>G06F 7/492</u> ,	7/502 Half adders; Full adders consisting of two
777713	G06F 7/498 take precedence)	cascaded half adders { (G06F 7/5013 takes
7/4917	{Dividing}	precedence)}
7/492	using a binary weighted representation	7/503 using carry switching, i.e. the incoming
	within each denomination {(G06F 7/498	carry being connected directly, or only
	takes precedence)}	via an inverter, to the carry output under
7/4925	• • • • {Adding; Subtracting (<u>G06F 7/493</u> takes	control of a carry propagate signal
	precedence)}	7/504 in bit-serial fashion, i.e. having a single digit-
7/493	the representation being the natural binary	handling circuit treating all denominations
	coded representation, i.e. 8421-code	after each other
7/494	Adding; Subtracting	7/5045 {for multiple operands}
7/495	in digit-serial fashion, i.e. having a single digit-handling circuit treating	7/505 in bit-parallel fashion, i.e. having a different digit-handling circuit for each denomination
	all denominations after each other	7/5052 {using carry completion detection, either
7/496	Multiplying; Dividing	over all stages or at sample stages only
7/498	using counter-type accumulators	7/5055 {in which one operand is a constant, i.e.
7/4981	{Adding; Subtracting}	incrementers or decrementers}
7/4983	{Multiplying; Dividing}	7/5057 {using table look-up; using programmable
7/4985	{by successive additions or	logic arrays (G06F 7/509 takes
., ., 05	subtractions}	precedence)}

7/506	with simultaneous carry generation for, or	7/5375 {Non restoring calculation, where each
7,507	propagation over, two or more stages	digit is either negative, zero or positive,
7/507	using selection between two conditionally calculated carry or sum values	e.g. SRT;} 7/544 for evaluating functions by calculation {(G06F 7/4824 takes precedence)}
7/508	using carry look-ahead circuits	7/5443 {Sum of products (for applications thereof,
7/509	• • • • for multiple operands, e.g. digital integrators	see the relevant places, e.g. <u>G06F 17/10</u> , <u>H03H 17/00</u>)}
7/5095	• • • • • {word-serial, i.e. with an accumulator-register}	7/5446 {using crossaddition algorithms, e.g. CORDIC}
7/52	Multiplying; Dividing (G06F 7/483 - G06F 7/491, G06F 7/544 take	7/548 Trigonometric functions; Co-ordinate transformations
7/523	precedence) Multiplying only	7/552 Powers or roots {, e.g. Pythagorean sums} 7/5525 {Roots or inverse roots of single
7/5235	 Multiplying only {using indirect methods, e.g. quarter	operands}
.,	square method, via logarithmic domain}	7/556 Logarithmic or exponential functions
7/525	• • • • in serial-serial fashion, i.e. both operands being entered serially (G06F 7/533 takes precedence)	7/57 Arithmetic logic units [ALU], i.e. arrangements or devices for performing two or more of the operations covered by groups G06F 7/483
7/527	being entered serially and the other in parallel (G06F 7/533 takes precedence)	- G06F 7/556 or for performing logical operations {(G06F 7/49, G06F 7/491 take precedence)}
7/5272	• • • • { with row wise addition of partial products }	7/575 Basic arithmetic logic units, i.e. devices selectable to perform either addition,
7/5275	• • • • • {using carry save adders}	subtraction or one of several logical operations, using, at least partially, the same
7/5277	• • • • { with column wise addition of partial products }	circuitry
7/53	in parallel-parallel fashion, i.e. both	7/58 • Random or pseudo-random number generators
	operands being entered in parallel	7/582 {Pseudo-random number generators}
7/5306	(G06F 7/533 takes precedence) {with row wise addition of partial}	7/584 • • • {using finite field arithmetic, e.g. using a linear feedback shift register}
775500	products (<u>G06F 7/5324</u> takes precedence)}	7/586 {using an integer algorithm, e.g. using linear congruential method}
7/5312	{using carry save adders}	7/588 • • {Random number generators, i.e. based on natural
7/5318	• • • • { with column wise addition of partial	stochastic processes}
	products, e.g. using Wallace tree, Dadda counters (<u>G06F 7/5324</u> takes precedence)}	 Methods or arrangements for performing computations using a digital non-denominational number representation, i.e. number representation
7/5324	•••• {partitioned, i.e. using repetitively a smaller parallel parallel multiplier or using an array of such smaller	without radix; Computing devices using combinations of denominational and non-denominational quantity representations {, e.g.
7/522	multipliers}	using difunction pulse trains, STEELE computers, phase computers (conversion of digital data to
7/533	• • • • Reduction of the number of iteration steps or stages, e.g. using the Booth algorithm, log-sum, odd-even	or from non-denominational form H03M 5/00, H03M 7/00)}
7/5332	{by skipping over strings of zeroes or	7/602 {using delta-sigma sequences}
7/5334	ones, e.g. using the Booth Algorithm \\ \{\text{by using multiple bit scanning, i.e.}	 7/605 • {Additive or subtractive mixing of two pulse rates into one (beat-frequency oscillators H03B 21/00; input circuits of electric counters, e.g. up-down
	by decoding groups of successive multiplier bits in order to select an	counters H03K 21/00)}
	appropriate precalculated multiple of the multiplicand as a partial product}	7/607 • • {number-of-ones counters, i.e. devices for counting the number of input lines set to ONE
7/5336	• • • • • • {overlapped, i.e. with successive	among a plurality of input lines, also called bit counters or parallel counters (for applications
	bitgroups sharing one or more bits being recoded into signed	thereof, see the relevant places, e.g. G06F 7/49, G06F 7/5013, G06F 7/509, H03M 1/00,
	digit representation, e.g. using the Modified Booth Algorithm}	<u>H03M 7/20</u>)}
7/5338	• • • • • { each bitgroup having two new bits, e.g. 2nd order MBA}	7/62 • Performing operations exclusively by counting total number of pulses {; Multiplication,
7/535	Dividing only	division or derived operations using combined
7/537	Reduction of the number of iteration	denominational and incremental processing by counters, i.e. without column shift (G06F 7/68
	steps or stages, e.g. using the Sweeny-Robertson-Tocher [SRT] algorithm	takes precedence)}

7/64	Digital differential analysers, i.e. computing	8/00	Arrangements for software engineering (testing or
	devices for differentiation, integration or solving differential or integral equations, using pulses representing increments; Other incremental		debugging <u>G06F 11/36</u> ; administrative, planning or organisation aspects of software project management
	computing devices for solving difference	0/10	G06Q 10/06)
	equations (G06F 7/70 takes precedence;	8/10 8/20	Requirements analysis; Specification techniques
	differential analysers using hybrid computing		. Software design
	techniques G06J 1/02 {; DDA application in	8/22	• • {Procedural}
	numerical control G05B 19/18})	8/24	• • {Object-oriented}
7/66	wherein pulses represent unitary increments	8/30	• Creation or generation of source code
7/68	onlyusing pulse rate multipliers or dividers {pulse	8/31	• • {Programming languages or programming paradigms}
7/08	rate multipliers or dividers per se}(G06F 7/70 takes precedence {; frequency division in	8/311	• • • {Functional or applicative languages; Rewrite languages}
	electronic watches <u>G04G 3/02</u> ; frequency multiplication or division in oscillators	8/312	• • • {List processing, e.g. LISP programming language}
	<u>H03B 19/00;</u> frequency dividing counters <u>per se</u> <u>H03K 23/00 - H03K 29/00</u> })	8/313	• • • {Logic programming, e.g. PROLOG programming language}
7/70	using stochastic pulse trains, i.e. randomly	8/3135	• • • {Unification or backtracking}
	occurring pulses the average pulse rates of which represent numbers {(conversion of analogue	8/314	• • {Parallel programming languages (G06F 8/313 takes precedence)}
	signals into stochastic pulse trains and vice versa	8/315	• • • {Object-oriented languages}
	H03M 1/04)}	8/316	{Aspect-oriented ranguages} {Aspect-oriented programming techniques}
7/72	using residue arithmetic	8/33	Intelligent editors
7/721	• • • • • • • • • • • • • • • • •	8/34	Graphical or visual programming
77721	calculation (<u>G06F 7/724</u> , <u>G06F 7/727</u> ,	8/35	model driven
	G06F 7/728 take precedence)}	8/355	• • • {Round-trip engineering}
7/722	• • • {Modular multiplication (G06F 7/724,	8/36	Software reuse
	G06F 7/727, G06F 7/728 take precedence)}	8/37	• • {Compiler construction; Parser generation}
7/723	{Modular exponentiation (G06F 7/724,	8/38	• for implementing user interfaces
	<u>G06F 7/727</u> , <u>G06F 7/728</u> take precedence)}	8/40	Transformation of program code
7/724	• • • {Finite field arithmetic (for error detection	8/41	Compilation
	or correction in general H03M 13/00, in	8/42	{Syntactic analysis}
	computers <u>G06F 11/10</u>)}	8/423	· · · · {Preprocessors}
7/725	• • • • {over elliptic curves}	8/425	{Lexical analysis}
7/726	{Inversion; Reciprocal calculation; Division	8/427	· · · · {Parsing}
7/727	of elements of a finite field}	8/43	• • {Checking; Contextual analysis}
1/121	• • • {Modulo N arithmetic, with N being either (2**n)-1,2**n or (2**n)+1, e.g. mod 3, mod 4 or mod 5 (G06F 7/728 takes precedence)}	8/433	• • • {Dependency analysis; Data or control flow analysis}
7/728	• • • {using Montgomery reduction}	8/434	• • • • {Pointers; Aliasing}
7/729	• • • {using representation by a residue number	8/436	• • • {Semantic checking}
	system}	8/437	• • • • {Type checking}
7/74	Selecting or encoding within a word the position of	8/44	• • • {Encoding}
	one or more bits having a specified value, e.g. most	8/441	• • • • {Register allocation; Assignment of physical
	or least significant one or zero detection, priority encoders {(with shifting G06F 5/01)}	8/443	memory space to logical memory space} {Optimisation}
7/76	Arrangements for rearranging, permuting or	8/4432	• • • • {Reducing the energy consumption}
	selecting data according to predetermined rules,	8/4434	{Reducing the memory space required by
7/7/2	independently of the content of the data		the program code}
7/762	 {having at least two separately controlled rearrangement levels, e.g. 	8/4435	• • • • • {Detection or removal of dead or redundant code}
	multistage interconnection networks	8/4436	{Exlining; Procedural abstraction}
7/7/1	(G06F7/764 - G06F7/768) take precedence)	8/4441	• • • • • {Reducing the execution time required by
7/764	. (Masking)	0/4/12	the program code}
7/766 7/768	 {Generation of all possible permutations} {Data position reversal, e.g. bit reversal, byte	8/4442	{Reducing the number of cache misses; Data prefetching (cache prefetching
	swapping}		<u>G06F 12/0862</u>)}
7/78	• • for changing the order of data flow, e.g. matrix	8/4443	• • • • • {Inlining}
	transposition or LIFO buffers; Overflow or	8/445	• • • {Exploiting fine grain parallelism, i.e.
7/705	underflow handling therefor		parallelism at instruction level (run-time
7/785	 • {having a sequence of storage locations each being individually accessible for both enqueue 	0/11/21	instruction scheduling G06F 9/3836)}
	and dequeue operations, e.g. using a RAM}	8/4451 8/4452	{Avoiding pipeline stalls} {Software pipelining}
	operations, e.g. doing a ra in i	8/4432 8/447	{Software piperining} {Target code generation}
		0/44/	• • • \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \

8/45	• • • {Exploiting coarse grain parallelism in	9/223 {Execution means for microinstructions
0/43	compilation, i.e. parallelism between groups of	irrespective of the microinstruction function,
	instructions}	e.g. decoding of microinstructions and
8/451	{Code distribution (considering CPU load	nanoinstructions; timing of microinstructions;
0/ 151	at run-time <u>G06F 9/505</u> ; load rebalancing	programmable logic arrays; delays and fan-out
	G06F 9/5083)}	problems}
8/452	{Loops}	9/226 • • • {Microinstruction function, e.g. input/output
8/453	• • • • {Data distribution}	microinstruction; diagnostic microinstruction;
8/454	{Consistency (cache consistency protocols	microinstruction format}
0/454	in hierarchically structured memory	9/24 Loading of the microprogram
	systems <u>G06F 12/0815</u>)}	9/26 Address formation of the next micro-instruction
8/456	• • • {Parallelism detection}	(G06F 9/28 takes precedence) {; Microprogram
8/457	Communication (intertask communication)	storage or retrieval arrangements}
0/43/	G06F 9/54)}	9/261 {Microinstruction address formation}
8/458	• • • {Synchronisation, e.g. post-wait, barriers,	9/262 {Arrangements for next microinstruction
0/450	locks (synchronisation among tasks	selection}
	G06F 9/52)}	9/264 {Microinstruction selection based on
8/47	• • • {Retargetable compilers}	results of processing}
8/48	{Incremental compilation (software reuse}	9/265 {by address selection on input of
0/40	G06F 8/36)}	storage}
8/49	• • • {Partial evaluation}	9/267 {by instruction selection on output of
8/51	Source to source	storage}
8/52	Binary to binary	9/268 {Microinstruction selection not based on
		processing results, e.g. interrupt, patch,
8/53	. Decompilation; Disassembly	first cycle store, diagnostic programs}
8/54	. Link editing before load time	9/28 Enhancement of operational speed, e.g. by
8/60	Software deployment	using several microcontrol devices operating in
8/61	Installation	parallel
8/62	{Uninstallation}	9/30 . Arrangements for executing machine instructions,
8/63	• • • {Image based installation; Cloning; Build to	e.g. instruction decode (for executing
	order}	microinstructions G06F 9/22)
8/64	• • • {Retargetable}	9/30003 {Arrangements for executing specific machine
8/65	Updates (security arrangements therefor	instructions}
	<u>G06F 21/57</u>)	9/30007 {to perform operations on data operands}
8/654	• • • using techniques specially adapted for alterable	9/3001 {Arithmetic instructions}
	solid state memories, e.g. for EEPROM or flash	9/30014 {with variable precision}
	memories	9/30018 {Bit or string instructions}
8/656	while running	9/30021 {Compare instructions, e.g. Greater-Than,
8/658	Incremental updates; Differential updates	Equal-To, MINMAX}
8/66	• • • {of program code stored in read-only memory	9/30025 {Format conversion instructions, e.g.
	[ROM]}	Floating-Point to Integer, decimal
8/70	Software maintenance or management	conversion}
8/71	• • Version control (security arrangements therefor	9/30029 {Logical and Boolean instructions, e.g.
	G06F 21/57); Configuration management	XOR, NOT}
8/72	Code refactoring	9/30032 • • • • • {Movement instructions, e.g. MOVE,
8/73	Program documentation	SHIFT, ROTATE, SHUFFLE}
8/74	Reverse engineering; Extracting design	9/30036 {Instructions to perform operations on
	information from source code	packed data, e.g. vector, tile or matrix
8/75	Structural analysis for program understanding	operations}
8/751	• • • {Code clone detection}	9/30038 {using a mask}
8/76	Adapting program code to run in a different	9/3004 • • • {to perform operations on memory}
	environment; Porting	9/30043 {LOAD or STORE instructions; Clear
8/77	Software metrics	instruction}
8/78	• • {Methods to solve the "Year 2000" [Y2K]	9/30047 {Prefetch instructions; cache control
	problem}	instructions}
0/00	A	9/3005 {to perform operations for flow control}
9/00	Arrangements for program control, e.g. control	9/30054 {Unconditional branch instructions}
	units (program control for peripheral devices	9/30058 {Conditional branch instructions}
0/02	<u>G06F 13/10</u>)	9/30061 {Multi-way branch instructions, e.g.
9/02	• using wired connections, e.g. plugboards	CASE}
9/04	• using record carriers containing only program	9/30065 {Loop control instructions; iterative
0/06	instructions (<u>G06F 9/06</u> takes precedence)	instructions, e.g. LOOP, REPEAT}
9/06	• using stored programs, i.e. using an internal store of	9/30069 {Instruction skipping instructions, e.g.
0/22	processing equipment to receive or retain programs	SKIP}
9/22	Microcontrol or microprogram arrangements	~ ,

9/30072	• • • {to perform conditional operations, e.g. using predicates or guards}	9/324 {using program counter relative addressing}
9/30076	• • • {to perform miscellaneous control operations, e.g. NOP}	9/325 • • • • {for loops, e.g. loop detection or loop counter}
9/30079	• • • • {Pipeline control instructions, e.g.	9/327 {for interrupts}
	multicycle NOP}	9/328 {for runtime instruction patching}
9/30083	• • • • {Power or thermal control instructions}	9/34 Addressing or accessing the instruction
9/30087	• • • • {Synchronisation or serialisation instructions}	operand or the result {; Formation of operand address; Addressing modes (address translation
9/3009	• • • • {Thread control instructions}	<u>G06F 12/00</u>)}
9/30094	• • • {Condition code generation, e.g. Carry, Zero flag}	9/342 {Extension of operand address space} 9/345 of multiple operands or results {(addressing)
9/30098	• • • {Register arrangements}	multiple banks <u>G06F 12/06</u>)}
9/30101	{Special purpose registers}	9/3455 {using stride}
9/30105	• • • {Register structure}	9/35 Indirect addressing
9/30109	• • • • {having multiple operands in a single	9/355 Indexed addressing
2/20102	register}	9/3552 {using wraparound, e.g. modulo or circular
9/30112	• • • • {comprising data of variable length}	addressing}
	• • • • (Shadow registers, e.g. coupled registers,	9/3555 {using scaling, e.g. multiplication of
	not forming part of the register space}	index}
9/3012	• • • (Organisation of register space, e.g. banked	
	or distributed register file}	9/38 Concurrent instruction execution, e.g. pipeline
9/30123	• • • • {according to context, e.g. thread buffers}	or look ahead
9/30127	• • • • {Register windows}	9/3802 {Instruction prefetching}
9/3013	• • • • {according to data content, e.g. floating- point registers, address registers}	9/3804 {for branches, e.g. hedging, branch folding}
9/30134	• • • • {Register stacks; shift registers}	9/3806 (using address prediction, e.g. return
9/30138	• • • • {Extension of register space, e.g. register cache}	stack, branch history buffer} 9/3808 {for instruction reuse, e.g. trace cache,}
9/30141	• {Implementation provisions of register files, e.g. ports}	branch target cache } 9/381 {Loop buffering}
0/20145		The second secon
	• • • {Instruction analysis, e.g. decoding, instruction word fields}	into instruction stream}
	• • • { of variable length instructions }	9/3814 {Implementation provisions of instruction
9/30152	• • • • {Determining start or end of instruction; determining instruction length}	buffers, e.g. prefetch buffer; banks} 9/3816 {Instruction alignment, e.g. cache line
9/30156	{Special purpose encoding of instructions,	crossing}
	e.g. Gray coding}	9/3818 {Decoding for concurrent execution}
9/3016	• • • {Decoding the operand specifier, e.g. specifier format}	9/382 {Pipelined decoding, e.g. using predecoding}
9/30163	• • • • {with implied specifier, e.g. top of stack}	9/3822 {Parallel decoding, e.g. parallel decode
	• • • • {with implied specifier, e.g. top of statek} • • • • {of immediate specifier, e.g. constants}	units}
		9/3824 {Operand accessing}
9/3017	• • • {Runtime instruction translation, e.g. macros}	
9/30174	• • • { for non-native instruction set, e.g. Javabyte, legacy code }	9/3826 {Bypassing or forwarding of data results, e.g. locally between pipeline stages
9/30178	• • • {of compressed or encrypted instructions}	or within a pipeline stage}
9/30181	• • • {Instruction operation extension or modification}	9/3828 {with global bypass, e.g. between pipelines, between clusters}
9/30185	• • • { according to one or more bits in the instruction, e.g. prefix, sub-opcode}	9/383 {Operand prefetching (cache prefetching <u>G06F 12/0862</u>)}
9/30189	• • • {according to execution mode, e.g. mode flag}	9/3832 {Value prediction for operands; operand history buffers}
9/30192	• • • • {according to data descriptor, e.g. dynamic	9/3834 {Maintaining memory consistency}
9/30192	data typing }	9/3836 {Instruction issuing, e.g. dynamic instruction
9/30196	• • • {using decoder, e.g. decoder per instruction	scheduling or out of order instruction execution}
0./22	set, adaptable or programmable decoders}	9/3838 {Dependency mechanisms, e.g. register
9/32	• Address formation of the next instruction, e.g. by incrementing the instruction counter	scoreboarding}
	(G06F 9/38 takes precedence)	9/384 {Register renaming}
9/321	• • • {Program or instruction counter, e.g.	9/3842 {Speculative instruction execution}
	incrementing}	9/3844 {using dynamic branch prediction, e.g.
9/322	• • • { for non-sequential address }	using branch history tables}
9/323	• • • • {for indirect branch instructions}	9/3846 {using static prediction, e.g. branch taken strategy}

9/3848	• • • • {using hybrid branch prediction,	9/4401 Bootstrapping (security arrangements therefor
	e.g. selection between prediction	G06F 21/57)
	techniques}	9/4403 {Processor initialisation}
9/3851	• • • • {from multiple instruction streams, e.g.	9/4405 • • • • {Initialisation of multiprocessor systems}
	multistreaming}	9/4406 {Loading of operating system}
9/3853	• • • • {of compound instructions}	9/4408 {Boot device selection}
9/3854	• • • {Instruction completion, e.g. retiring, committing or graduating}	9/441 {Multiboot arrangements, i.e. selecting an operating system to be loaded}
9/3856	• • • • {Reordering of instructions, e.g. using queues or age tags}	9/4411 {Configuring for operating with peripheral devices; Loading of device drivers}
9/3858	• • • • {Result writeback, i.e. updating the	9/4413 {Plug-and-play [PnP]}
	architectural state or memory}	9/4415 {Self describing peripheral devices}
9/38585	• • • • { with result invalidation, e.g.	9/4416 {Network booting; Remote initial program
	nullification}	loading [RIPL]}
9/3861	• • • {Recovery, e.g. branch miss-prediction,	9/4418 {Suspend and resume; Hibernate and awake}
	exception handling (error detection or	9/442 {Shutdown}
	correction <u>G06F 11/00</u>)}	9/445 Program loading or initiating (bootstrapping
9/3863	• • • • {using multiple copies of the architectural	G06F 9/4401; security arrangements for
	state, e.g. shadow registers}	program loading or initiating G06F 21/57)
9/3865	• • • • {using deferred exception handling, e.g.	9/44505 {Configuring for program initiating, e.g.
	exception flags}	using registry, configuration files}
9/3867	• • • {using instruction pipelines}	9/4451 {User profiles; Roaming}
9/3869	{Implementation aspects, e.g. pipeline	9/44521 {Dynamic linking or loading; Link editing at
	latches; pipeline synchronisation and	or after load time, e.g. Java class loading}
	clocking}	9/44526 {Plug-ins; Add-ons}
9/3871	{Asynchronous instruction pipeline, e.g.	9/44536 {Selecting among different versions}
0.400=0	using handshake signals between stages}	9/44542 {Retargetable}
9/3873	• • • • {Variable length pipelines, e.g. elastic	9/44547 {Fat binaries}
0/2075	pipeline}	9/44552 {Conflict resolution, i.e. enabling
9/3875	• • • • {Pipelining a single stage, e.g.	coexistence of conflicting executables}
9/3877	superpipelining}	9/44557 {Code layout in executable memory}
9/30//	• • • • {using a slave processor, e.g. coprocessor (peripheral processor <u>G06F 13/12</u> ; vector	9/44563 {Sharing}
	processor <u>G06F 15/8053</u>)}	9/44568 {Immediately runnable code}
9/3879	• • • • {for non-native instruction execution, e.g.	9/44573 {Execute-in-place [XIP]}
2/3017	executing a command; for Java instruction	9/44578 {Preparing or optimising for loading}
	set}	9/44584 {Portable applications, i.e. making
9/3881	{Arrangements for communication of	applications self-contained, e.g. U3 standard}
2000/2002	instructions and data}	9/44589 • • • • { Program code verification, e.g. Java
2009/3883	Two-engine architectures, i.e. stand-alone processor acting as a slave processor	bytecode verification, proof-carrying code
9/3885	• • • {using a plurality of independent parallel	(high-level semantic checks <u>G06F 8/43;</u> prevention of errors by analysis, debugging
	functional units}	or testing of software G06F 11/36)}
9/3887	• • • • {controlled by a single instruction for	9/44594 {Unloading}
0.4000=0	multiple data lanes [SIMD]}	9/448 Execution paradigms, e.g. implementations of
9/38873	{Iterative single instructions for multiple	programming paradigms
0/20075	data lanes [SIMD]}	9/4482 {Procedural}
9/38875	{for adaptable or variable architectural vector length}	9/4484 {Executing subprograms}
9/3888	• • • • {controlled by a single instruction for	9/4486 {Formation of subprogram jump
	multiple threads [SIMT] in parallel}	address}
9/38885	{Divergence aspects}	9/4488 {Object-oriented}
9/3889	 {controlled by multiple instructions, e.g. MIMD, decoupled access or execute} 	9/449 {Object-oriented method invocation or resolution}
9/3891	{organised in groups of units sharing	9/4491 {Optimising based on receiver type}
	resources, e.g. clusters}	9/4492 {Inheritance}
9/3893	• • • • {controlled in tandem, e.g. multiplier-	9/4493 {Object persistence}
	accumulator}	9/4494 {data driven}
9/3895	• • • • • {for complex operations, e.g.	9/4496 {Unification in logic programming}
	multidimensional or interleaved address	9/4498 {Finite state machines}
0/2005	generators, macros}	9/451 Execution arrangements for user interfaces
9/3897	• • • • • { with adaptable data path }	9/452 {Remote windowing, e.g. X-Window
9/44	Arrangements for executing specific programs	System, desktop virtualisation (protocols for virtual reality <u>H04L 67/131</u>)}

9/453	{Help systems}	9/467 {Transactional memory (G06F 9/528 takes
9/454	• • • • {Multi-language systems; Localisation;	precedence)}
9/455	Internationalisation \\ Emulation; Interpretation; Software simulation,	9/468 • • • {Specific access rights for resources, e.g. using capability register}
7/433	e.g. virtualisation or emulation of application or operating system execution engines	9/48 Program initiating; Program switching, e.g. by interrupt
9/45504	• • • {Abstract machines for programme code	9/4806 {Task transfer initiation or dispatching}
	execution, e.g. Java virtual machine [JVM],	9/4812 {by interrupt, e.g. masked}
	interpreters, emulators}	9/4818 {Priority circuits therefor}
9/45508	• • • • {Runtime interpretation or emulation, e.g.	9/4825 {Interrupt from clock, e.g. time of day}
	emulator loops, bytecode interpretation}	9/4831 {with variable priority}
9/45512	• • • • {Command shells}	9/4837 { time dependent }
9/45516	{Runtime code conversion or	9/4843 {by program, e.g. task dispatcher,
0/4550	optimisation}	supervisor, operating system}
9/4552	{Involving translation to a different instruction set architecture, e.g. just-intime translation in a JVM}	9/485 {Task life-cycle, e.g. stopping, restarting, resuming execution
0/45525	• • • • • {Optimisation or modification within	$(\underline{G06F 9/4881} \text{ takes precedence})\}$
9/43323	the same instruction set architecture, e.g. HP Dynamo}	9/4856 {resumption being on a different machine, e.g. task migration, virtual machine migration (G06F 9/5088
9/45529	• • • • {Embedded in an application, e.g.	takes precedence)}
	JavaScript in a Web browser}	9/4862 {the task being a mobile agent, i.e.
9/45533	{Hypervisors; Virtual machine monitors}	specifically designed to migrate}
9/45537	• • • • {Provision of facilities of other operating environments, e.g. WINE (I/O emulation	9/4868 {with creation or replication}
0/45541	<u>G06F 13/105</u>)}	9/4875 {with migration policy, e.g. auction, contract negotiation}
	• • • • {Bare-metal, i.e. hypervisor runs directly on hardware}	9/4881 {Scheduling strategies for dispatcher, e.g. round robin, multi-level priority
9/45545	• • • • {Guest-host, i.e. hypervisor is an	queues}
	application program itself, e.g. VirtualBox}	9/4887 {involving deadlines, e.g. rate based, periodic}
9/4555	• • • {Para-virtualisation, i.e. guest operating system has to be modified}	9/4893 {taking into account power or heat
9/45554	{Instruction set architectures of guest OS and hypervisor or native processor differ, e.g. Bochs or VirtualPC on PowerPC	criteria (power management in computers in general G06F 1/3203; thermal management in computers in
	MacOS}	general G06F 1/206)} 9/50 • • • Allocation of resources, e.g. of the central
9/45558	{Hypervisor-specific management and	processing unit [CPU]
	integration aspects}	9/5005 {to service a request}
2009/45562	• • • • • {Creating, deleting, cloning virtual	9/5011 {the resources being hardware resources
	machine instances}	other than CPUs, Servers and Terminals}
	{Nested virtual machines}	9/5016 {the resource being the memory}
2009/4557	{Distribution of virtual machine	9/5022 • • • • • {Mechanisms to release resources}
	instances; Migration and load balancing}	9/5027 {the resource being a machine, e.g. CPUs, Servers, Terminals}
2009/45575	{Starting, stopping, suspending or	9/5033 {considering data affinity}
2000/45570	resuming virtual machine instances} {I/O management, e.g. providing access	9/5038 {considering the execution order of a
	to device drivers or storage}	plurality of tasks, e.g. taking priority or time dependency constraints into
	{Memory management, e.g. access or allocation}	consideration (scheduling strategies G06F 9/4881 and subgroups)}
	• • • • {Isolation or security of virtual machine instances}	9/5044 {considering hardware capabilities} 9/505 {considering the load}
	• • • • • {Monitoring or debugging support}	9/5055 {considering software capabilities,
2009/45595	• • • • • {Network integration; Enabling network access in virtual machine instances}	i.e. software resources associated or available to the machine}
9/46	• • Multiprogramming arrangements	9/5061 {Partitioning or combining of resources}
9/461	• • • {Saving or restoring of program or task context}	9/5066 {Algorithms for mapping a plurality of inter-dependent sub-tasks onto a plurality
9/462	• • • { with multiple register sets }	of physical CPUs (mappping at compile
9/463	• • • {Program control block organisation}	time, see G06F 8/451)}
9/465	• • • {Distributed object oriented systems (remote	9/5072 {Grid computing}
9/466	method invocation [RMI] <u>G06F 9/548</u>)} {Transaction processing}	

9/5077	• • • • {Logical partitioning of resources; Management or configuration of virtualized resources (specific details on emulation or internal functioning of virtual machines G06F 9/455)}	11/0703 • Error or fault processing not based on redundancy, i.e. by taking additional measures to deal with the error or fault not making use of redundancy in operation, in hardware, or in data representation}
9/5083	• • • {Techniques for rebalancing the load in a distributed system}	11/0706 {the processing taking place on a specific hardware platform or in a specific software
9/5088	• • • • {involving task migration}	environment}
9/5094	• • • • {where the allocation takes into account power or heat criteria (power management in computers in general <u>G06F 1/3203</u> ; thermal management in computers in general	11/0709 {in a distributed system consisting of a plurality of standalone computer nodes, e.g. clusters, client-server systems} 11/0712 {in a virtual computing platform, e.g.
	<u>G06F 1/206</u>)}	logically partitioned systems}
9/52	• Program synchronisation; Mutual exclusion, e.g. by means of semaphores	11/0715 {in a system implementing multitasking (multitasking per se G06F 9/46)}
9/522	• • • {Barrier synchronisation}	11/0718 {in an object-oriented system}
9/524	{Deadlock detection or avoidance}	11/0721 { within a central processing unit [CPU]}
9/526	{Mutual exclusion algorithms}	11/0724 {in a multiprocessor or a multi-core unit
9/528	• • • • {by using speculative mechanisms}	(multiprocessors per se G06F 15/80)}
9/54	Interprogram communication	11/0727 {in a storage system, e.g. in a DASD or
9/541	• • • { via adapters, e.g. between incompatible applications }	network based storage system (drivers for digital recording or reproducing units
9/542	• • • {Event management; Broadcasting; Multicasting; Notifications}	<u>G06F 3/06</u> ; circuits for error detection or correction within digital recording
9/543	• • • {User-generated data transfer, e.g. clipboards, dynamic data exchange [DDE], object linking and embedding [OLE]}	or reproducing units G11B 20/18; for distributed storage of data in networks, e.g. transport arrangements for network
9/544	• • • {Buffers; Shared memory; Pipes}	file system [NFS], storage area networks
9/545	• • • {where tasks reside in different layers, e.g. user- and kernel-space}	[SAN] or network attached storage [NAS], H04L 67/1097)}
9/546	{Message passing systems or structures, e.g. queues}	11/073 {in a memory management context, e.g. virtual memory or cache management
9/547	• • • • {Remote procedure calls [RPC]; Web services}	(memory management <u>G06F 12/00</u> ; testing of static memory units <u>G11C 29/00</u>)}
9/548	• • • • {Object oriented; Remote method	11/0733 {in a data processing system embedded in
3/340	invocation [RMI] (non-remote method invocation G06F 9/449)}	an image processing device, e.g. printer, facsimile, scanner}
	111vocation <u>(300F 9/449</u>)}	11/0736 {in functional embedded systems, i.e. in
11/00	Error detection; Error correction; Monitoring (error detection, correction or monitoring in	a data processing system designed as a combination of hardware and software
	information storage based on relative movement	dedicated to performing a certain function
	between record carrier and transducer <u>G11B 20/18</u> ; monitoring, i.e. supervising the progress of recording	(testing or monitoring of automated control systems <u>G05B 23/02</u>)}
	or reproducing <u>G11B 27/36</u> ; in static stores <u>G11C 29/00</u>)	11/0739 {in a data processing system embedded in automotive or aircraft systems}
		11/0742 (in a data processing system embedded
	NOTE In this group the indexing codes of	in a mobile device, e.g. mobile phones, handheld devices}
11/002	G06F 1/00 - G06F 15/00 are added	11/0745 {in an input/output transactions management context (input/output processing in general
11/002	 {protecting against parasitic influences, e.g. noise, temperatures} 	G06F 13/00)} 11/0748 {in a remote unit communicating with a
	WARNING	single-box computer node experiencing an error/fault (remote testing G06F 11/2294)}
	This group is no longer used for the classification of new documents as from January 1, 2011. The documents are classified in G06F 11/07 and	11/0751 • • • {Error or fault detection not based on redundancy (power supply failures G06F 1/30;
	subgroups according to the features used for	network fault management <u>H04L 41/06</u>)}
	protecting	11/0754 {by exceeding limits}
11/004	• {Error avoidance (G06F 11/07 and subgroups take	11/0757 {by exceeding a time limit, i.e. time-out, e.g. watchdogs}
11/006	precedence)} • {Identification (G06F 11/2289 takes precedence)}	11/076 {by exceeding a count or rate limit, e.g. word- or bit count limit}
11/008	• {Reliability or availability analysis}	11/0763 {by bit configuration check, e.g. of formats
11/07	• Responding to the occurrence of a fault, e.g. fault tolerance	or tags} 11/0766 • • • {Error or fault reporting or storing}

11/0769	• • • • {Readable error formats, e.g. cross-platform generic formats, human understandable	11/1056 {Updating check bits on partial write, i.e. read/modify/write}
11/0772	formats} {Means for error signaling, e.g. using	11/106 {Correcting systematically all correctable errors, i.e. scrubbing}
	interrupts, exception flags, dedicated error registers}	11/1064 {in cache or content addressable memories}
11/0775	{Content or structure details of the error	11/1068 {in sector programmable memories,
	report, e.g. specific table structure, specific	e.g. flash disk (<u>G06F 11/1072</u> takes
	error fields}	precedence)}
11/0778	• • • {Dumping, i.e. gathering error/state	11/1072 • • • • {in multilevel memories}
	information after a fault for later diagnosis}	11/1076 {Parity data used in redundant arrays of
11/0781	• • • {Error filtering or prioritizing based on a	independent storages, e.g. in RAID systems}
	policy defined by the user or on a policy defined by a hardware/software module, e.g.	11/108 {Parity data distribution in semiconductor
	according to a severity level}	storages, e.g. in SSD}
11/0784	• • • {Routing of error reports, e.g. with a specific	11/1084 {Degraded mode, e.g. caused by single or multiple storage removals or disk failures}
11,0,0.	transmission path or data flow}	11/1088 {Reconstruction on already foreseen single
11/0787	{Storage of error reports, e.g. persistent data	or plurality of spare disks}
	storage, storage using memory protection}	11/1092 {Rebuilding, e.g. when physically
11/079	• • • {Root cause analysis, i.e. error or fault	replacing a failing disk}
	diagnosis (in a hardware test environment	11/1096 {Parity calculation or recalculation after
	G06F 11/22; in a software test environment	configuration or reconfiguration of the
11/0702	G06F 11/36)}	system}
11/0793	• • • {Remedial or corrective actions (recovery from an exception in an instruction pipeline	11/14 • Error detection or correction of the data by
	G06F 9/3861; by retry G06F 11/1402; for	redundancy in operation (G06F 11/16 takes
	recovering from a failure of a protocol instance	precedence)
	or entity $\underline{\text{H04L } 69/40}$)	11/1402 {Saving, restoring, recovering or retrying} 11/1405 {at machine instruction level}
11/0796	• • {Safety measures, i.e. ensuring safe condition in	11/1405 {at machine instruction level} 11/1407 {Checkpointing the instruction stream}
	the event of error, e.g. for controlling element}	11/140 {Checkpointing the instruction stream}
11/08	Error detection or correction by redundancy in	11/1415 {at system level}
	data representation, e.g. by using checking codes	11/1417 {Boot up procedures}
11/085	• • • {using codes with inherent redundancy, e.g. n-	11/142 {Reconfiguring to eliminate the error
11/10	out-of-m codes}	(group management mechanisms in a peer-
11/10	Adding special bits or symbols to the coded information, e.g. parity check, casting out 9's or	to-peer network <u>H04L 67/1044</u>)}
	11's	11/1423 {by reconfiguration of paths}
11/1004	• • • {to protect a block of data words, e.g.	11/1425 (by reconfiguration of node
	CRC or checksum (G06F 11/1076 takes	membership}
	precedence; security arrangements for	11/1428 { with loss of hardware functionality}
	protecting computers or computer systems	11/143 {with loss of software functionality}
	against unauthorized activity <u>G06F 21/00</u>)}	11/1433 {during software upgrading}
11/1008	{in individual solid state devices	11/1435 {using file system or storage system metadata}
11/1012	(G06F 11/1004 takes precedence)} {using codes or arrangements adapted for	11/1438 {Restarting or rejuvenating}
11/1012	a specific type of error (G06F 11/1048	11/1441 {Resetting or repowering}
	takes precedence)}	11/1443 {Transmit or communication errors}
11/1016	{Error in accessing a memory location,	11/1446 {Point-in-time backing up or restoration of
	i.e. addressing error}	persistent data}
11/102	{Error in check bits}	11/1448 {Management of the data involved in
11/1024	• • • • • {Identification of the type of error}	backup or backup restore}
11/1028	• • • • • {Adjacent errors, e.g. error in n-bit	11/1451 {by selection of backup contents}
	(n>1) wide storage units, i.e. package	11/1453 {using de-duplication of the data}
11/1022	error}	11/1456 {Hardware arrangements for backup}
11/1032	{Simple parity}	11/1458 {Management of the backup or restore
11/1036	{Unidirectional errors}	process}
11/104	• • • • • {using arithmetic codes, i.e. codes which are preserved during operation,	11/1461 {Backup scheduling policy}
	e.g. modulo 9 or 11 check}	11/1464 {for networked environments}
11/1044	• • • • { with specific ECC/EDC distribution }	11/1466 {to make the backup process non-
11/1044	{with specific ECC/EDC distribution} {using arrangements adapted for a specific	disruptive} 11/1469 {Backup restoration techniques}
3 . 0	error detection or correction feature}	11/1471 {Backup restoration techniques}
11/1052	• • • • • {Bypassing or disabling error detection	recovery}
	or correction}	,,

11/1474			
	• • • • {in transactions (<u>G06F 16/20</u> takes precedence)}	11/167	• • • {Error detection by comparing the memory output}
11/1476	• • {in neural networks}	11/1675	{Temporal synchronisation or re-
11/1479	• • • {Generic software techniques for error detection or fault masking}		synchronisation of redundant processing components}
11/1482	• • • • {by means of middleware or OS	11/1679	• • • {at clock signal level}
11/1402	functionality}	11/1683	• • • {at instruction level}
11/1484	• • • • {involving virtual machines}	11/1687	{at event level, e.g. by interrupt or result of
		11/100/	polling}
11/1487	{using N-version programming}	11/1/01	* T'
11/1489	{through recovery blocks}	11/1691	• • • {using a quantum}
11/1492	• • • • {by run-time replication performed by the	11/1695	• • • {which are operating with time diversity}
	application software}	11/18	• • using passive fault-masking of the redundant
11/1494	· · · · { N-modular type}		circuits {(error detection by comparing the
11/1497	 . • {Details of time redundant execution on a single processing unit} 		output of redundant processing systems with continued operation after detection of the error
11/16	• Error detection or correction of the data by		<u>G06F 11/165</u>)}
11,10	redundancy in hardware	11/181	{Eliminating the failing redundant
11/1604	• • • {where the fault affects the clock signals of		component}
11/1004	a processing unit and the redundancy is at or	11/182	• • • {based on mutual exchange of the output
	within the level of clock signal generation		between redundant processing components}
	hardware}	11/183	• • • {by voting, the voting not being performed
11/1608	• • {Error detection by comparing the output		by the redundant components}
11/1000	signals of redundant hardware (G06F 11/1629,	11/184	• • • • { where the redundant components
	G06F 11/1666 take precedence; error detection	11/104	implement processing functionality}
	or correction in information storage based on	11/185	• • • • • • {and the voting is itself performed
	relative movement between record carrier and	11/103	redundantly}
	transducer G11B 20/18; checking static stores	11/186	• • • • {Passive fault masking when reading
	for correct operation G11C 29/00; for logic	11/100	multiple copies of the same data}
	circuits <u>H03K 19/003</u> , <u>H03K 19/007</u> ; for pulse	11/187	• • • • {Voting techniques}
	counters or frequency dividers <u>H03K 21/40</u>)}		
11/1612	• • • { where the redundant component is	11/188	• • • • {where exact match is not required}
11/1012	persistent storage}	11/20	using active fault-masking, e.g. by switching
11/1616	• • • {where the redundant component is an I/O		out faulty elements or by switching in spare
11/1010	device or an adapter therefor}	11/2002	elements
11/162	{Displays}	11/2002	• • • • {where interconnections or communication
11/1625	{ In communications, e.g. transmission,		control functionality are redundant (flexible
11/1023	interfaces}		arrangements for bus networks involving redundancy <u>H04L 12/40176</u>)}
11/1629	• • Error detection by comparing the output of		
		11/2005	• • • • {using redundant communication
	redundant processing systems}		controllers}
11/1633	redundant processing systems} {using mutual exchange of the output	11/2005 11/2007	
11/1633	redundant processing systems} {using mutual exchange of the output between the redundant processing		controllers}
	redundant processing systems} {using mutual exchange of the output between the redundant processing components}	11/2007	controllers} {using redundant communication media}
11/1633 11/1637	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in	11/2007 11/201	controllers} {using redundant communication media} {between storage system components}
	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in one or some but not all of the redundant	11/2007 11/201	controllers} {using redundant communication media} {between storage system components} {and using different communication
11/1637	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components}	11/2007 11/201 11/2012	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols}
	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by	11/2007 11/201 11/2012	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply
11/1637 11/1641	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components}	11/2007 11/201 11/2012 11/2015	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)}
11/1637	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant	11/2007 11/201 11/2012 11/2015	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control
11/1637 11/1641	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components}	11/2007 11/201 11/2012 11/2015	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant
11/1637 11/1641	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant	11/2007 11/201 11/2012 11/2015	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control
11/1637 11/1641 11/1645	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant hardware} { with continued operation after detection of the error}	11/2007 11/201 11/2012 11/2015	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant
11/1637 11/1641 11/1645	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant hardware} { with continued operation after detection of the error} { where the output of only one of the	11/2007 11/201 11/2012 11/2015 11/2017	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)}
11/1637 11/1641 11/1645 11/165	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant hardware} { with continued operation after detection of the error} { where the output of only one of the redundant processing components can drive	11/2007 11/201 11/2012 11/2015 11/2017	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant
11/1637 11/1641 11/1645 11/165	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant hardware} { with continued operation after detection of the error} { where the output of only one of the	11/2007 11/201 11/2012 11/2015 11/2017	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant (redundant communication control
11/1637 11/1641 11/1645 11/165	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant hardware} { with continued operation after detection of the error} { where the output of only one of the redundant processing components can drive	11/2007 11/201 11/2012 11/2015 11/2017	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)}
11/1637 11/1641 11/1645 11/165 11/1654	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant hardware} { with continued operation after detection of the error} { where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O}	11/2007 11/201 11/2012 11/2015 11/2017 11/202	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)} {Failover techniques}
11/1637 11/1641 11/1645 11/165 11/1654	redundant processing systems} { using mutual exchange of the output between the redundant processing components} { using additional compare functionality in one or some but not all of the redundant processing components} { where the comparison is not performed by the redundant processing components} { and the comparison itself uses redundant hardware} { with continued operation after detection of the error} { where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} { Data re-synchronization of a redundant	11/2007 11/201 11/2012 11/2015 11/2017	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)} {Failover techniques} {using centralised failover control
11/1637 11/1641 11/1645 11/165 11/1654	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in one or some but not all of the redundant processing components} {where the comparison is not performed by the redundant processing components} {and the comparison itself uses redundant hardware} {with continued operation after detection of the error} {where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} {Data re-synchronization of a redundant component, or initial sync of replacement,	11/2007 11/201 11/2012 11/2015 11/2017 11/202 11/202	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2005, redundant storage control functionality G06F 11/2009)} {Failover techniques} {using centralised failover control functionality}
11/1637 11/1641 11/1645 11/165 11/1654 11/1658	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in one or some but not all of the redundant processing components} {where the comparison is not performed by the redundant processing components} {and the comparison itself uses redundant hardware} {with continued operation after detection of the error} {where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} {Data re-synchronization of a redundant component, or initial sync of replacement, additional or spare unit}	11/2007 11/201 11/2012 11/2015 11/2017 11/202	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {Where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {Where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2005, redundant storage control functionality G06F 11/2009)} {Failover techniques} {using centralised failover control functionality} {eliminating a faulty processor or
11/1637 11/1641 11/1645 11/165 11/1654 11/1658	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in one or some but not all of the redundant processing components} {where the comparison is not performed by the redundant processing components} {and the comparison itself uses redundant hardware} {with continued operation after detection of the error} {where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} {Data re-synchronization of a redundant component, or initial sync of replacement, additional or spare unit} {the resynchronized component or unit	11/2007 11/201 11/2012 11/2015 11/2017 11/202 11/202 11/2023 11/2025	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {Where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {Where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)} {Failover techniques} {using centralised failover control functionality} {eliminating a faulty processor or activating a spare}
11/1637 11/1641 11/1645 11/165 11/1654 11/1658	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in one or some but not all of the redundant processing components} {where the comparison is not performed by the redundant processing components} {and the comparison itself uses redundant hardware} {with continued operation after detection of the error} {where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} {Data re-synchronization of a redundant component, or initial sync of replacement, additional or spare unit} {the resynchronized component or unit being a persistent storage device (re-	11/2007 11/201 11/2012 11/2015 11/2017 11/202 11/202 11/2023 11/2028 11/203	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {Where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {Where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)} {Failover techniques} {using centralised failover control functionality} {eliminating a faulty processor or activating a spare} {using migration}
11/1637 11/1641 11/1645 11/165 11/1654 11/1658	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in one or some but not all of the redundant processing components} {where the comparison is not performed by the redundant processing components} {and the comparison itself uses redundant hardware} {with continued operation after detection of the error} {where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} {Data re-synchronization of a redundant component, or initial sync of replacement, additional or spare unit} {the resynchronized component or unit being a persistent storage device (resynchronization of failed mirror storage	11/2007 11/201 11/2012 11/2015 11/2017 11/202 11/202 11/2023 11/2025 11/203 11/203 11/203	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)} {Failover techniques} {using centralised failover control functionality} {eliminating a faulty processor or activating a spare} {using migration} {switching over of hardware resources}
11/1637 11/1641 11/1645 11/165 11/1654 11/1658	redundant processing systems} {using mutual exchange of the output between the redundant processing components} {using additional compare functionality in one or some but not all of the redundant processing components} {where the comparison is not performed by the redundant processing components} {and the comparison itself uses redundant hardware} {with continued operation after detection of the error} {where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} {Data re-synchronization of a redundant component, or initial sync of replacement, additional or spare unit} {the resynchronized component or unit being a persistent storage device (resynchronization of failed mirror storage G06F 11/2082; rebuild or reconstruction of	11/2007 11/201 11/2012 11/2015 11/2017 11/202 11/202 11/2025 11/2028 11/203 11/203 11/2035	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {Where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2005; redundant (redundant communication control functionality G06F 11/2005, redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)} {Failover techniques} {using centralised failover control functionality} {eliminating a faulty processor or activating a spare} {using migration} {switching over of hardware resources} {without idle spare hardware}
11/1637 11/1641 11/1645 11/165 11/1654 11/1658 11/1662	redundant processing systems} { using mutual exchange of the output between the redundant processing components } { using additional compare functionality in one or some but not all of the redundant processing components } { where the comparison is not performed by the redundant processing components } { and the comparison itself uses redundant hardware } { with continued operation after detection of the error } { where the output of only one of the redundant processing components can drive the attached hardware, e.g. memory or I/O} { Data re-synchronization of a redundant component, or initial sync of replacement, additional or spare unit } { the resynchronized component or unit being a persistent storage device (resynchronization of failed mirror storage G06F 11/2082; rebuild or reconstruction of parity RAID storage G06F 11/1008)}	11/2007 11/201 11/2012 11/2015 11/2017 11/202 11/202 11/2023 11/2025 11/203 11/203 11/203	controllers} {using redundant communication media} {between storage system components} {and using different communication protocols} {Redundant power supplies (power supply failure G06F 1/30)} {where memory access, memory control or I/O control functionality is redundant (redundant communication control functionality G06F 11/2005; redundant storage control functionality G06F 11/2089)} {where processing functionality is redundant (redundant communication control functionality G06F 11/2005, redundant storage control functionality G06F 11/2005, redundant storage control functionality G06F 11/2089)} {Failover techniques} {using centralised failover control functionality} {eliminating a faulty processor or activating a spare} {using migration} {switching over of hardware resources}

11/2041			
11/2071	• • • • { with more than one idle spare processing	11/2273	• • {Test methods}
	component}	11/2284	• • {by power-on test, e.g. power-on self test
11/2043	• • • • { where the redundant components share a		[POST]}
	common memory address space}	11/2289	• • {by configuration test}
11/2046	{ where the redundant components share	11/2294	• • {by remote test}
	persistent storage (G06F 11/2043 takes	11/24	Marginal checking {or other specified testing}
	precedence)}	11/24	
11/2048	• • • • { where the redundant components share		methods not covered by <u>G06F 11/26</u> , e.g. race
11/2046			tests}
	neither address space nor persistent	11/25	• Testing of logic operation, e.g. by logic analysers
	storage}	11/26	 Functional testing
11/2051	• • • • {in regular structures}	11/261	• • • {by simulating additional hardware, e.g. fault
11/2053	• • • { where persistent mass storage functionality		simulation}
	or persistent mass storage control	11/263	Generation of test inputs, e.g. test vectors,
	functionality is redundant (error detection or	,	patterns or sequences {; with adaptation of the
	correction in information storage based on		tested hardware for testability with external
	relative movement between record carrier		testers}
	and transducer G11B 20/18)	11/2635	• • • {using a storage for the test inputs, e.g. test
11/2056	• • • • {by mirroring}	11/2033	
11/2058	• • • • {using more than 2 mirrored copies}		ROM, script files}
		11/267	Reconfiguring circuits for testing, e.g. LSSD,
11/2061	• • • • {combined with de-clustering of data}		partitioning
11/2064	• • • • • {while ensuring consistency}	11/27	Built-in tests
11/2066	• • • • • {Optimisation of the communication	11/273	Tester hardware, i.e. output processing circuits
	load}		$\{(\underline{G06F 11/263} \text{ takes precedence})\}$
11/2069	{Management of state, configuration or	11/2733	{Test interface between tester and unit under
	failover}	11/2/33	test}
11/2071	{using a plurality of controllers}	11/2736	,
11/2074	{Asynchronous techniques}		• • • { using a dedicated service processor for test}
		11/277	with comparison between actual response
11/2076	{Synchronous techniques}		and known fault-free response
11/2079	• • • • • {Bidirectional techniques}	11/28	 by checking the correct order of processing
11/2082	• • • • • {Data synchronisation}		(<u>G06F 11/08</u> - <u>G06F 11/26</u> take precedence;
11/2084	• • • • • {on the same storage unit}		monitoring patterns of pulse trains <u>H03K 5/19</u>)
11/2087	• • • • • { with a common controller }	11/30	 Monitoring
11/2089	{Redundant storage control functionality}	11/3003	• • {Monitoring arrangements specially adapted
11/2092	{Techniques of failing over between		to the computing system or computing system
11/20/2	control units}		component being monitored}
	· · · · · · · · · · · · · · · · · · ·		
11/2004		11/3006	where the computing system is distributed
11/2094	• • • • {Redundant storage or storage space	11/3006	• • • { where the computing system is distributed,
	(G06F 11/2056 takes precedence)}	11/3006	e.g. networked systems, clusters,
11/2094 11/2097	(G06F 11/2056 takes precedence)} {maintaining the standby controller/	11/3006	e.g. networked systems, clusters, multiprocessor systems (multiprogramming
	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re-	11/3006	e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements <u>G06F 9/46</u> ; allocation of
	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and		e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements <u>G06F 9/46</u> ; allocation of resources <u>G06F 9/50</u>)}
	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re-	11/3006	e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements <u>G06F 9/46</u> ; allocation of resources <u>G06F 9/50</u>)} • • {where the computing system is a virtual
	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and		 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system is a virtual computing platform, e.g. logically partitioned
11/2097	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer		 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533;
11/2097	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or		 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system is a virtual computing platform, e.g. logically partitioned
11/2097	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing		 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533;
11/2097	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware	11/301	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • { where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)}
11/2097 11/22 11/2205	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware being tested}	11/301	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded
11/2097	(G06F 11/2056 takes precedence)} • • • {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} • Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing • {using arrangements specific to the hardware being tested} • • {to test buses, lines or interfaces, e.g. stuck-at	11/301	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain
11/2097 11/22 11/2205 11/221	 (G06F 11/2056 takes precedence)} • • • {maintaining the standby controller/ processing unit updated (initialisation or resynchronisation thereof G06F 11/1658 and subgroups)} • Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing • {using arrangements specific to the hardware being tested} • • {to test buses, lines or interfaces, e.g. stuck-at or open line faults} 	11/301	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive
11/2097 11/22 11/2205 11/221 11/2215	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits}	11/301	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of
11/2097 11/22 11/2205 11/221	 (G06F 11/2056 takes precedence)} • • • {maintaining the standby controller/ processing unit updated (initialisation or resynchronisation thereof G06F 11/1658 and subgroups)} • Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing • {using arrangements specific to the hardware being tested} • • {to test buses, lines or interfaces, e.g. stuck-at or open line faults} 	11/301 11/3013	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)}
11/2097 11/22 11/2205 11/221 11/2215	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units}	11/301	e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • Where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • Where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} • Where the computing system is implementing
11/2097 11/22 11/2205 11/221 11/2215	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral	11/301 11/3013	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • Where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • Where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} • Where the computing system is implementing multitasking (multiprogramming arrangements
11/2097 11/22 11/2205 11/221 11/2215 11/2221	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units}	11/301 11/3013	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test interrupt circuits}	11/301 11/3013 11/3017	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)}
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231 11/2236	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test interrupt circuits} {to test CPU or processors}	11/301 11/3013	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} {where the computing system component is a
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one	11/3013 11/3017 11/302	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} • • {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system component is a software system}
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231 11/2236	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master	11/301 11/3013 11/3017	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • (where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • (where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} • • (where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • (where the computing system component is a software system) • • (where the computing system component is a
11/2097 11/22 11/2205 11/221 11/2221 11/2226 11/2231 11/2236 11/2242	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master (G06F 11/2736 takes precedence)}	11/301 11/3013 11/3017 11/302 11/3024	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • (where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} . • (where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} . • (where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • (where the computing system component is a software system) . • (where the computing system component is a central processing unit [CPU])
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231 11/2236	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master (G06F 11/2736 takes precedence)} . {Verification or detection of system hardware	11/3013 11/3017 11/302	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • (where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • (where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} • • (where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • (where the computing system component is a software system) • • (where the computing system component is a
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231 11/2236 11/2242	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing . {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master (G06F 11/2736 takes precedence)} . {Verification or detection of system hardware configuration}	11/301 11/3013 11/3017 11/302 11/3024	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} . • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} . • {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • {where the computing system component is a software system} . • {where the computing system component is a central processing unit [CPU]}
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2231 11/2236 11/2242 11/2247 11/2252	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master (G06F 11/2736 takes precedence)} {Verification or detection of system hardware configuration} . {using fault dictionaries}	11/301 11/3013 11/3017 11/302 11/3024	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} • • {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system component is a software system} • • {where the computing system component is a central processing unit [CPU]} • • {where the computing system component is a
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231 11/2236 11/2242 11/2247 11/2252 11/2257	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master (G06F 11/2736 takes precedence)} {Verification or detection of system hardware configuration} . {using fault dictionaries} . {using expert systems}	11/301 11/3013 11/3017 11/302 11/3024 11/3027	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} • • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} • • {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} • • {where the computing system component is a software system} • • {where the computing system component is a central processing unit [CPU]} • • {where the computing system component is a bus}
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2231 11/2236 11/2242 11/2247 11/2252	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master (G06F 11/2736 takes precedence)} {Verification or detection of system hardware configuration} . {using fault dictionaries}	11/301 11/3013 11/3017 11/302 11/3024 11/3027	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} . • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} . • {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • {where the computing system component is a software system} . • {where the computing system component is a bus} . • {where the computing system component is a bus} . • {where the computing system component is a
11/2097 11/22 11/2205 11/221 11/2215 11/2221 11/2226 11/2231 11/2236 11/2242 11/2247 11/2252 11/2257	(G06F 11/2056 takes precedence)} {maintaining the standby controller/ processing unit updated (initialisation or re- synchronisation thereof G06F 11/1658 and subgroups)} . Detection or location of defective computer hardware by testing during standby operation or during idle time, e.g. start-up testing {using arrangements specific to the hardware being tested} {to test buses, lines or interfaces, e.g. stuck-at or open line faults} {to test error correction or detection circuits} {to test input/output devices or peripheral units} {to test ALU} {to test CPU or processors} {in multi-processor systems, e.g. one processor becoming the test master (G06F 11/2736 takes precedence)} {Verification or detection of system hardware configuration} . {using fault dictionaries} . {using expert systems}	11/301 11/3013 11/3017 11/302 11/3024 11/3027	 e.g. networked systems, clusters, multiprocessor systems (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • {where the computing system is a virtual computing platform, e.g. logically partitioned systems (virtual machines G06F 9/45533; logical partitioning of resources G06F 9/5077)} . • {where the computing system is an embedded system, i.e. a combination of hardware and software dedicated to perform a certain function in mobile devices, printers, automotive or aircraft systems (testing or monitoring of control systems or parts thereof G05B 23/02)} . • {where the computing system is implementing multitasking (multiprogramming arrangements G06F 9/46; allocation of resources G06F 9/50)} . • {where the computing system component is a software system} . • {where the computing system component is a bus} . • {where the computing system component is a bus} . • {where the computing system component is a

11/3034	 • • { where the computing system component is a storage system, e.g. DASD based or network based (digital input from or digital output to record carriers <u>G06F 3/06</u>; digital recording or reproducing <u>G11B 20/18</u>; for distributed 	11/3075 { the data filtering being achieved in order to maintain consistency among the monitored data, e.g. ensuring that the monitored data belong to the same timeframe, to the same system or component}
	storage of data in networks, e.g. transport arrangements for network file system [NFS], storage area networks [SAN] or network	11/3079 • • • • { the data filtering being achieved by reporting only the changes of the monitored data }
11/3037	 attached storage [NAS], H04L 67/1097)} • { where the computing system component is a memory, e.g. virtual memory, cache (accessing, 	11/3082 {the data filtering being achieved by aggregating or compressing the monitored data}
	addressing or allocating within memory systems or architectures <u>G06F 12/00</u> ; checking stores for correct operation <u>G11C 29/00</u>)}	11/3086 • • • {where the reporting involves the use of self describing data formats, i.e. metadata, markup languages, human readable formats}
11/3041	• • • {where the computing system component is an input/output interface (interconnection of, or transfer of information or other signals between, memories, input/output devices or central processing units G06F 13/00)}	11/3089 • • {Monitoring arrangements determined by the means or processing involved in sensing the monitored data, e.g. interfaces, connectors, sensors, probes, agents (software debugging using additional hardware using a specific debug
11/3044	• • • {where the computing system component is the mechanical casing of the computing system}	interface <u>G06F 11/3656</u> ; performance evaluation by tracing or monitoring <u>G06F 11/3466</u>)}
11/3048	• • • {where the topology of the computing system or computing system component explicitly influences the manifesting extinity of against	11/3093 • • • {Configuration details thereof, e.g. installation, enabling, spatial arrangement of the probes}
11/3051	influences the monitoring activity, e.g. serial, hierarchical systems} • {Monitoring arrangements for monitoring the	11/3096 • • • {wherein the means or processing minimize the use of computing system or of computing system component resources, e.g. non-intrusive
11/3001	configuration of the computing system or of the computing system component, e.g. monitoring the presence of processing resources, peripherals, I/O links, software programs (verification or	monitoring which minimizes the probe effect: sniffing, intercepting, indirectly deriving the monitored data from other directly available data}
	detection of system hardware configuration G06F 11/2247)}	11/32 • with visual {or acoustical} indication of the functioning of the machine
11/3055	• • {Monitoring arrangements for monitoring the status of the computing system or of the	11/321 • • • {Display for diagnostics, e.g. diagnostic result display, self-test user interface}
	computing system component, e.g. monitoring if the computing system is on, off, available, not available (error or fault processing without	11/322 {Display of waveforms, e.g. of logic analysers (G06F 11/323 takes precedence)}
	redundancy <u>G06F 11/0703</u> ; error detection or	11/323 {Visualisation of programs or trace data}
	correction by redundancy in data representation	11/324 {Display of status information}
	G06F 11/08; error detection or correction by	11/325 {by lamps or LED's}
	redundancy in operation G06F 11/14; error	11/326 {for error or online/offline status}
	detection or correction by redundancy in	11/327 {Alarm or error message display}
11/3058	hardware <u>G06F 11/16</u>)} {Monitoring arrangements for monitoring	11/328 {Computer systems status display (G06F 11/327 takes precedence)}
	environmental properties or parameters of the computing system or of the computing system component, e.g. monitoring of power, currents, temperature, humidity, position, vibrations	11/34 • Recording or statistical evaluation of computer activity, e.g. of down time, of input/output operation {; Recording or statistical evaluation of user activity, e.g. usability assessment}
	(thermal management in cooling arrangements of	11/3404 {for parallel or distributed programming}
	a computing system <u>G06F 1/206</u>)}	11/3409 {for performance assessment}
11/3062	• • • {where the monitored property is the power consumption (power management in a	11/3414 {Workload generation, e.g. scripts, playback}
11/2065	computing system G06F 1/3203)}	11/3419 {by assessing time}
11/3065	• • {Monitoring arrangements determined by the means or processing involved in reporting the monitored data (error or fault reporting or logging)	11/3423 { where the assessed time is active or idle time}
	G06F 11/0766)}	11/3428 {Benchmarking}
11/3068	• • {where the reporting involves data format conversion}	11/3433 {for load management (allocation of a server based on load conditions <u>G06F 9/505</u> ; load rebalancing <u>G06F 9/5083</u> ; redistributing
11/3072	• • • {where the reporting involves data filtering, e.g. pattern matching, time or event triggered,	the load in a network by a load balancer H04L 67/1029)}
	adaptive or policy-based reporting}	11/3438 • • • { monitoring of user actions (tracking the activity of the user <u>H04L 67/535</u>)}
		11/3442 { for planning or managing the needed capacity }

11/3447	• • • {Performance evaluation by modeling}	12/023	• • {Free address space management}
11/3452	• • • {Performance evaluation by statistical	12/0238	{Memory management in non-volatile
	analysis}		memory, e.g. resistive RAM or ferroelectric
11/3457	• • {Performance evaluation by simulation}		memory}
		12/0246	* *
11/3461	{Trace driven simulation}	12/0240	• • • • {in block erasable memory, e.g. flash
11/3466	• • • {Performance evaluation by tracing or		memory}
	monitoring}	12/0253	• • • • {Garbage collection, i.e. reclamation of
11/3471	{Address tracing}		unreferenced memory}
11/3476	{Data logging (<u>G06F 11/14, G06F 11/2205</u>	12/0261	• • • • {using reference counting}
11/31/0	take precedence)}	12/0269	{Incremental or concurrent garbage
11/240	*	12/0209	collection, e.g. in real-time systems
11/348	• • • {Circuit details, i.e. tracer hardware}		(G06F 12/0261 takes precedence)
11/3485	• • • {for I/O devices}	10/0076	
11/349	• • • {for interfaces, buses}	12/0276	{Generational garbage collection}
11/3495	{for systems}	12/0284	{Multiple user address space allocation, e.g.
11/36	 Prevention of errors by analysis, debugging or 		using different base addresses (interprocessor
	testing of software		communication <u>G06F 15/163</u>)}
11/3604	Analysis of software for verifying properties of	12/0292	• • • {using tables or multilevel address translation
11/3004			means (G06F 12/023 takes precedence;
	programs (testing of software G06F 11/3668)		address translation in virtual memory systems
11/3608	• • • {using formal methods, e.g. model checking,		G06F 12/10)}
	abstract interpretation (theorem proving	12/04	Addressing variable-length words or parts of
	<u>G06N 5/013</u>)}	12/04	
11/3612	• • • {by runtime analysis (performance monitoring	10/04	words
	G06F 11/3466)}	12/06	Addressing a physical block of locations, e.g.
11/3616	• • · {using software metrics}		base addressing, module addressing, memory
11/362	Debugging of software		dedication (G06F 12/08 takes precedence)
			NOTE
11/3624	• • • {by performing operations on the source code,		
	e.g. via a compiler}		This group is limited to Module addressing
11/3628	• • • {of optimised code (optimisation <u>G06F 8/443</u>)}		or allocation; base addressing is classified in
11/3632	• (of specific synchronisation aspects)		G06F 12/0223.
11/3636	• • {by tracing the execution of the program}		
11/364	• • • {tracing values on a bus}	12/0607	{Interleaved addressing}
11/3644	• • {by instrumenting at runtime}	12/0615	• • {Address space extension}
11/3648	{using additional hardware}	12/0623	• • • {for memory modules}
	{in-circuit-emulation [ICE] arrangements}	12/063	{for I/O modules, e.g. memory mapped I/O
11/3652	• • • {in-circuit-emulation ICE arrangements}		
11/3656	• • • {using a specific debug interface}		(I/O protocol <u>G06F 13/42</u>)}
	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes	12/0638	(I/O protocol <u>G06F 13/42</u>)} {Combination of memories, e.g. ROM and
11/3656	• • • {using a specific debug interface}		(I/O protocol G06F 13/42)} • • • {Combination of memories, e.g. ROM and RAM such as to permit replacement or
11/3656	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes		 (I/O protocol G06F 13/42)} • • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by
11/3656 11/366 11/3668	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software 		 (I/O protocol G06F 13/42)} • (Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of
11/3656 11/366 11/3668 11/3672	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} 		(I/O protocol G06F 13/42)} • • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking
11/3656 11/366 11/3668 11/3672 11/3676	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} 		(I/O protocol G06F 13/42)} • • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by
11/3656 11/366 11/3668 11/3672	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test 	12/0638	(I/O protocol <u>G06F 13/42</u>)} • • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction <u>G06F 9/26</u> ; masking faults in memories by using spares or by reconfiguring <u>G11C 29/70</u>)}
11/3656 11/366 11/3668 11/3672 11/3676 11/368	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} 		(I/O protocol G06F 13/42)} • • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by
11/3656 11/366 11/3668 11/3672 11/3676	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test 	12/0638	(I/O protocol <u>G06F 13/42</u>)} • • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction <u>G06F 9/26</u> ; masking faults in memories by using spares or by reconfiguring <u>G11C 29/70</u>)}
11/3656 11/366 11/3668 11/3672 11/3676 11/368	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} 	12/0638 12/0646 12/0653	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment}
11/3656 11/366 11/3668 11/3672 11/3676 11/368	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test 	12/0638 12/0646 12/0653 12/0661	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {and decentralised selection}
11/3656 11/366 11/3668 11/3672 11/3676 11/368	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} 	12/0638 12/0646 12/0653 12/0661 12/0669	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {and decentralised address assignment} . • {with decentralised address assignment}
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} 	12/0638 12/0646 12/0653 12/0669 12/0676	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • • {with centralised address assignment} . • • {with decentralised address assignment} . • • {the address being position dependent}
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} 	12/0638 12/0646 12/0653 12/0661 12/0669	(I/O protocol G06F 13/42)} • Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} • Configuration or reconfiguration} • With centralised address assignment} • And decentralised selection} • With decentralised address assignment}
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} 	12/0638 12/0646 12/0653 12/0669 12/0676	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • • {with centralised address assignment} . • • {with decentralised address assignment} . • • {the address being position dependent}
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} . Environments for analysis, debugging or testing 	12/0638 12/0646 12/0653 12/0669 12/0676	(I/O protocol G06F 13/42)} • Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} • Configuration or reconfiguration} • With centralised address assignment} • And decentralised selection} • With decentralised address assignment}
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} 	12/0638 12/0646 12/0653 12/0669 12/0676	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • {with enddress being position dependent} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow
11/3656 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 • • • {using a specific debug interface} • • {using diagnostics (G06F 11/0703 takes precedence)} • • Testing of software • • {Test management} • • {for coverage analysis} • • • {for test version control, e.g. updating test cases to a new software version} • • • {for test design, e.g. generating new test cases} • • • {for test execution, e.g. scheduling of test suites} • • • {for test results analysis} • • • {Methods or tools to render software testable} • • Environments for analysis, debugging or testing of software 	12/0638 12/0646 12/0653 12/0661 12/0669 12/0676 12/0684	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {the address being position dependent} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection}
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} . Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} . Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory	12/0638 12/0646 12/0653 12/0661 12/0669 12/0676 12/0684 12/0692	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {the address being position dependent} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing}
11/3656 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} . Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or	12/0638 12/0646 12/0653 12/0661 12/0669 12/0676 12/0684	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {the address being position dependent} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g.
11/3656 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {for test results analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage 	12/0638 12/0646 12/0653 12/0661 12/0669 12/0676 12/0684 12/0692 12/08	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . in hierarchically structured memory systems, e.g. virtual memory systems
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) 	12/0638 12/0646 12/0653 12/0661 12/0669 12/0676 12/0684 12/0692	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the
11/3656 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program 	12/0638 12/0646 12/0653 12/0661 12/0669 12/0676 12/0684 12/0692 12/08	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} . Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for 	12/0638 12/0646 12/0653 12/0669 12/0676 12/0684 12/0692 12/08 12/0802	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) 	12/0638 12/0646 12/0653 12/0661 12/0669 12/0676 12/0684 12/0692 12/08	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches . • with main memory updating (G06F 12/0806
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} . Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for 	12/0638 12/0646 12/0653 12/0669 12/0676 12/0684 12/0692 12/0802 12/0804	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches . • with main memory updating (G06F 12/0806 takes precedence)
11/3656 11/366 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3696 11/3696 11/3698	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) 	12/0638 12/0646 12/0653 12/0669 12/0676 12/0684 12/0692 12/08 12/0802	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches . • with main memory updating (G06F 12/0806
11/3656 11/3666 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698 12/00	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) {with multidimensional access, e.g. row/column, matrix} 	12/0638 12/0646 12/0653 12/0669 12/0676 12/0684 12/0692 12/0802 12/0804	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {with decentralised address assignment} . • • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches . • with main memory updating (G06F 12/0806 takes precedence)
11/3656 11/3666 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698 12/00	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) . {with multidimensional access, e.g. row/column, matrix} . {with look ahead addressing means} 	12/0638 12/0646 12/0653 12/0669 12/0676 12/0684 12/0692 12/0802 12/0804	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {the address being position dependent} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches . • with main memory updating (G06F 12/0806 takes precedence) . • Multiuser, multiprocessor or multiprocessing cache systems
11/3656 11/3666 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698 12/00	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} . Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) {with multidimensional access, e.g. row/column, matrix} {with look ahead addressing means} {User address space allocation, e.g. contiguous or 	12/0638 12/0646 12/0653 12/0669 12/0676 12/0684 12/0692 12/0802 12/0804 12/0806	(I/O protocol G06F 13/42)} • Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} • Configuration or reconfiguration} • With centralised address assignment} • And decentralised selection} • With decentralised address assignment} • Multiconfiguration, e.g. presence or absence of unit detected by addressing, overflow detection} • Multiconfiguration, e.g. local and global addressing} • in hierarchically structured memory systems, e.g. virtual memory systems • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches • With main memory updating (G06F 12/0806 takes precedence) • Multiuser, multiprocessor or multiprocessing cache systems • With cache invalidating means
11/3656 11/3666 11/3668 11/3672 11/3676 11/368 11/3684 11/3688 11/3692 11/3696 11/3698 12/00	 {using a specific debug interface} {using diagnostics (G06F 11/0703 takes precedence)} Testing of software {Test management} {for coverage analysis} {for test version control, e.g. updating test cases to a new software version} {for test design, e.g. generating new test cases} {for test execution, e.g. scheduling of test suites} {for test results analysis} {Methods or tools to render software testable} Environments for analysis, debugging or testing of software Accessing, addressing or allocating within memory systems or architectures (digital input from, or digital output to record carriers, e.g. to disk storage units, G06F 3/06) . Addressing or allocation; Relocation (program address sequencing G06F 9/00; arrangements for selecting an address in a digital store G11C 8/00) . {with multidimensional access, e.g. row/column, matrix} . {with look ahead addressing means} 	12/0638 12/0646 12/0653 12/0669 12/0676 12/0684 12/0692 12/0802 12/0804 12/0806	 (I/O protocol G06F 13/42)} . • {Combination of memories, e.g. ROM and RAM such as to permit replacement or supplementing of words in one module by words in another module (address formation of the next microinstruction G06F 9/26; masking faults in memories by using spares or by reconfiguring G11C 29/70)} . • {Configuration or reconfiguration} . • {with centralised address assignment} . • {with decentralised selection} . • {the address being position dependent} . • {with feedback, e.g. presence or absence of unit detected by addressing, overflow detection} . • {Multiconfiguration, e.g. local and global addressing} . • in hierarchically structured memory systems, e.g. virtual memory systems . • Addressing of a memory level in which the access to the desired data or data block requires associative addressing means, e.g. caches . • with main memory updating (G06F 12/0806 takes precedence) . • Multiuser, multiprocessor or multiprocessing cache systems

10/0012		10/0007
12/0813	with a network or matrix configuration	12/0897 with two or more cache hierarchy levels
12/0815	Cache consistency protocols	(with multilevel cache hierarchies
12/0817	• • • • using directory methods	<u>G06F 12/0811</u>)
12/082	{Associative directories	12/10 Address translation
	(G06F 12/0822 takes precedence)	12/1009 using page tables, e.g. page table structures
12/0822	(Copy directories (local copy tags for	12/1018 involving hashing techniques, e.g. inverted
12,0022	implementing a bus snooping protocol	page tables
	G06F 12/0831)}	12/1027 using associative or pseudo-associative
12/0024		
12/0824	(Distributed directories, e.g. linked	address translation means, e.g. translation
	lists of caches}	look-aside buffer [TLB]
12/0826	• • • • • {Limited pointers directories; State-	12/1036 for multiple virtual address spaces, e.g.
	only directories without pointers}	segmentation (G06F 12/1045 takes
12/0828	• • • • • { with concurrent directory accessing,	precedence)
	i.e. handling multiple concurrent	12/1045 associated with a data cache
	coherency transactions}	12/1054 {the data cache being concurrently
12/0831	• • • • • using a bus scheme, e.g. with bus	physically addressed}
12/0631		
4.00000	monitoring or watching means	12/1063 {the data cache being concurrently
12/0833	• • • • • • {in combination with broadcast means	virtually addressed}
	(e.g. for invalidation or updating)}	12/1072 Decentralised address translation, e.g. in
12/0835	{for main memory peripheral accesses	distributed shared memory systems
	(e.g. I/O or DMA)}	12/1081 for peripheral access to main memory, e.g.
12/0837	with software control, e.g. non-	direct memory access [DMA]
12,003,	cacheable data	12/109 for multiple virtual address spaces, e.g.
12/084		segmentation (G06F 12/1036 takes
	with a shared cache	precedence)
12/0842	• • • for multiprocessing or multitasking	
12/0844	• • • Multiple simultaneous or quasi-simultaneous	12/12 Replacement control
	cache accessing	12/121 using replacement algorithms
12/0846	Cache with multiple tag or data arrays	12/122 of the least frequently used [LFU] type,
	being simultaneously accessible	e.g. with individual count value
12/0848	{Partitioned cache, e.g. separate	12/123 with age lists, e.g. queue, most recently
12/0040	instruction and operand caches}	used [MRU] list or least recently used
12/0051		[LRU] list
12/0851	• • • • {Cache with interleaved addressing}	
12/0853	Cache with multiport tag or data arrays	12/124 {being minimized, e.g. non MRU}
12/0855	Overlapped cache accessing, e.g. pipeline	12/125 {being generated by decoding an array
	(G06F 12/0846 takes precedence)	or storage}
12/0857	{by multiple requestors}	12/126 with special data handling, e.g. priority
12/0859	• • • • { with reload from main memory }	of data or instructions, handling errors or
12/0862	with prefetch	pinning
	-	12/127 using additional replacement algorithms
12/0864	using pseudo-associative means, e.g. set-	12/128 adapted to multidimensional cache
	associative or hashing	systems, e.g. set-associative, multicache,
12/0866	• • • for peripheral storage systems, e.g. disk	•
	cache	multiset or multilevel
12/0868	Data transfer between cache memory and	12/14 • Protection against unauthorised use of memory {or
	other subsystems, e.g. storage devices or	access to memory}
	host systems	12/1408 • • {by using cryptography (for digital transmission
12/0971	Allocation or management of cache space	H04L 9/00)}
12/0871		12/1416 {by checking the object accessibility, e.g. type of
12/0873	Mapping of cache memory to specific	access defined by the memory independently of
	storage devices or parts thereof	subject rights (G06F 12/1458 takes precedence)
12/0875	with dedicated cache, e.g. instruction or stack	
12/0877	Cache access modes	12/1425 {the protection being physical, e.g. cell, word,
12/0879	Burst mode	block}
		12/1433 {for a module or a part of a module}
12/0882	Page mode	12/1441 { for a range }
12/0884	• • • • Parallel mode, e.g. in parallel with main	12/145 {the protection being virtual, e.g. for virtual
	memory or CPU	blocks or segments before a translation
12/0886	Variable-length word access	mechanism}
12/0888	using selective caching, e.g. bypass	,
12/0891	using clearing, invalidating or resetting	12/1458 • • {by checking the subject access rights}
12/00/1	means	12/1466 • • • {Key-lock mechanism}
10/0000		12/1475 {in a virtual system, e.g. with translation
12/0893	Caches characterised by their organisation or	means}
	structure	12/1483 {using an access-table, e.g. matrix or list}
12/0895	• • • • of parts of caches, e.g. directory or tag	12/1491 • • • {using an access-table, e.g. matrix of list}
	array	
		privilege levels, memory rings}

12/16	 Protection against loss of memory contents 	13/225	• • • { with priority control }
	{(contains no material, see G06F 11/00)}	13/24	• • using interrupt (<u>G06F 13/32</u> takes precedence)
13/00	Interconnection of, or transfer of information or	13/26	with priority control
13/00	other signals between, memories, input/output	13/28	using burst mode transfer, e.g. direct memory
	devices or central processing units (interface		access {DMA}, cycle steal (G06F 13/32 takes
	<u>.</u>		precedence)
	circuits for specific input/output devices G06F 3/00	13/282	• • • {Cycle stealing DMA (<u>G06F 13/30</u> takes
	{; multiprogram control therefor G06F 9/46};	10,202	precedence)}
	multiprocessor systems G06F 15/16)	13/285	• • • • {Halt processor DMA (<u>G06F 13/30</u> takes
13/10	 Program control for peripheral devices 	13/203	precedence)}
	(<u>G06F 13/14</u> - <u>G06F 13/42</u> take precedence)	13/287	• • • • {Multiplexed DMA (<u>G06F 13/30</u> takes
13/102	• • {where the programme performs an interfacing	13/207	precedence)}
	function, e.g. device driver (G06F 13/105 takes	12/20	
	precedence; scheduling within device drivers	13/30	with priority control
	<u>G06F 9/52</u> ; contention policies within device	13/32	using combination of interrupt and burst mode
	drivers G06F 9/4881)}		transfer
13/105	• • { where the programme performs an input/output	13/34	with priority control
	emulation function}	13/36	 for access to common bus or bus system
13/107	• • • {Terminal emulation}	13/362	 with centralised access control
13/12	using hardware independent of the central	13/3625	• • • { using a time dependent access}
	processor, e.g. channel or peripheral processor	13/364	using independent requests or grants, e.g.
13/122	• • • { where hardware performs an I/O function		using separated request and grant lines
	other than control of data transfer}	13/366	using a centralised polling arbiter
13/124	• • • {where hardware is a sequential transfer control	13/368	• • • with decentralised access control
	unit, e.g. microprocessor, peripheral processor	13/37	using a physical-position-dependent priority,
	or state-machine}	13/3/	e.g. daisy chain, round robin or token passing
13/126	{and has means for transferring I/O	13/372	• • • using a time-dependent priority, e.g.
	instructions and statuses between control unit	13/3/2	individually loaded time counters or time slot
	and main processor}	13/374	using a self-select method with individual
13/128	{for dedicated transfers to a network (for	10,07.	priority code comparator
	protocol converters <u>G06F 13/387</u>)}	13/376	using a contention resolving method, e.g.
13/14	Handling requests for interconnection or transfer	13/3/0	collision detection, collision avoidance
13/16	• • for access to memory bus (G06F 13/28 takes	13/378	using a parallel poll method
	precedence)	13/38	• Information transfer, e.g. on bus (G06F 13/14 takes
13/1605	• • • {based on arbitration (arbitration in handling	13/30	precedence)
	access to a common bus or bus system	13/382	• • {using universal interface adapter}
	<u>G06F 13/36</u>)}	13/385	• • {for adaptation of a particular data processing
13/161	• • • { with latency improvement }	13/303	system to different peripheral devices}
13/1615	{using a concurrent pipeline structrure}	13/387	• • • {for adaptation of different data processing
13/1621	{by maintaining request order}	13/307	systems to different peripheral devices, e.g.
13/1626	• • • {by reordering requests}		protocol converters for incompatible systems,
13/1631	{through address comparison}		open system}
13/1636	{using refresh}	13/40	Bus structure { (for computer networks)
13/1642	{with request queuing}	13/40	G06F 15/163; for optical bus networks
	The state of the s		H04B 10/25)}
13/1647	• • • { with interleaved bank access }	13/4004	• • {Coupling between buses}
13/1652	{in a multiprocessor architecture	13/4009	• • • {with data restructuring}
	(interprocessor communication using	13/4009	• • • { with data restructuring } • • • • { with data re-ordering, e.g. Endian
10/1655	common memory <u>G06F 15/167</u>)}	13/4013	conversion}
13/1657	{Access to multiple memories}	13/4018	• • • • {with data-width conversion}
13/1663	{Access to shared memory}		
13/1668	• • • {Details of memory controller}	13/4022	• • • { using switching circuits, e.g. switching
13/1673	• • • {using buffers}		matrix, connection or expansion network
13/1678	• • • {using bus width}	12/4027	$(\underline{G06F 13/4009} \text{ takes precedence})$
13/1684	• • • {using multiple buses}	13/4027	• • • { using bus bridges (<u>G06F 13/4022</u> takes
13/1689	(Synchronisation and timing concerns	12/4021	precedence)} (with orbitration)
	(synchronisation on a memory bus	13/4031	{with arbitration}
	<u>G06F 13/4234</u>)}	13/4036	• • • • { and deadlock prevention }
13/1694	• • • • {Configuration of memory controller to	13/404	• • • • { with address mapping }
	different memory types}	13/4045	• • • • {where the bus bridge performs an
13/18	• • • based on priority control (G06F 13/1605 takes	10//07	extender function}
	precedence)	13/405	• • • • {where the bridge performs a
13/20	for access to input/output bus		synchronising function}
13/22	• • • using successive scanning, e.g. polling		
	(G06F 13/24 takes precedence)		

13/4054	• • • • • {where the function is bus cycle	15/025 • • {adapted to a specific application}
	extension, e.g. to meet the timing	15/0258 {for unit conversion}
	requirements of the target bus}	15/0266 {for time management, e.g. calendars, diaries}
13/4059	• • • • • { where the synchronisation uses buffers,	15/0275 { for measuring }
	e.g. for speed matching between buses}	15/0283 {for data storage and retrieval}
13/4063	• • • {Device-to-bus coupling}	15/0291 {for reading, e.g. e-books (constructional
13/4068	• • • {Electrical coupling}	details of portable computers G06F 1/1613)}
13/4072	• • • • • {Drivers or receivers (<u>G06F 13/4086</u> takes	15/04 • programmed simultaneously with the introduction
	precedence; for multistate logic circuits	of data to be processed, e.g. on the same record
	<u>H03K 19/0002</u>)}	carrier
13/4077	• • • • • {Precharging or discharging}	15/08 . using a plugboard for programming
13/4081	{Live connection to bus, e.g. hot-plugging	15/10 . Tabulators
	(current or voltage limitation during live	15/12 having provision for both printed and punched
	insertion <u>H02H 9/004</u>)}	output
13/4086	• • • • {Bus impedance matching, e.g.	15/14 . Calculating-punches
	termination}	15/16 • Combinations of two or more digital computers
13/409	{Mechanical coupling (back panels	each having at least an arithmetic unit, a program
	H05K 7/1438)}	unit and a register, e.g. for a simultaneous
13/4095	• • • • {in incremental bus architectures, e.g. bus	processing of several programs {(coordinating
	stacks}	program control therefor G06F 9/52; in regulating
13/42	Bus transfer protocol, e.g. handshake;	and control system <u>G05B</u>)}
	Synchronisation	15/161 • {Computing infrastructure, e.g. computer clusters,
13/4204	{on a parallel bus}	blade chassis or hardware partitioning (casings,
13/4208	• • • {being a system bus, e.g. VME bus,	cabinets, racks or drawers for data centers
13/ 1200	Futurebus, Multibus}	H05K 5/00)}
13/4213	• • • • { with asynchronous protocol }	15/163 . Interprocessor communication
13/4217	• • • • {with synchronous protocol}	15/167 using a common memory, e.g. mailbox
13/4221	• • • {with synchronous protects} • • • {being an input/output bus, e.g. ISA bus,	15/17 using an input/output type connection, e.g.
13/7221	EISA bus, PCI bus, SCSI bus}	channel, I/O port
13/4226	{with asynchronous protocol}	15/173 using an interconnection network, e.g. matrix,
13/423	{with synchronous protocol}	shuffle, pyramid, star, snowflake
13/4234	{with synchronous protocor} {being a memory bus}	15/17306 {Intercommunication techniques}
		15/17302 {Routing techniques specific to parallel
13/4239	{with asynchronous protocol}	machines, e.g. wormhole, store and
13/4243	• • • • {with synchronous protocol}	forward, shortest path problem congestion
13/4247	• • • {on a daisy chain bus}	(routing on a LAN <u>H04L 45/00</u>)}
13/4252	• • • {using a handshaking protocol}	15/17318 {Parallel communications techniques, e.g.
13/4256	• • • { using a clocked protocol }	gather, scatter, reduce, roadcast, multicast,
13/426	• • • { using an embedded synchronisation, e.g.	all to all}
	Firewire bus, Fibre Channel bus, SSA bus}	15/17325 {Synchronisation; Hardware support
13/4265	• • • {on a point to point bus (<u>G06F 13/4247</u> ,	therefor (intertask synchronisation
	$\underline{\text{G06F } 13/4282}$ take precedence)}	$\frac{G06F 9/52}{}$
13/4269	• • • {using a handshaking protocol, e.g.	15/17331 {Distributed shared memory [DSM], e.g.
	Centronics connection}	remote direct memory access [RDMA]}
13/4273	• • • { using a clocked protocol}	15/17337 {Direct connection machines, e.g.
13/4278	• • • { using an embedded synchronisation}	completely connected computers, point to
13/4282	• • • {on a serial bus, e.g. I2C bus, SPI bus (on daisy	point communication networks (coupling
	chain buses <u>G06F 13/4247</u>)}	between buses G06F 13/4004)}
13/4286	{using a handshaking protocol, e.g. RS232C	15/17343 {wherein the interconnection is
	link}	dynamically configurable, e.g. having
13/4291	• • • {using a clocked protocol}	loosely coupled nearest neighbor
13/4295	• • • {using an embedded synchronisation}	architecture (reconfigurable processors
. =		arrays <u>G06F 15/7867</u>)}
15/00	Digital computers in general (details G06F 1/00 –	15/1735 {Network adapters, e.g. SCI, Myrinet
	G06F 13/00); Data processing equipment in general	(protocol engines <u>H04L 69/12</u>)}
15/02	manually operated with input through keyboard and	15/17356 {Indirect interconnection networks}
	computation using a built-in program, e.g. pocket	15/17362 { hierarchical topologies}
	calculators	
15/0208	• • {for combination with other devices having a	
	different main function, e.g. watches, pens}	15/17375 {One dimensional, e.g. linear array,
15/0216	• • {Constructional details or arrangements}	ring}
15/0225	• • {User interface arrangements, e.g. keyboard,	15/17381 {Two dimensional, e.g. mesh, torus}
	display; Interfaces to other computer systems}	15/17387 {Three dimensional, e.g. hypercubes}
15/0233	• • • {with printing provisions}	
15/00/41		
15/0241	• • {of the IC-card-like type}	

15/17393	broadcasting scattering, gathering, hot spot contention, combining/	15/7896	• • {Modular architectures, e.g. assembled from a number of identical packages}
15/177	decombining} Initialisation or configuration control {(processor)	15/80	 comprising an array of processing units with common control, e.g. single instruction multiple data processors (G06F 15/82 takes precedence
	initialisation G06F 9/4405)}		{; for correlation function computation
15/76	 Architectures of general purpose stored program computers (with program plugboard G06F 15/08; 	15/8007	G06F 17/15}) {single instruction multiple data [SIMD]
	multicomputers G06F 15/16)		multiprocessors}
2015/761	• • {Indexing scheme relating to architectures of general purpose stored programme computers}	15/8015	• • • {One dimensional arrays, e.g. rings, linear arrays, buses}
2015/763	{ASIC}	15/8023	{Two dimensional arrays, e.g. mesh, torus}
2015/765	{Cache}	15/803	• • • {Three-dimensional arrays or hypercubes}
2015/766	• • {Flash EPROM}	15/8038	• • {Associative processors}
2015/768	{Gate array}	15/8046	{Systolic arrays}
15/78	comprising a single central processing unit	15/8053	• • {Vector processors}
15/7803	• • • {System on board, i.e. computer system	15/8061	{Details on data memory access}
	on one or more PCB, e.g. motherboards,	15/8069	{using a cache}
	daughterboards or blades}	15/8076	{Details on data register access}
15/7807	{System on chip, i.e. computer system on a	15/8084	{Special arrangements thereof, e.g. mask
	single chip; System in package, i.e. computer		or switch}
	system on one or more chips in a single	15/8092	{Array of vector units}
	package}	15/82	data or demand driven
15/781	• • • {On-chip cache; Off-chip memory}	15/825	• • • {Dataflow computers}
15/7814	• • • {Specially adapted for real time processing, e.g. comprising hardware timers}	16/00	Information retrieval; Database structures
15/7817	• • • {Specially adapted for signal processing, e.g.	1.6/1.0	therefor; File system structures therefor
4.7.7004	Harvard architectures}	16/10	• File systems; File servers
15/7821	• • • • {Tightly coupled to memory, e.g.	16/11	• File system administration, e.g. details of
	computational memory, smart memory, processor in memory}		archiving or snapshots (file system backup G06F 11/14)
15/7825	{Globally asynchronous, locally	16/113	{Details of archiving (lifecycle management in
	synchronous, e.g. network on chip}		storage systems G06F 3/0649; backup systems
15/7828	• • { without memory }		<u>G06F 11/1446</u>)}
15/7832	• • • {on one IC chip (single chip	16/116	{Details of conversion of file system types or
	microprocessors)}		formats}
15/7835	• • • {on more than one IC chip}	16/119	• • • {Details of migration of file systems (migration
15/7839	• • • {with memory}		mechanisms in storage systems <u>G06F 3/0647</u>)}
15/7842	{on one IC chip (single chip	16/122	• • • {using management policies (backup systems
15/5046	microcontrollers)}		G06F 11/1446; file migration policies for HSM systems G06F 16/185)}
15/7846	• • • • {On-chip cache and off-chip main memory}	16/125	• • • • {characterised by the use of retention
15/785	• • • • • { with decentralized control, e.g. smart		policies (retention policies for HSM systems
13/763	memories}		G06F 16/185)}
15/7853	· · · · · {including a ROM}	16/128	• • • {Details of file system snapshots on the file-
15/7857	• • • • • {Including a ROW} • • • • • {using interleaved memory (addressing}		level, e.g. snapshot creation, administration,
13/7637	G06F 12/0607)}		deletion (use of snapshots for error detection or
15/786	• • • • {using a single memory module}		correction <u>G06F 11/14</u> , <u>G06F 11/16</u>)}
15/7864	{on more than one IC chip}	16/13	• File access structures, e.g. distributed indices
15/7867	• • {with reconfigurable architecture}		(arrangements of input from, or output to, record
15/7871	{Reconfiguration support, e.g. configuration		carriers <u>G06F 3/06</u>)
13/7071	loading, configuration switching, or	16/134	• • • {Distributed indices}
	hardware OS}	16/137	{Hash-based (content-based indexing of textual
15/7875	• • • • {for multiple contexts}		data <u>G06F 16/31</u>)}
15/7878	• • • • {for pipeline reconfiguration}	16/14	Details of searching files based on file metadata
15/7882	{for self reconfiguration}	16/144	{Query formulation}
15/7885	{Runtime interface, e.g. data exchange,	16/148	• • • {File search processing}
13/1003		16/152	{using file content signatures, e.g. hash values}
1 <i>5 /</i> 7000	runtime control}		values
15/7889	{Reconfigurable logic implemented as a	16/156	• • • {Query results presentation}
15/7889	• • • • • {Reconfigurable logic implemented as a co-processor (instruction execution using a	16/156 16/16	 {Query results presentation} File or folder operations, e.g. details of user
	• • • • • {Reconfigurable logic implemented as a co-processor (instruction execution using a coprocessor <u>G06F 9/3877</u>)}		• • • {Query results presentation}
15/7889 15/7892	• • • • • {Reconfigurable logic implemented as a co-processor (instruction execution using a		 {Query results presentation} File or folder operations, e.g. details of user

1 - 11 - 1	(57)	16(1004
16/164	• • {File meta data generation}	16/1824 {implemented using Network-attached
16/166	• • • {File name conversion}	Storage [NAS] architecture (distributed or
16/168	• • • {Details of user interfaces specifically adapted	networked storage systems G06F 3/067;
	to file systems, e.g. browsing and visualisation,	protocols for distributed storage of data in a
	2d or 3d GUIs (query results presentation	network <u>H04L 67/1097</u>)}
	G06F 16/156)}	16/1827 {Management specifically adapted to NAS
16/17	Details of further file system functions	(management of storage area networks
		[SAN] <u>G06F 3/067</u>)}
16/172	Caching, prefetching or hoarding of files	16/183 {Provision of network file services by
16/1724	• • • {Details of de-fragmentation performed by the	·
	file system (saving storage space on storage	network file servers, e.g. by using NFS,
	systems G06F 3/0608; management of blocks	CIFS (network file access protocols
	in storage devices G06F 3/064)}	<u>H04L 67/1097</u>)}
16/1727	{Details of free space management performed	16/1834 {implemented based on peer-to-peer
	by the file system (saving storage space on	networks, e.g. gnutella (p2p communication
	storage systems G06F 3/0608; management of	protocols <u>H04L 67/104</u>)}
	blocks in storage devices G06F 3/064)}	16/1837 {Management specially adapted to peer-
16/173	• • • {Customisation support for file systems,	to-peer storage networks (topology
10/1/3	e.g. localisation, multi-language support,	management mechanisms of peer-to-peer
		networks <u>H04L 67/1042</u>)}
1.6/1.50.4	personalisation}	16/184 {implemented as replicated file system}
16/1734	• • • {Details of monitoring file system events, e.g.	
	by the use of hooks, filter drivers, logs}	16/1844 {Management specifically adapted to
16/1737	• • • { for reducing power consumption or coping	replicated file systems}
	with limited storage space, e.g. in mobile	16/1847 • • • {specifically adapted to static storage, e.g.
	devices (saving storage space on storage	adapted to flash memory or SSD}
	devices <u>G06F 3/0608</u> ; power saving in storage	16/185 Hierarchical storage management [HSM]
	systems $\overline{\text{G06F 3/0625}}$)	systems, e.g. file migration or policies thereof
16/174	Redundancy elimination performed by the file	(details of archiving G06F 16/11)
10/1/4	system (management of the data involved in	16/1858 {Parallel file systems, i.e. file systems
		supporting multiple processors}
	backup or backup restore using de-duplication	
16/15/14	of the data <u>G06F 11/14</u>)	16/1865 {Transactional file systems}
16/1744	• • • {using compression, e.g. sparse files}	16/1873 {Versioning file systems, temporal file
16/1748	• • • {De-duplication implemented within the	systems, e.g. file system supporting different
	file system, e.g. based on file segments	historic versions of files}
	(de-duplication techniques in storage	16/188 Virtual file systems
	systems for the management of data blocks	16/192 {Implementing virtual folder structures}
	G06F 3/0641)}	16/196 {Specific adaptations of the file system
16/1752	• • • • {based on file chunks}	to access devices and non-file objects via
16/1756	{based on delta files}	standard file system access operations, e.g.
16/176	Support for shared access to files; File sharing	pseudo file systems (dedicated interfaces to
10/1/0		storage systems (G06F 3/0601)}
16/17/7	support	
16/1767	{Concurrency control, e.g. optimistic or	. of structured data, e.g. relational data
	pessimistic approaches}	16/21 . Design, administration or maintenance of
16/1774	• • • • {Locking methods, e.g. locking methods	databases
	for file systems allowing shared and	16/211 {Schema design and management}
	concurrent access to files}	16/212 { with details for data modelling support}
16/178	Techniques for file synchronisation in file	16/213 { with details for schema evolution support}
	systems	16/214 {Database migration support}
16/1787	{Details of non-transparently synchronising	
10/1/07	file systems}	16/215 Improving data quality; Data cleansing, e.g.
1.6/1704	· · · · · · · · · · · · · · · · · · ·	de-duplication, removing invalid entries or
16/1794	• • • {Details of file format conversion}	correcting typographical errors
	WARNING .	16/217 {Database tuning (<u>G06F 16/2282</u> takes
		precedence; database performance monitoring
	Group G06F 16/1794 is impacted by	<u>G06F 11/3409</u>)}
	reclassification into group G06F 16/258.	16/219 {Managing data history or versioning (querying
	Groups G06F 16/1794 and G06F 16/258	versioned data G06F 16/2474; querying
	should be considered in order to perform	temporal data <u>G06F 16/2477</u>)}
	a complete search.	16/22 •• Indexing; Data structures therefor; Storage
		-
16/18	File system types	structures
16/1805	• • • {Append-only file systems, e.g. using logs or	16/221 {Column-oriented storage; Management
	journals to store data}	thereof}
16/181	• • • {providing write once read many [WORM]	16/2219 {Large Object storage; Management thereof}
10/101		16/2228 {Indexing structures}
16/1015	semantics}	16/2237 {Vectors, bitmaps or matrices}
16/1815	{Journaling file systems}	16/2246 {Trees, e.g. B+trees}
16/182	Distributed file systems	15/2270 • • • • [11005, 0.g. D+11005]

16/2255	(Hogh tables)	16/24 Overvine
16/2255 16/2264	 {Hash tables} {Multidimensional index structures}	16/24 Querying 16/242 Query formulation
16/2272	{Management thereof}	16/2423 {Interactive query statement specification
16/2282	{Tablespace storage structures; Management	based on a database schema}
	thereof}	16/2425 {Iterative querying; Query formulation based
16/2291	• • • {User-Defined Types; Storage management	on the results of a preceding query}
	thereof}	16/2428 {Query predicate definition using graphical
16/23	Updating	user interfaces, including menus and forms (G06F 16/2423 takes precedence)}
	WARNING	16/243 {Natural language query formulation}
	Group G06F 16/23 is impacted by	16/2433 {Query language query formulation}
	reclassification into group G06F 16/25.	16/2435 {Active constructs}
	Groups G06F 16/23 and G06F 16/25 should	16/2438 {Embedded query languages}
	be considered in order to perform a complete	16/244 {Grouping and aggregation}
	search.	16/2443 {Stored procedures}
16/2308	• • • {Concurrency control (transaction processing	16/2445 {Data retrieval commands; View
10/2000	G06F 9/466)}	definitions}
	WARNING	16/2448 (for particular applications; for
		extensibility, e.g. user defined types}
	Group G06F 16/2308 is impacted by reclassification into groups G06F 16/2315,	16/245 Query processing 16/2452 Query translation
	G06F 16/2322, G06F 16/2329,	16/24522 {Translation of natural language queries to
	G06F 16/2336, and G06F 16/2343.	structured queries }
	All groups listed in this Warning should be	16/24524 {Access plan code generation and
	considered in order to perform a complete	invalidation; Reuse of access plans}
	search.	16/24526 • • • • • {Internal representations for queries}
16/2315	{Optimistic concurrency control}	16/24528 {Standardisation; Simplification}
10/2313		16/2453 Query optimisation
	<u>WARNING</u>	16/24532 {of parallel queries}
	Groups G06F 16/2315 - G06F 16/2329	16/24534 {Query rewriting; Transformation}
	are incomplete pending reclassification of	16/24535 {of sub-queries or views} 16/24537 {of operators}
	documents from group G06F 16/2308.	16/24539 {or operators}
	Groups <u>G06F 16/2308</u> and <u>G06F 16/2315</u> - <u>G06F 16/2329</u> should be	results }
	considered in order to perform a complete	16/2454 {Optimisation of common expressions}
	search.	16/24542 {Plan optimisation}
1.6/0222		16/24544 {Join order optimisation}
16/2322	{using timestamps}	16/24545 {Selectivity estimation or
16/2329 16/2336	 {using versioning} {Pessimistic concurrency control approaches,	determination}
10/2330	e.g. locking or multiple versions without	16/24547 {Optimisations to support specific
	time stamps}	applications; Extensibility of optimisers}
	WARNING	16/24549 {Run-time optimisation}
		16/2455 Query execution
	Groups G06F 16/2336 and G06F 16/2343 are incomplete pending reclassification of	16/24552 {Database cache management}
	documents from group G06F 16/2308.	16/24553 • • • • • {of query operations}
	Groups G06F 16/2308, G06F 16/2336,	16/24554 {Unary operations; Data partitioning
	and G06F 16/2343 should be considered	operations}
	in order to perform a complete search.	16/24556 {Aggregation; Duplicate elimination}
16/2343	{Locking methods, e.g. distributed locking	16/24557 {Efficient disk access during query execution}
10/2343	or locking implementation details}	16/24558 {Binary matching operations}
16/235	{Update request formulation}	16/2456 {Join operations}
16/2358	• • • {Change logging, detection, and notification	16/24561 {Intermediate data storage techniques
	(replication <u>G06F 16/27</u>)}	for performance improvement}
16/2365	• • • {Ensuring data consistency and integrity}	16/24562 {Pointer or reference processing
16/2372	• • • {Updates performed during offline database	operations}
16/2270	operations} {Updates performed during online database	16/24564 {Applying rules; Deductive queries}
16/2379	operations; commit processing }	16/24565 {Triggers; Constraints} 16/24566 {Recursive queries}
16/2386	{Bulk updating operations (data conversion	16/24566 {Recursive queries} 16/24568 {Data stream processing; Continuous
	details <u>G06F 16/258</u>)}	queries}
16/2393	• • {Updating materialised views}	1)

16/24569	• • • • {Query processing with adaptation to	16/273	{Asynchronous replication or reconciliation}
	specific hardware, e.g. adapted for using		WARNING
1 < /2 1 5 5	GPUs or SSDs}		
16/2457 16/24573	 with adaptation to user needs {using data annotations, e.g. user-defined		Groups G06F 16/273 is incomplete pending reclassification of documents from group
	metadata}		<u>G06F 16/27</u> .
	• • • • {using context}		Groups <u>G06F 16/27</u> and <u>G06F 16/273</u>
	• • • • {using ranking}		should be considered in order to perform a
16/2458	• • • Special types of queries, e.g. statistical		complete search.
16/2462	queries, fuzzy queries or distributed queries	16/275	• • • {Synchronous replication}
16/2462 16/2465	{Approximate or statistical queries} {Query processing support for facilitating		WARNING
10/2403	data mining operations in structured		
	databases}		Groups G06F 16/275 is incomplete pending reclassification of documents from group
16/2468	• • • • {Fuzzy queries}		G06F 16/27.
16/2471	• • • • {Distributed queries}		Groups G06F 16/27 and G06F 16/275
16/2474	• • • • { Sequence data queries, e.g. querying		should be considered in order to perform a
	versioned data}		complete search.
16/2477	{Temporal data queries}	16/278	• • • {Data partitioning, e.g. horizontal or vertical
16/248	Presentation of query results	10/2/8	• • {Data partitioning, e.g. horizontal or vertical partitioning}
16/25	Integrating or interfacing systems involving database management systems		•
			WARNING
	WARNING		Groups G06F 16/278 is incomplete pending
	Group G06F 16/25 is incomplete pending reclassification of documents from group		reclassification of documents from group G06F 16/27.
	G06F 16/23.		Groups G06F 16/27 and G06F 16/278
	Groups G06F 16/23 and G06F 16/25 should		should be considered in order to perform a
	be considered in order to perform a complete		complete search.
	search.	16/28	Databases characterised by their database models,
16/252	{between a Database Management System and		e.g. relational or object models
	a front-end application}	16/282	• • {Hierarchical databases, e.g. IMS, LDAP data
16/254	• • • {Extract, transform and load [ETL] procedures,	4 4 (2.0.2)	stores or Lotus Notes}
	e.g. ETL data flows in data warehouses}	16/283	{Multi-dimensional databases or data
16/256	• • · {in federated or virtual databases}	16/284	warehouses, e.g. MOLAP or ROLAP} {Relational databases}
16/258	• • {Data format conversion from or to a database}	16/285	{Clustering or classification}
	WARNING	16/287	• • • • {Visualization; Browsing}
	Groups G06F 16/258 is incomplete pending	16/288	• • • {Entity relationship models}
	reclassification of documents from group	16/289	{Object oriented databases}
	<u>G06F 16/1794</u> .	16/29	Geographical information databases
	Groups G06F 16/1794 and G06F 16/258	16/30	• of unstructured textual data (document management
	should be considered in order to perform a		systems <u>G06F 16/93</u>)
	complete search.		<u>NOTE</u>
16/26	Visual data mining; Browsing structured data		In groups G06F 16/30, G06F 16/31,
16/27	Replication, distribution or synchronisation of		G06F 16/313, G06F 16/316, G06F 16/319,
	data between databases or within a distributed		G06F 16/322, G06F 16/325, G06F 16/328,
	database system; Distributed database system		G06F 16/33, G06F 16/332, G06F 16/3322,
	architectures therefor		G06F 16/3323, G06F 16/3325, G06F 16/3326, G06F 16/3328, G06F 16/3329, G06F 16/33295,
	WARNING		G06F 16/3331, G06F 16/3332, G06F 16/3334,
	Group G06F 16/27 is impacted by		G06F 16/3335, G06F 16/3337, G06F 16/3338,
	reclassification into groups G06F 16/273,		G06F 16/334, G06F 16/3341, G06F 16/3343,
	<u>G06F 16/275</u> , and <u>G06F 16/278</u> .		G06F 16/3344, G06F 16/3346, G06F 16/3347,
	All groups listed in this Warning should be		G06F 16/3349, G06F 16/335, G06F 16/337, G06F 16/338, G06F 16/34, G06F 16/345

G06F 16/338, G06F 16/34, G06F 16/345,

G06F 16/35, G06F 16/353, G06F 16/355, G06F 16/358, G06F 16/36, G06F 16/367 and G06F 16/374, subject matter relevant to retrieval characterised by using metadata, when it is determined to be novel and non-obvious, must also be classified in groups G06F 16/38,

considered in order to perform a complete

search.

G06F

G06F 16/30			
(continued)	G06F 16/381, G06F 16/382, G06F 16/383 and G06F 16/387.	16/335	• • • Filtering based on additional data, e.g. user or group profiles (filtering in web context
16/31	. Indexing; Data structures therefor; Storage	16/337	<u>G06F 16/9535</u> , <u>G06F 16/9536</u>) {Profile generation, learning or
16/313	structures• {Selection or weighting of terms for indexing}		modification}
16/316	{Selection of weighting of terms for indexing} {Indexing structures}	16/338	Presentation of query results
16/319	{Inverted lists}	16/34	Browsing; Visualisation therefor (browsing)
16/322	{Trees}		or visualisation for clustering or classification
16/325	{Hash tables}		<u>G06F 16/358</u>)
16/328	{Management therefor}		<u>WARNING</u>
16/33	Querying		Group G06F 16/34 is impacted by
16/332	Query formulation		reclassification into group G06F 16/358.
16/3322	• • • {using system suggestions (G06F 16/3325 takes precedence)}		Groups <u>G06F 16/34</u> and <u>G06F 16/358</u> should be considered in order to perform a complete
16/3323	• • • • { using document space presentation or		search.
	visualization, e.g. category, hierarchy or	16/345	• • • {Summarisation for human users}
	range presentation and selection}	16/35	Clustering; Classification
16/3325	{Reformulation based on results of preceding	16/353	into predefined classes
16/2226	query}	16/355	Creation or modification of classes or clusters
16/3326	{using relevance feedback from the user,	16/358	Browsing; Visualisation therefor
	e.g. relevance feedback on documents, documents sets, document terms or		WARNING
	passages}		
16/3328	{using graphical result space		Group G06F 16/358 is incomplete pending
	presentation or visualisation}		reclassification of documents from group G06F 16/34.
16/3329	Natural language query formulation		Groups G06F 16/34 and G06F 16/358
	WARNING		should be considered in order to perform a
	Group G06F 16/3329 is impacted		complete search.
	by reclassification into group	16/36	Creation of semantic tools, e.g. ontology or
	<u>G06F 16/33295</u> .	10/30	thesauri
	Groups G06F 16/3329 and	16/367	· · · {Ontology}
	G06F 16/33295 should be considered in	16/374	· · · {Thesaurus}
	order to perform a complete search.	16/38	Retrieval characterised by using metadata,
16/33295	{in dialogue systems}		e.g. metadata not derived from the content or
	WARNING		metadata generated manually
			WARNING
	Group <u>G06F 16/33295</u> is incomplete pending reclassification of documents		Group G06F 16/38 is impacted by
	from group G06F 16/3329.		reclassification into groups G06F 16/383 and
	Groups <u>G06F 16/3329</u> and		<u>G06F 16/387</u> .
	G06F 16/33295 should be considered in		All groups listed in this Warning should be
	order to perform a complete search.		considered in order to perform a complete
			search.
16/3331	Query processing	16/381	• • • {using identifiers, e.g. barcodes, RFIDs (for
16/3332	Query translation	10/301	URLs <u>G06F 16/9554</u>)}
16/3334	{Selection or weighting of terms from	16/382	• • • {using citations (hypermedia G06F 16/94)}
	queries, including natural language queries}	16/383	• • • using metadata automatically derived from the
16/3335	{Syntactic pre-processing, e.g. stopword		content
10,000	elimination, stemming}		WARNING
16/3337	• • • • {Translation of the query language, e.g.		Group G06F 16/383 is incomplete pending
	Chinese to English}		reclassification of documents from group
16/3338	{Query expansion}		G06F 16/38.
16/334	Query execution (filtering based on		Groups G06F 16/38 and G06F 16/383
16/2241	additional data G06F 16/335)		should be considered in order to perform a
16/3341	{using boolean model}		complete search.
16/3343	{using phonetics}		
16/3344	{using natural language analysis}		
16/3346 16/3347	 {using probabilistic model} {using vector based model}		
16/3347	Reuse of stored results of previous queries		
10/3349	Reuse of stored results of previous queries		

16/387 . . . using geographical or spatial information, e.g. 16/436 . . . {using biological or physiological data of a human being, e.g. blood pressure, facial location expression, gestures} WARNING • • • {Administration of user profiles, e.g. 16/437 Group G06F 16/387 is incomplete pending generation, initialisation, adaptation, reclassification of documents from group distribution} G06F 16/38. 16/438 . . . Presentation of query results Groups <u>G06F 16/38</u> and <u>G06F 16/387</u> 16/4387 • • • {by the use of playlists} should be considered in order to perform a 16/4393 • • • • • Multimedia presentations, e.g. slide complete search. shows, multimedia albums} 16/44 . . Browsing; Visualisation therefor 16/40 . of multimedia data, e.g. slideshows comprising 16/444 . . . {Spatial browsing, e.g. 2D maps, 3D or virtual image and additional audio data (retrieval of still spaces } image data G06F 16/50; retrieval of audio data 16/447 . . . {Temporal browsing, e.g. timeline} G06F 16/60; retrieval of video data G06F 16/70) 16/45 . . Clustering; Classification NOTE WARNING In groups <u>G06F 16/40</u>, <u>G06F 16/41</u>, Group G06F 16/45 is incomplete pending G06F 16/43, G06F 16/432, G06F 16/433, reclassification of documents from group G06F 16/434, G06F 16/435, G06F 16/436, G06F 16/40 G06F 16/437, G06F 16/438, G06F 16/4387, G06F 16/4393, G06F 16/44, G06F 16/444, Groups G06F 16/40 and G06F 16/45 should G06F 16/447 and G06F 16/45, subject matter be considered in order to perform a complete relevant to retrieval characterised by using metadata, when it is determined to be novel and 16/48 . . Retrieval characterised by using metadata, non-obvious, must also be classified in groups e.g. metadata not derived from the content or G06F 16/48, G06F 16/483, G06F 16/487 and metadata generated manually G06F 16/489. **WARNING WARNING** Group G06F 16/48 is incomplete pending Group G06F 16/40 is impacted by reclassification of documents from groups reclassification into groups G06F 16/45, G06F 16/40 and G06F 16/43. G06F 16/48, G06F 16/483, G06F 16/487, and Groups G06F 16/40, G06F 16/43, and G06F 16/489. G06F 16/48 should be considered in order to All groups listed in this Warning should be perform a complete search. considered in order to perform a complete search. 16/483 . . . using metadata automatically derived from the content 16/41 . . Indexing; Data structures therefor; Storage structures WARNING 16/43 . . Querying Group G06F 16/483 is incomplete pending reclassification of documents from groups WARNING G06F 16/40 and G06F 16/43. Group G06F 16/43 is impacted by Groups G06F 16/40, G06F 16/43, and reclassification into groups G06F 16/432, G06F 16/483 should be considered in order G06F 16/48, G06F 16/483, G06F 16/487, and to perform a complete search. G06F 16/489. All groups listed in this Warning should be 16/487 . . . using geographical or spatial information, e.g. considered in order to perform a complete location search. WARNING 16/432 . . . Query formulation Group G06F 16/487 is incomplete pending reclassification of documents from groups **WARNING** G06F 16/40 and G06F 16/43. Group G06F 16/432 is incomplete pending Groups G06F 16/40, G06F 16/43, and reclassification of documents from group G06F 16/487 should be considered in order G06F 16/43. to perform a complete search. Groups G06F 16/43 and G06F 16/432 should be considered in order to perform a complete search. 16/433 • • • {using audio data} 16/434 • • • {using image data, e.g. images, photos, pictures taken by a user} 16/435 . . . Filtering based on additional data, e.g. user or group profiles

16/489 . . . {using time information}

WARNING

Group G06F 16/489 is incomplete pending reclassification of documents from groups G06F 16/40 and G06F 16/43.

Groups G06F 16/40, G06F 16/43, and G06F 16/489 should be considered in order to perform a complete search.

16/50 • of still image data

NOTE

In groups G06F 16/50, G06F 16/51, G06F 16/53, G06F 16/532, G06F 16/535, G06F 16/538, G06F 16/54, G06F 16/55 and G06F 16/56, subject matter relevant to retrieval characterised by using metadata, when it is determined to be novel and non-obvious, must also be classified in groups G06F 16/58, G06F 16/583, G06F 16/5838, G06F 16/5846, G06F 16/5854, G06F 16/5862 and G06F 16/587.

WARNING

Group <u>G06F 16/50</u> is impacted by reclassification into groups <u>G06F 16/53</u>, <u>G06F 16/532</u>, <u>G06F 16/535</u>, <u>G06F 16/538</u>, and <u>G06F 16/55</u>.

All groups listed in this Warning should be considered in order to perform a complete search.

16/51 . Indexing; Data structures therefor; Storage structures

16/53 . . Querying

WARNING

Group G06F 16/53 is incomplete pending reclassification of documents from group G06F 16/50.

Groups <u>G06F 16/50</u> and <u>G06F 16/53</u> should be considered in order to perform a complete search

16/532 . . . Query formulation, e.g. graphical querying

WARNING

Group G06F 16/532 is incomplete pending reclassification of documents from group G06F 16/50.

Groups <u>G06F 16/50</u> and <u>G06F 16/532</u> should be considered in order to perform a complete search.

16/535 • • • Filtering based on additional data, e.g. user or group profiles

WARNING

Group G06F 16/535 is incomplete pending reclassification of documents from group G06F 16/50.

Groups <u>G06F 16/50</u> and <u>G06F 16/535</u> should be considered in order to perform a complete search.

16/538 . . . Presentation of query results

WARNING

Group G06F 16/538 is incomplete pending reclassification of documents from group G06F 16/50.

Groups <u>G06F 16/50</u> and <u>G06F 16/538</u> should be considered in order to perform a complete search.

16/54 . . Browsing; Visualisation therefor

16/55 . . Clustering; Classification

WARNING

Group G06F 16/55 is incomplete pending reclassification of documents from group G06F 16/50.

Groups <u>G06F 16/50</u> and <u>G06F 16/55</u> should be considered in order to perform a complete search.

16/56 . . having vectorial format

16/58

 Retrieval characterised by using metadata,
 e.g. metadata not derived from the content or
 metadata generated manually

WARNING

Group <u>G06F 16/58</u> is impacted by reclassification into group <u>G06F 16/587</u>.

Groups <u>G06F 16/58</u> and <u>G06F 16/587</u> should be considered in order to perform a complete search.

16/583 . . . using metadata automatically derived from the content

16/5838 {using colour}

WARNING

Group <u>G06F 16/5838</u> is impacted by reclassification into groups <u>G06F 16/5846</u>, <u>G06F 16/5854</u>, and <u>G06F 16/5862</u>.

All groups listed in this Warning should be considered in order to perform a complete search.

16/5846 {using extracted text}

WARNING

Group <u>G06F 16/5846</u> is incomplete pending reclassification of documents from group <u>G06F 16/5838</u>.

Groups <u>G06F 16/5838</u> and <u>G06F 16/5846</u> should be considered in order to perform a complete search.

16/5854 {using shape and object relationship}

WARNING

Group G06F 16/5854 is incomplete pending reclassification of documents from group G06F 16/5838.

Groups <u>G06F 16/5838</u> and <u>G06F 16/5854</u> should be considered in order to perform a complete search.

16/5862 • • • {using texture} 16/634 • • • {Query by example, e.g. query by humming} . . . Filtering based on additional data, e.g. user or 16/635 WARNING group profiles Group G06F 16/5862 is incomplete • • • {by using biological or physiological data} 16/636 pending reclassification of documents . . . {Administration of user profiles, e.g. 16/637 from group G06F 16/5838. generation, initialization, adaptation or Groups G06F 16/5838 and G06F 16/5862 distribution } should be considered in order to perform 16/638 . . . Presentation of query results a complete search. 16/639 • • • {using playlists} . . Browsing; Visualisation therefor (generation of a 16/64 16/5866 . . . {using information manually generated, e.g. list or set of audio data G06F 16/638) tags, keywords, comments, manually generated 16/65 . . Clustering; Classification location and time information} **WARNING** WARNING Group G06F 16/65 is incomplete pending Group G06F 16/5866 is impacted by reclassification of documents from group reclassification into group G06F 16/587. Groups G06F 16/5866 and G06F 16/587 Groups G06F 16/60 and G06F 16/65 should should be considered in order to perform a be considered in order to perform a complete complete search. 16/587 . . . using geographical or spatial information, e.g. 16/68 . . Retrieval characterised by using metadata, location e.g. metadata not derived from the content or WARNING metadata generated manually Group G06F 16/587 is incomplete pending WARNING reclassification of documents from groups Group G06F 16/68 is impacted by G06F 16/58 and G06F 16/5866. reclassification into group G06F 16/687. Groups G06F 16/58, G06F 16/5866, and Groups G06F 16/68 and G06F 16/687 should G06F 16/587 should be considered in order be considered in order to perform a complete to perform a complete search. search. 16/60 . of audio data 16/683 . . . using metadata automatically derived from the NOTE content 16/685 • • • { using automatically derived transcript of In groups G06F 16/60, G06F 16/61, G06F 16/63, G06F 16/632, G06F 16/634, audio data, e.g. lyrics (speech recognition G06F 16/635, G06F 16/636, G06F 16/637, G10L 15/00)} G06F 16/638, G06F 16/639, G06F 16/64, 16/686 . . . {using information manually generated, and G06F 16/65, subject matter relevant to e.g. tags, keywords, comments, title or retrieval characterised by using metadata, when artist information, time, location or usage it is determined to be novel and non-obvious, information, user ratings} must also be classified in groups G06F 16/68, **WARNING** G06F 16/683, G06F 16/685, G06F 16/686 and Group G06F 16/686 is impacted by G06F 16/687. reclassification into group G06F 16/687. **WARNING** Groups G06F 16/686 and G06F 16/687 Group G06F 16/60 is impacted by should be considered in order to perform a reclassification into groups G06F 16/63 and complete search. G06F 16/65. 16/687 . . . using geographical or spatial information, e.g. Groups G06F 16/60, G06F 16/63, and location G06F 16/65 should be considered in order to WARNING perform a complete search. Group G06F 16/687 is incomplete pending 16/61 . . Indexing; Data structures therefor; Storage reclassification of documents from groups structures G06F 16/68 and G06F 16/686. 16/63 . . Querying Groups G06F 16/68, G06F 16/686, and WARNING G06F 16/687 should be considered in order to perform a complete search. Group G06F 16/63 is incomplete pending reclassification of documents from group 16/70 . of video data G06F 16/60. NOTE Groups G06F 16/60 and G06F 16/63 should be considered in order to perform a complete In groups G06F 16/70, G06F 16/71, search. G06F 16/73, G06F 16/732, G06F 16/7328, G06F 16/7335, G06F 16/7343, G06F 16/735, 16/632 . . . Query formulation G06F 16/738, G06F 16/739, G06F 16/74,

G06F 16/70			
(continued)	G06F 16/743, G06F 16/745, G06F 16/78	16/748	• • {Hypervideo (linking data to content, e.g. by
	and G06F 16/75, subject matter relevant to retrieval characterised by using metadata, when		linking an URL to a video object in the context
	it is determined to be novel and non-obvious,	16/75	of video distribution systems <u>H04N 21/858</u>)} • Clustering; Classification
	must also be classified in groups G06F 16/78,	10/73	-
	G06F 16/783, G06F 16/7834, G06F 16/7837,		WARNING
	<u>G06F 16/784</u> , <u>G06F 16/7844</u> , <u>G06F 16/7847</u> ,		Group G06F 16/75 is incomplete pending
	<u>G06F 16/785</u> , <u>G06F 16/7854</u> , <u>G06F 16/7857</u> ,		reclassification of documents from group
	G06F 16/786, G06F 16/7864, G06F 16/7867		<u>G06F 16/70</u> .
	and <u>G06F 16/787</u> .		Groups <u>G06F 16/70</u> and <u>G06F 16/75</u> should
	WARNING		be considered in order to perform a complete search.
	Group G06F 16/70 is impacted by	16/70	
	reclassification into group G06F 16/75.	16/78	Retrieval characterised by using metadata, e.g. metadata not derived from the content or
	Groups <u>G06F 16/70</u> and <u>G06F 16/75</u> should		metadata generated manually
	be considered in order to perform a complete search.		WARNING
	search.		
16/71	Indexing; Data structures therefor; Storage		Group G06F 16/78 is impacted by
	structures		reclassification into group G06F 16/787.
16/73	Querying		Groups G06F 16/78 and G06F 16/787 should be considered in order to perform a complete
	WARNING		search.
	Group G06F 16/73 is impacted by		
	reclassification into group G06F 16/732.	16/783	using metadata automatically derived from the
	Groups G06F 16/73 and G06F 16/732 should	16/7024	content
	be considered in order to perform a complete	16/7834 16/7837	 {using audio features} {using objects detected or recognised in the
	search.	10/7037	video content}
16/732	Query formulation	16/784	{the detected or recognised objects being
	WARNING		people}
	Group G06F 16/732 is incomplete pending	16/7844	• • • {using original textual content or text
	reclassification of documents from group		extracted from visual content or transcript of audio data}
	G06F 16/73.	16/7847	• • • {using low-level visual features of the video
	Groups <u>G06F 16/73</u> and <u>G06F 16/732</u>	10,7017	content}
	should be considered in order to perform a	16/785	• • • • {using colour or luminescence}
	complete search.	16/7854	• • • • {using shape (<u>G06F 16/7837</u> takes
16/7328	• • • {Query by example, e.g. a complete video		precedence)}
	frame or video sequence (graphical querying	16/7857	• • • • {using texture (<u>G06F 16/7837</u> takes
	<u>G06F 16/7335</u>)}	16/786	precedence)} {using motion, e.g. object motion or
16/7335	• • • Graphical querying, e.g. query-by-region,	10/780	• • • • {using motion, e.g. object motion or camera motion}
	query-by-sketch, query-by-trajectory,	16/7864	• • • • {using domain-transform features, e.g.
	GUIs for designating a person/face/ object as a query predicate (end-user		DCT or wavelet transform coefficients}
	interface involving hot spots associated	16/7867	• • • {using information manually generated, e.g.
	with the video HO4N 21/4725; end-user		tags, keywords, comments, title and artist
	interface for selecting a Region of Interest		information, manually generated time, location and usage information, user ratings}
1.6/50.40	<u>H04N 21/4728</u>)}		
16/7343 16/735	 {Query language or query format} Filtering based on additional data, e.g. user or		<u>WARNING</u>
10/733	group profiles		Group G06F 16/7867 is impacted by
16/738	Presentation of query results		reclassification into group G06F 16/787.
16/739	• • • {in form of a video summary, e.g. the		Groups G06F 16/7867 and G06F 16/787
	video summary being a video sequence, a		should be considered in order to perform a complete search.
	composite still image or having synthesized		complete search.
16/74	frames} Browsing; Visualisation therefor (end-user)		
10/74	interfaces for requesting or interacting with		
	video content, e.g. video on demand interfaces or		
	electronic program guides, <u>H04N 21/472</u>)		
16/743	• • {a collection of video files or sequences}		
16/745	• • • {the internal structure of a single video		
	sequence}		

16/787

. . . using geographical or spatial information, e.g.

retrieval characterised by using metadata, when location it is determined to be novel and non-obvious, WARNING must also be classified in groups G06F 16/907, Group G06F 16/787 is incomplete pending G06F 16/907, and G06F 16/909. reclassification of documents from groups **WARNING** G06F 16/78 and G06F 16/7867. Groups G06F 16/78, G06F 16/7867, and Group G06F 16/90 is impacted by G06F 16/787 should be considered in order reclassification into group G06F 16/906. to perform a complete search. Groups G06F 16/90 and G06F 16/906 should be considered in order to perform a complete 16/80 . of semi-structured data, e.g. markup language search. structured data such as SGML, XML or HTML (content-based retrieval of web data G06F 16/95) 16/901 . . Indexing; Data structures therefor; Storage Indexing, e.g. XML tags; Data structures therefor; 16/81 structures (for retrieval from the web Storage structures G06F 16/951) 16/9014 • • {hash tables} WARNING • • • {using directory or table look-up (use of a 16/9017 Group G06F 16/81 is incomplete pending directory or look-up table in file systems reclassification of documents from group G06F 16/13)} G06F 16/83. • • • {using more than one table in sequence, i.e. 16/902 Groups G06F 16/83 and G06F 16/81 should systems with three or more layers} be considered in order to perform a complete • • • {Graphs; Linked lists (G06F 16/9027 takes 16/9024 search. precedence)} . . . {Trees} 16/9027 16/83 . . Querying 16/903 . . Querying (for retrieval from the web **WARN**ING G06F 16/953) Group G06F 16/83 is impacted by WARNING reclassification into groups G06F 16/81 and Group G06F 16/903 is impacted by G06F 16/835. reclassification into group G06F 16/9035. Groups G06F 16/83, G06F 16/81, and Groups G06F 16/903 and G06F 16/9035 G06F 16/835 should be considered in order to should be considered in order to perform a perform a complete search. complete search. 16/832 . . . Query formulation . . . Query formulation 16/9032 16/835 . . . Query processing 16/90324 . . . {using system suggestions} **WARNING** 16/90328 {using search space presentation or Group G06F 16/835 is incomplete pending visualization, e.g. category or range reclassification of documents from group presentation and selection} G06F 16/83. 16/90332 {Natural language query formulation or dialogue systems} Groups G06F 16/83 and G06F 16/835 16/90335 . . . {Query processing} should be considered in order to perform a • • • {by using parallel associative memories or complete search. 16/90339 content-addressable memories} • • • {Query translation} 16/8358 16/90344 {by using string matching techniques} 16/8365 . . . {Query optimisation} 16/90348 {by searching ordered data, e.g. alpha-16/8373 . . . {Query execution} numerically ordered data} 16/838 . . . Presentation of query results 16/9035 . . . Filtering based on additional data, e.g. user or 16/84 . . Mapping; Conversion group profiles 16/86 . . . {Mapping to a database} WARNING 16/88 {Mark-up to mark-up conversion (conversion Group G06F 16/9035 is incomplete pending for visualization in web browsing reclassification of documents from group G06F 16/9577)} G06F 16/903. 16/90 . Details of database functions independent of the retrieved data types Groups G06F 16/903 and G06F 16/9035 should be considered in order to perform a NOTE complete search. In groups G06F 16/90, G06F 16/901, 16/9038 . . . Presentation of query results G06F 16/9014, G06F 16/9017, G06F 16/902, . . Browsing; Visualisation therefor (for navigating G06F 16/9024, G06F 16/9027, G06F 16/903, 16/904 G06F 16/9032, G06F 16/90324, the web G06F 16/954; browsing optimisation for G06F 16/90328, G06F 16/90332, the web G06F 16/957) G06F 16/90335, G06F 16/90339, G06F 16/90344, G06F 16/90348, G06F 16/9035, G06F 16/9038, G06F 16/904,

and G06F 16/906, subject matter relevant to

16/906 . . Clustering; Classification 16/953 . . . Querying, e.g. by the use of web search engines WARNING WARNING Group G06F 16/906 is incomplete pending Group G06F 16/953 is incomplete pending reclassification of documents from group reclassification of documents from group G06F 16/90. G06F 16/951. Groups G06F 16/90 and G06F 16/906 should Groups G06F 16/951 and G06F 16/953 be considered in order to perform a complete should be considered in order to perform a search. complete search. 16/907 . . Retrieval characterised by using metadata, 16/9532 . . . Query formulation e.g. metadata not derived from the content or WARNING metadata generated manually Group G06F 16/9532 is incomplete **WARNING** pending reclassification of documents Group G06F 16/907 is impacted by from group G06F 16/951. reclassification into groups G06F 16/908 and Groups G06F 16/951 and G06F 16/9532 G06F 16/909. should be considered in order to perform Groups G06F 16/907, G06F 16/908, and a complete search. G06F 16/909 should be considered in order to . . . Search customisation based on user profiles 16/9535 perform a complete search. and personalisation 16/908 . . . using metadata automatically derived from the WARNING content Group G06F 16/9535 is impacted by WARNING reclassification into groups G06F 16/9536 Group G06F 16/908 is incomplete pending and G06F 16/9538. reclassification of documents from group Groups G06F 16/9535, G06F 16/9536, G06F 16/907. and G06F 16/9538 should be considered Groups G06F 16/907 and G06F 16/908 in order to perform a complete search. should be considered in order to perform a 16/9536 Search customisation based on social or complete search. collaborative filtering 16/909 . . . using geographical or spatial information, e.g. **WARNING** location (spatiotemporally dependent retrieval Group G06F 16/9536 is incomplete from the web G06F 16/9537) pending reclassification of documents **WARNING** from group G06F 16/9535. Group G06F 16/909 is incomplete pending Groups G06F 16/9535 and G06F 16/9536 reclassification of documents from group should be considered in order to perform G06F 16/907. a complete search. Groups G06F 16/907 and G06F 16/909 . . . Spatial or temporal dependent retrieval, e.g. 16/9537 should be considered in order to perform a spatiotemporal queries complete search. . . . Presentation of query results 16/9538 16/93 . . Document management systems WARNING 16/94 • • • {Hypermedia (Hyperlinking G06F 40/134)} Group G06F 16/9538 is incomplete . . Retrieval from the web 16/95 pending reclassification of documents 16/951 . . . Indexing; Web crawling techniques from groups G06F 16/951 and **WARNING** G06F 16/9535. Group G06F 16/951 is impacted by Groups G06F 16/951, G06F 16/9535, and reclassification into groups G06F 16/953, G06F 16/9538 should be considered in G06F 16/9532 and G06F 16/9538. order to perform a complete search. All groups listed in this Warning should be 16/954 . . . Navigation, e.g. using categorised browsing considered in order to perform a complete 16/955 . . . using information identifiers, e.g. uniform search. resource locators [URL] 16/9554 • • • {by using bar codes} . . . {Details of hyperlinks; Management of 16/9558 linked annotations} 16/9562 . . . {Bookmark management} • • • {URL specific, e.g. using aliases, detecting 16/9566 broken or misspelled links} 16/957 . . . Browsing optimisation, e.g. caching or content distillation 16/9574 • • • { of access to content, e.g. by caching }

16/9577	• • • {Optimising the visualization of content, e.g.	17/175 • • • {of multidimensional data}
16/958	distillation of HTML documents} Organisation or management of web site	17/18 • for evaluating statistical data {, e.g. average values, frequency distributions, probability
1.6/072	content, e.g. publishing, maintaining pages or automatic linking	functions, regression analysis (forecasting specially adapted for a specific administrative,
16/972	 • • • {Access to data in other repository systems, e.g. legacy data or dynamic Web page generation} 	business or logistic context <u>G06Q 10/04</u>)} 17/40 Data acquisition and logging (for input to computer <u>G06F 3/00</u>)
16/986	• • • {Document structures and storage, e.g. HTML extensions}	18/00 Pattern recognition
17/00	District and the second	18/10 • Pre-processing; Data cleansing
17/00	Digital computing or data processing equipment or methods, specially adapted for specific functions	 Statistical pre-processing, e.g. techniques for normalisation or restoring missing data
	(information retrieval, database structures or file system structures therefor <u>G06F 16/00</u>)	18/20 • Analysing
17/10	Complex mathematical operations {(function)	 18/21 . Design or setup of recognition systems or techniques; Extraction of features in feature
17/10	generation by table look-up <u>G06F 1/03</u> ; evaluation of elementary functions by calculation	space; Blind source separation
17/11	G06F 7/544)} • for solving equations {, e.g. nonlinear equations,	18/211 Selection of the most significant subset of features
1//11	general mathematical optimization problems (optimization specially adapted for a specific	18/2111 by using evolutionary computational techniques, e.g. genetic algorithms
	administrative, business or logistic context	18/2113 by ranking or filtering the set of features,
17/12	G06Q 10/04)} • Simultaneous equations {, e.g. systems of	e.g. using a measure of variance or of feature cross-correlation
1//12	linear equations { , e.g. systems of	18/2115 by evaluating different subsets according to an optimisation criterion, e.g. class
17/13	• • Differential equations (using digital differential analysers G06F 7/64)	separability, forward selection or backward elimination
17/14	Fourier, Walsh or analogous domain	18/213 Feature extraction, e.g. by transforming the
	transformations {, e.g. Laplace, Hilbert,	feature space; Summarisation; Mappings, e.g.
	Karhunen-Loeve, transforms (for correlation	subspace methods
	function computation <u>G06F 17/156</u> ; spectrum analysers <u>G01R 23/16</u>)}	18/2131 based on a transform domain processing, e.g. wavelet transform
17/141	• • {Discrete Fourier transforms}	18/2132 based on discrimination criteria, e.g.
17/142	 {Fast Fourier transforms, e.g. using a Cooley-Tukey type algorithm} 	discriminant analysis
17/144	• • • {Prime factor Fourier transforms, e.g.	18/21322 {Rendering the within-class scatter matrix non-singular}
	Winograd transforms, number theoretic transforms}	18/21324 { involving projections, e.g. Fisherface techniques }
17/145	• • • {Square transforms, e.g. Hadamard, Walsh, Haar, Hough, Slant transforms}	18/21326 {involving optimisations, e.g. using regularisation techniques}
17/147	• • • {Discrete orthonormal transforms, e.g. discrete cosine transform, discrete sine transform,	18/21328 {involving subspace restrictions, e.g. nullspace techniques}
	and variations therefrom, e.g. modified	18/2133 based on naturality criteria, e.g. with non-
	discrete cosine transform, integer transforms	negative factorisation or negative correlation
17/140	approximating the discrete cosine transform (G06F 17/145 takes precedence)}	18/2134 based on separation criteria, e.g. independent component analysis
17/148 17/15	 {Wavelet transforms}. Correlation function computation {including	18/21342 {using statistical independence, i.e.
17/13	computation of convolution operations (arithmetic circuits for sum of products per	minimising mutual information or maximising non-gaussianity}
	se, e.g. multiply-accumulators G06F 7/5443;	18/21343 {using decorrelation or non-stationarity,
	digital filters, e.g. FIR, IIR, adaptive filters	e.g. minimising lagged cross-correlations}
	<u>H03H 17/00</u>)}	18/21345 {enforcing sparsity or involving a domain transformation}
17/153	• • • {Multidimensional correlation or convolution}	18/21347 {using domain transformations}
17/156	 • (using a domain transform, e.g. Fourier transform, polynomial transform, number 	18/21348 {overcoming non-stationarity or
	theoretic transform}	permutations}
17/16	• • Matrix or vector computation {, e.g. matrix-	18/2135 based on approximation criteria, e.g.
	matrix or matrix-vector multiplication, matrix	principal component analysis 18/21355 {nonlinear criteria, e.g. embedding a}
17/17	factorization (matrix transposition G06F 7/78)}	18/21355 { nonlinear criteria, e.g. embedding a manifold in a Euclidean space}
17/17	Function evaluation by approximation methods, e.g. inter- or extrapolation, smoothing, least mean	18/2136 based on sparsity criteria, e.g. with an
	square method ({G06F 17/18 takes precedence }; interpolation for numerical control G05B 19/18)	overcomplete basis
	-	

18/2137	• • • based on criteria of topology preservation, e.g. multidimensional scaling or self-	18/2433 Single-class perspective, e.g. one-against- all classification; Novelty detection; Outlier
	organising maps	detection
18/21375		18/245 relating to the decision surface
10,210,0	embedding of pattern manifold}	18/2451 linear, e.g. hyperplane
18/214	Generating training patterns; Bootstrap	18/2453 non-linear, e.g. polynomial classifier
	methods, e.g. bagging or boosting	18/24765 {Rule-based classification}
18/2148	{characterised by the process organisation or	18/25 . Fusion techniques
	structure, e.g. boosting cascade}	18/251 { of input or preprocessed data}
18/2155	{characterised by the incorporation of	18/253 {of extracted features}
	unlabelled data, e.g. multiple instance	18/254 {of classification results, e.g. of results related
	learning [MIL], semi-supervised techniques	to same input data}
	using expectation-maximisation [EM] or	18/256 { of results relating to different input data,
10/21/2	naïve labelling}	e.g. multimodal recognition}
18/2163	• • {Partitioning the feature space}	18/257 {Belief theory, e.g. Dempster-Shafer}
18/217	• • {Validation; Performance evaluation; Active	18/259 {Fusion by voting}
18/2178	pattern learning techniques } {based on feedback of a supervisor}	18/26 . Discovering frequent patterns
18/2178	{based on reedback of a supervisor} {the supervisor being an automated	18/27 Regression, e.g. linear or logistic regression
10/2103	module, e.g. intelligent oracle}	18/28 Determining representative reference patterns,
18/2193	• • • {based on specific statistical tests}	e.g. by averaging or distorting; Generating
18/22	Matching criteria, e.g. proximity measures	dictionaries
18/23	Clustering techniques	18/285 • • {Selection of pattern recognition techniques, e.g.
18/231	Hierarchical techniques, i.e. dividing or	of classifiers in a multi-classifier system}
10/231	merging pattern sets so as to obtain a	18/29 {Graphical models, e.g. Bayesian networks}
	dendrogram	18/295 {Markov models or related models, e.g. semi-
18/232	Non-hierarchical techniques	Markov models; Markov random fields; Networks embedding Markov models}
18/2321	• • • using statistics or function optimisation, e.g.	18/30 • Post-processing
	modelling of probability density functions	18/40 • Software arrangements specially adapted for
18/23211	• • • • with adaptive number of clusters	pattern recognition, e.g. user interfaces or toolboxes
18/23213		therefor
	means clustering	18/41 {Interactive pattern learning with a human
18/2323	based on graph theory, e.g. minimum	18/41 • • {Interactive pattern learning with a human teacher}
	• • • based on graph theory, e.g. minimum spanning trees [MST] or graph cuts	teacher}
18/2325	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation 	teacher} 21/00 Security arrangements for protecting computers,
18/2325 18/2337	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering 	teacher} 21/00 Security arrangements for protecting computers, components thereof, programs or data against
18/2325 18/2337 18/24	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering . Classification techniques 	teacher} 21/00 Security arrangements for protecting computers,
18/2325 18/2337	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. 	teacher} 21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity
18/2325 18/2337 18/24 18/241	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television
18/2325 18/2337 18/24	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering . Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material
18/2325 18/2337 18/24 18/241	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television
18/2325 18/2337 18/24 18/241 18/2411	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering . Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE
18/2325 18/2337 18/24 18/241 18/2411	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering . Classification techniques . relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions
18/2325 18/2337 18/24 18/241 18/2411 18/2413	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns (Distances to prototypes) (Distances to cluster centroïds) 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE
18/2325 18/2337 18/24 18/241 18/2411 18/2413	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated:
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24133 18/24137 18/2414	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded.
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24133	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • • {by binding digital rights to specific entities}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24133 18/24137 18/2414	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • • {by binding digital rights to specific entities} 21/1011 • • {to devices}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24143	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns Qbistances to prototypes Smoothing the distance, e.g. radial basis function networks [RBFN] Spistances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN] 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • • {to devices} 21/1012 • • {to domains}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24143	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns Qbistances to prototypes {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1012 • {to devices} 21/1013 • {to locations}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24133 18/24137 18/2414 18/24143	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns [Distances to prototypes] [Distances to cluster centroïds] {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • {to devices} 21/1012 • {to domains} 21/1013 • • {to locations} 21/1014 • • {to tokens}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24143	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} based on parametric or probabilistic models, 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • • {to devices} 21/1012 • • {to domains} 21/1013 • • {to locations} 21/1014 • • {to tokens} 21/1015 • • {to users}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24133 18/24137 18/2414 18/24143	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns [Distances to prototypes] [Distances to cluster centroïds] {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • • {to devices} 21/1012 • • {to domains} 21/1013 • • {to locations} 21/1014 • • {to tokens} 21/1015 • • {Arrangements for software license management}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24133 18/24137 18/2414 18/24143	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} based on parametric or probabilistic models, e.g. based on likelihood ratio or false acceptance rate versus a false rejection rate 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • • {to devices} 21/1012 • • {to domains} 21/1013 • • {to locations} 21/1014 • • {to tokens} 21/1015 • • {to users} • {Arrangements for software license management or administration, e.g. for managing licenses at
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24133 18/24137 18/2414 18/24143	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} based on parametric or probabilistic models, e.g. based on likelihood ratio or false 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • {to devices} 21/1012 • {to domains} 21/1013 • • {to locations} 21/1014 • • {to tokens} 21/1015 • • {to users} • {Arrangements for software license management or administration, e.g. for managing licenses at corporate level}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24143 18/24145 18/24145	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns {Distances to prototypes} {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} based on parametric or probabilistic models, e.g. based on likelihood ratio or false acceptance rate versus a false rejection rate {Bayesian classification} 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • {to devices} 21/1012 • {to domains} 21/1013 • • {to locations} 21/1014 • • {to tokens} 21/1015 • • {to users} 1/1015 • • {Arrangements for software license management or administration, e.g. for managing licenses at corporate level}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24143 18/24145 18/2415 18/243 18/2431	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns Qbistances to prototypes {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} based on parametric or probabilistic models, e.g. based on likelihood ratio or false acceptance rate versus a false rejection rate {Bayesian classification} relating to the number of classes 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1012 • {to devices} 21/1013 • • {to domains} 21/1014 • • {to tokens} 21/1015 • • {to users} 21/1016 • • {Arrangements for software license management or administration, e.g. for managing licenses at corporate level} 21/106 • • {Enforcing content protection by specific content
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24143 18/24145 18/2415 18/243 18/2431	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns Qbistances to prototypes {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} based on parametric or probabilistic models, e.g. based on likelihood ratio or false acceptance rate versus a false rejection rate {Bayesian classification} relating to the number of classes Multiple classes 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 • Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: • "content" means any intellectually created work whose copyright is to be safeguarded. 21/101 • {by binding digital rights to specific entities} 21/1011 • {to devices} 21/1012 • {to domains} 21/1013 • {to locations} 21/1014 • {to tokens} 21/1015 • {Arrangements for software license management or administration, e.g. for managing licenses at corporate level} 21/106 • {Enforcing content protection by specific content processing}
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24147 18/24145 18/2415 18/2431 18/2431 18/24317	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns Qbistances to prototypes {Distances to cluster centroïds} {Smoothing the distance, e.g. radial basis function networks [RBFN]} {Distances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} {Distances to closest patterns, e.g. nearest neighbour classification} based on parametric or probabilistic models, e.g. based on likelihood ratio or false acceptance rate versus a false rejection rate {Bayesian classification} relating to the number of classes Multiple classes {Piecewise classification, i.e. whereby each classification requires several discriminant rules} 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated: 'content" means any intellectually created work whose copyright is to be safeguarded. 21/101
18/2325 18/2337 18/24 18/241 18/2411 18/2413 18/24137 18/2414 18/24147 18/24145 18/2415 18/2431 18/2431 18/24317	 based on graph theory, e.g. minimum spanning trees [MST] or graph cuts using vector quantisation using fuzzy logic, i.e. fuzzy clustering Classification techniques relating to the classification model, e.g. parametric or non-parametric approaches based on the proximity to a decision surface, e.g. support vector machines based on distances to training or reference patterns Qbistances to prototypes Smoothing the distance, e.g. radial basis function networks [RBFN]} Gbistances to neighbourhood prototypes, e.g. restricted Coulomb energy networks [RCEN]} Gbistances to closest patterns, e.g. nearest neighbour classification based on parametric or probabilistic models, e.g. based on likelihood ratio or false acceptance rate versus a false rejection rate Bayesian classification relating to the number of classes Multiple classes Piecewise classification, i.e. whereby each classification requires several discriminant 	21/00 Security arrangements for protecting computers, components thereof, programs or data against unauthorised activity 21/10 Protecting distributed programs or content, e.g. vending or licensing of copyrighted material (protection in video systems or pay television H04N 7/16) {; Digital rights management [DRM]} NOTE In this group, the following terms or expressions are used with the meaning indicated:

21/1066	• • {Hiding content}	21/43	wireless channels
21/1000	{License processing; Key processing}	21/43	Program or device authentication
21/107	{Conversion}	21/445	• • • {by mutual authentication, e.g. between devices
21/1073	{Conversion} {Definition}	21/443	or programs}
21/10/4	{Editing}	21/45	Structures or tools for the administration of
21/1075	{Revocation}	21/13	authentication
21/10/0	{Recurrent authorisation}	21/46	by designing passwords or checking the
21/10/7		21/10	strength of passwords
21/10/8	 {Logging; Metering} {Return}	21/50	Monitoring users, programs or devices to maintain
			the integrity of platforms, e.g. of processors,
21/108	 {Transfer of content, software, digital rights or licenses} 		firmware or operating systems
21/1082	{Backup or restore}	21/51	at application loading time, e.g. accepting,
21/1082	Partial license transfers		rejecting, starting or inhibiting executable
21/1083	 { ratual incense transfers } { via third party }		software based on integrity or source reliability
21/1084	The state of the s	21/52	 during program execution, e.g. stack integrity
21/1085	• • {Content sharing, e.g. peer-to-peer [P2P]}		{; Preventing unwanted data erasure; Buffer
21/1086	{Superdistribution}		overflow}
	 {Synchronisation} {by using transactions with atomicity,	21/53	• • • by executing in a restricted environment, e.g.
21/1088	consistency, or isolation and durability [ACID]		sandbox or secure virtual machine
	properties}	21/54	• • • by adding security routines or objects to
21/109	• {by using specially-adapted hardware at the		programs
21/109	client}	21/55	Detecting local intrusion or implementing
21/12	Protecting executable software		counter-measures
21/121	{Restricting unauthorised execution of	21/552	• • • {involving long-term monitoring or reporting}
21/121	programs}	21/554	• • • {involving event detection and direct action}
21/123	• • • {by using dedicated hardware, e.g. dongles,	21/556	• • • {involving covert channels, i.e. data leakage
21/123	smart cards, cryptographic processors, global		between processes (inhibiting the analysis of
	positioning systems [GPS] devices}		circuitry or operation with measures against
21/125	• • • • {by manipulating the program code, e.g.	21/56	power attack G06F 21/755)}
	source code, compiled code, interpreted	21/56	 Computer malware detection or handling, e.g. anti-virus arrangements
	code, machine code}	21/561	-
21/126	• • • • {Interacting with the operating system}	21/562	{Virus type analysis}
21/128	• • • {involving web programs, i.e. using		{Static detection}
	technology especially used in internet,	21/563	• • • • {by source code analysis}
	generally interacting with a web browser,	21/564	• • • • {by virus signature recognition}
	e.g. hypertext markup language [HTML],	21/565	• • • • {by checking file integrity}
	applets, java}	21/566	{Dynamic detection, i.e. detection performed
21/14	• • • against software analysis or reverse		at run-time, e.g. emulation, suspicious activities}
	engineering, e.g. by obfuscation	21/567	• • • {using dedicated hardware}
21/16	• Program or content traceability, e.g. by	21/568	{eliminating virus, restoring damaged files}
	watermarking		Certifying or maintaining trusted computer
21/30	• Authentication, i.e. establishing the identity or	21/57	platforms, e.g. secure boots or power-downs,
	authorisation of security principals		version controls, system software checks, secure
21/305	• • {by remotely controlling device operation}		updates or assessing vulnerabilities
21/31	User authentication	21/572	Secure firmware programming, e.g. of basic
21/313	• • • {using a call-back technique via a telephone	21/3/2	input output system [BIOS]}
	network}	21/575	• • • {Secure boot}
21/316	• • • {by observing the pattern of computer usage,	21/577	• • • (Assessing vulnerabilities and evaluating
	e.g. typical user behaviour}	21/3//	computer system security}
21/32	• • using biometric data, e.g. fingerprints, iris	21/60	• Protecting data
21/22	scans or voiceprints	21/602	• • {Providing cryptographic facilities or services}
21/33	using certificates	21/604	• • {Tools and structures for managing or
21/335	• • • { for accessing specific resources, e.g. using	21,004	administering access control systems}
21/24	Kerberos tickets}	21/606	• • {by securing the transmission between two
21/34	involving the use of external additional devices,		devices or processes}
21/25	e.g. dongles or smart cards	21/608	• • • {Secure printing}
21/35	by graphic or iconic paragraphs to the	21/62	• Protecting access to data via a platform, e.g. using
21/36	• • by graphic or iconic representation	· - ·	keys or access control rules
21/40	by quorum, i.e. whereby two or more security	21/6209	• • • {to a single file or object, e.g. in a secure
21/41	principals are required	– * *	envelope, encrypted and accessed using a key,
21/41	where a single sign-on provides access to a		or with access control rules appended to the
21/42	plurality of computers		object itself}
21/42	using separate channels for security data		

21/6218	• • • {to a system of files or objects, e.g. local or	21/88	Detecting or preventing theft or loss
21/6227	distributed file system or database} {where protection concerns the structure of	30/00	Computer-aided design [CAD]
	data, e.g. records, types, queries}		NOTE
21/6236 21/6245	 {between heterogeneous systems} {Protecting personal data, e.g. for financial		In this group, it is desirable to add the indexing codes of groups <u>G06F 2111/00</u> - <u>G06F 2119/00</u> .
21/6254	or medical purposes} {by anonymising data, e.g. decorrelating		WARNING
21/62/2	personal data from the owner's identification}		Group G06F 30/00 is impacted by reclassification into groups G06F 30/10, G06F 30/12,
21/6263	{during internet communication, e.g. revealing personal data from cookies}		G06F 2111/00 - G06F 2119/22.
21/6272	• • • • {by registering files or documents with a third party}		Groups <u>G06F 30/00</u> , <u>G06F 30/10</u> , <u>G06F 30/12</u> , and <u>G06F 2111/00</u> - <u>G06F 2119/22</u> should be considered in order to perform a complete search.
21/6281	• • • { at program execution time, where the protection is within the operating system}	30/10	• Geometric CAD
21/629	• • • {to features or functions of an application}		WARNING
21/64	 Protecting data integrity, e.g. using checksums, certificates or signatures 		Group G06F 30/10 is incomplete pending
21/645	• • { using a third party }		reclassification of documents from group
21/70	Protecting specific internal or peripheral		<u>G06F 30/00</u> .
	components, in which the protection of a component leads to protection of the entire computer		Groups <u>G06F 30/00</u> and <u>G06F 30/10</u> should be considered in order to perform a complete
21/71	to assure secure computing or processing of		search.
21/72	information	30/12	characterised by design entry means specially
21/72 21/725	in cryptographic circuits{operating on a secure reference time value}		adapted for CAD, e.g. graphical user interfaces
21/723	 {operating on a secure reference time value} by creating or determining hardware identification, e.g. serial numbers 		[GUI] specially adapted for CAD WARNING
21/74	operating in dual or compartmented mode, i.e.		Group G06F 30/12 is incomplete pending
21/75	at least one secure mode by inhibiting the analysis of circuitry or		reclassification of documents from groups G06F 30/00, G06F 30/17, and G06F 30/18.
21/75	operation		All groups listed in this Warning should be
21/755	• • • { with measures against power attack}		considered in order to perform a complete
21/76	in application-specific integrated circuits		search.
	[ASIC] or field-programmable devices, e.g.	30/13	Architectural design, e.g. computer-aided
	field-programmable gate arrays [FPGA] or programmable logic devices [PLD]	30/13	architectural design [CAAD] related to design of
21/77	in smart cards		buildings, bridges, landscapes, production plants
21/78	to assure secure storage of data (address-		or roads
	based protection against unauthorised use of	30/15	Vehicle, aircraft or watercraft design
	memory <u>G06F 12/14</u> ; record carriers for use with	30/17	Mechanical parametric or variational design
	machines and with at least a part designed to carry digital markings G06K 19/00)		WARNING
21/79	• • in semiconductor storage media, e.g. directly-		Group G06F 30/17 is impacted by
=1,7,9	addressable memories		reclassification into groups G06F 30/12 and
21/80	• • • in storage media based on magnetic or optical		G06F 2111/00 - G06F 2119/22.
	technology, e.g. disks with sectors (preventing		Groups <u>G06F 30/17</u> , <u>G06F 30/12</u> and <u>G06F 2111/00</u> - <u>G06F 2119/22</u> should be
	unauthorised reproduction or copying of disc- type recordable media <u>G11B 20/00</u>)		considered in order to perform a complete
21/805	• • • {using a security table for the storage subsystem}		search.
21/81	by operating on the power supply, e.g. enabling or disabling power-on, sleep or resume operations		
21/82	Protecting input, output or interconnection devices		
21/83	input devices, e.g. keyboards, mice or controllers thereof		
21/84	• • • output devices, e.g. displays or monitors		
21/85	• • interconnection devices, e.g. bus-connected or in-line devices		
21/86	Secure or tamper-resistant housings		
21/87	• • • by means of encapsulation, e.g. for integrated circuits		

30/18 . Network design, e.g. design based on topological or interconnect aspects of utility systems, piping, heating ventilation air conditioning [HVAC] or cabling (circuit design at the physical level G06F 30/39; network planning tools for wireless communication networks H04W 16/18)

WARNING

Group <u>G06F 30/18</u> is impacted by reclassification into groups <u>G06F 30/12</u> and <u>G06F 2111/00</u> - <u>G06F 2119/22</u>.
Groups <u>G06F 30/18</u>, <u>G06F 30/12</u> and

Groups G06F 30/18, G06F 30/12 and G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete search.

30/20 Design optimisation, verification or simulation (optimisation, verification or simulation of circuit designs G06F 30/30)

WARNING

Group <u>G06F 30/20</u> is impacted by reclassification into groups <u>G06F 30/25</u>, <u>G06F 30/27</u>, <u>G06F 30/28</u> and <u>G06F 2111/00</u> - <u>G06F 2119/22</u>.

Groups <u>G06F 30/20</u>, <u>G06F 30/25</u>, <u>G06F 30/27</u>, <u>G06F 30/28</u> and <u>G06F 2111/00</u> - <u>G06F 2119/22</u> should be considered in order to perform a complete search.

30/22 . . using Petri net models

30/23 . using finite element methods [FEM] or finite difference methods [FDM]

WARNING

Group G06F 30/23 is impacted by reclassification into groups G06F 30/25, G06F 30/367, G06F 30/398 and G06F 2111/00 - G06F 2119/22.

Groups G06F 30/23, G06F 30/25, G06F 30/367, G06F 30/398 and G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete search.

30/25 . . using particle-based methods

WARNING

Group <u>G06F 30/25</u> is incomplete pending reclassification of documents from groups <u>G06F 30/20</u> and <u>G06F 30/23</u>.

Groups G06F 30/20, G06F 30/23, and G06F 30/25 should be considered in order to perform a complete search.

 30/27 . using machine learning, e.g. artificial intelligence, neural networks, support vector machines [SVM] or training a model

WARNING

Group G06F 30/27 is incomplete pending reclassification of documents from group G06F 30/20.

Groups <u>G06F 30/20</u> and <u>G06F 30/27</u> should be considered in order to perform a complete search.

30/28 . . using fluid dynamics, e.g. using Navier-Stokes equations or computational fluid dynamics [CFD]

Group G06F 30/30 is impacted by

WARNING

Group G06F 30/28 is incomplete pending reclassification of documents from group G06F 30/20.

Groups $\underline{\text{G06F }30/20}$ and $\underline{\text{G06F }30/28}$ should be considered

30/30 . Circuit design

WARNING

reclassification into groups G06F 30/31, G06F 30/32, G06F 30/323, G06F 30/333, G06F 30/347, G06F 30/34, G06F 30/343, G06F 30/347, G06F 30/38 and G06F 2111/00 - G06F 2119/22.

Groups G06F 30/30, G06F 30/31, G06F 30/32, G06F 30/323, G06F 30/333, G06F 30/347, G06F 30/34, G06F 30/343, G06F 30/347, G06F 30/38 and G06F 2111/00 - G06F 2119/22 should be considered in order to perform a

30/31 • Design entry, e.g. editors specifically adapted for circuit design

WARNING

complete search.

Group G06F 30/31 is incomplete pending reclassification of documents from groups G06F 30/30, G06F 30/34, and G06F 30/36.

All groups listed in this Warning should be considered in order to perform a complete search.

30/32 • Circuit design at the digital level (reconfigurable circuits G06F 30/34)

WARNING

Group <u>G06F 30/32</u> is incomplete pending reclassification of documents from group <u>G06F 30/30</u>.

Groups <u>G06F 30/30</u> and <u>G06F 30/32</u> should be considered in order to perform a complete search.

 Translation or migration, e.g. logic to logic, hardware description language [HDL] translation or netlist translation

WARNING

Group <u>G06F 30/323</u> is incomplete pending reclassification of documents from groups <u>G06F 30/30</u> and <u>G06F 30/327</u>.

Groups <u>G06F 30/30</u>, <u>G06F 30/327</u>, and <u>G06F 30/323</u> should be considered in order to perform a complete search.

CPC - 2025.05 43

30/323

Design for testability [DFT], e.g. scan chain or 30/327 . . . Logic synthesis; Behaviour synthesis, e.g. 30/333 mapping logic, HDL to netlist, high-level built-in self-test [BIST] language to RTL or netlist WARNING WARNING Group G06F 30/333 is incomplete pending Group G06F 30/327 is impacted by reclassification of documents from group G06F 30/30. reclassification into groups G06F 30/323 and G06F 2111/00 - G06F 2119/22. Groups G06F 30/30 and G06F 30/333 Groups G06F 30/327, G06F 30/323 and should be considered in order to perform a G06F 2111/00 - G06F 2119/22 should be complete search. considered in order to perform a complete 30/337 . . . Design optimisation search. WARNING 30/33 Design verification, e.g. functional simulation Group G06F 30/337 is incomplete pending or model checking reclassification of documents from group WARNING G06F 30/30. Group G06F 30/33 is impacted Groups G06F 30/30 and G06F 30/337 by reclassification into groups should be considered in order to perform a G06F 30/3308, G06F 30/3315 and complete search. G06F 2111/00 - G06F 2119/22. 30/34 . . for reconfigurable circuits, e.g. field Groups G06F 30/33, programmable gate arrays [FPGA] or G06F 30/3308, G06F 30/3315 and programmable logic devices [PLD] G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete **WARNING** search. Group G06F 30/34 is incomplete pending reclassification of documents from group 30/3308 . . . using simulation G06F 30/30. **WARNING** Group G06F 30/34 is impacted by Group G06F 30/3308 is incomplete reclassification into groups G06F 30/31, pending reclassification of documents G06F 30/343, G06F 30/347 and from group G06F 30/33. G06F 2111/00 - G06F 2119/22. Groups G06F 30/33 and G06F 30/3308 Groups G06F 30/34, G06F 30/31, should be considered in order to perform G06F 30/343, G06F 30/347 and a complete search. G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete 30/331 with hardware acceleration, e.g. by using search. field programmable gate array [FPGA] or emulation 30/343 . . . Logical level 30/3312 Timing analysis **WARNING WARNING** Group G06F 30/343 is incomplete pending Group G06F 30/3312 is reclassification of documents from groups impacted by reclassification G06F 30/30 and G06F 30/34. into groups G06F 30/3315 and Groups G06F 30/30, G06F 30/34, and G06F 2111/00 - G06F 2119/22. G06F 30/343 should be considered in order Groups G06F 30/3312, G06F 30/3315 to perform a complete search. and G06F 2111/00 - G06F 2119/22 30/347 . . . Physical level, e.g. placement or routing should be considered in order to perform a complete search. **WARNING** 30/3315 . . . using static timing analysis [STA] Group G06F 30/347 is incomplete pending reclassification of documents from groups **WARNING** G06F 30/30, G06F 30/34, and G06F 30/39. Group G06F 30/3315 is incomplete Groups G06F 30/347, G06F 30/30, pending reclassification of documents G06F 30/34 and G06F 30/39 should be from groups G06F 30/33 and considered in order to perform a complete G06F 30/3312. search. Groups G06F 30/33, G06F 30/3312, and 30/35 G06F 30/3315 should be considered in . Delay-insensitive circuit design, e.g. asynchronous or self-timed order to perform a complete search. using formal methods, e.g. equivalence

CPC - 2025.05 44

checking or property checking

30/36 . Circuit design at the analogue level

WARNING

Group G06F 30/36 is impacted by reclassification into groups G06F 30/31, G06F 30/373, G06F 30/38 and G06F 2111/00 - G06F 2119/22.

Groups G06F 30/36, G06F 30/31, G06F 30/373, G06F 30/38 and G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete search.

 30/367 . . . Design verification, e.g. using simulation, simulation program with integrated circuit emphasis [SPICE], direct methods or relaxation methods

WARNING

Group G06F 30/367 is incomplete pending reclassification of documents from group G06F 30/23.

Groups <u>G06F 30/23</u> and <u>G06F 30/367</u> should be considered in order to perform a complete search.

30/373 . . . Design optimisation

WARNING

Group G06F 30/373 is incomplete pending reclassification of documents from group G06F 30/36.

Groups <u>G06F 30/36</u> and <u>G06F 30/373</u> should be considered in order to perform a complete search.

30/38 • Circuit design at the mixed level of analogue and digital signals

WARNING

Group G06F 30/38 is incomplete pending reclassification of documents from groups G06F 30/30 and G06F 30/36.

Groups G06F 30/30, G06F 30/36, and G06F 30/38 should be considered in order to perform a complete search.

30/39 • Circuit design at the physical level (physical level design for reconfigurable circuits G06F 30/347)

WARNING

Group G06F 30/39 is impacted by reclassification into groups G06F 30/347, G06F 30/396, G06F 30/398 and G06F 2111/00 - G06F 2119/22.

Groups G06F 30/39, G06F 30/347, G06F 30/396, G06F 30/398 and G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete search.

30/392 • • • Floor-planning or layout, e.g. partitioning or placement

WARNING

Group G06F 30/392 is impacted by reclassification into groups G06F 30/396 and G06F 2111/00 - G06F 2119/22.

Groups G06F 30/392, G06F 30/396 and G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete search

30/394 . . . Routing (<u>G06F 30/396</u> takes precedence)

WARNING

Group G06F 30/394 is impacted by reclassification into groups G06F 30/3947, G06F 30/3953, G06F 30/396 and G06F 2111/00 - G06F 2119/22.

Groups G06F 30/394, G06F 30/3947, G06F 30/3953, G06F 30/396 and G06F 2111/00 - G06F 2119/22 should be considered in order to perform a complete search.

30/3947 . . . global

WARNING

Group <u>G06F 30/3947</u> is incomplete pending reclassification of documents from group <u>G06F 30/394</u>.

Groups <u>G06F 30/394</u> and <u>G06F 30/3947</u> should be considered in order to perform a complete search.

30/3953 . . . detailed

WARNING

Group <u>G06F 30/3953</u> is incomplete pending reclassification of documents from group <u>G06F 30/394</u>.

Groups G06F 30/394 and G06F 30/3953 should be considered in order to perform a complete search.

30/396 . . . Clock trees

WARNING

Group G06F 30/396 is incomplete pending reclassification of documents from groups G06F 30/39, G06F 30/392, and G06F 30/394.

Group <u>G06F 30/396</u> is also impacted by reclassification into group G06F 2117/04.

All groups listed in this Warning should be considered in order to perform a complete search.

30/398	• • Design verification or optimisation, e.g.	40/174	Form filling; Merging
	using design rule check [DRC], layout versus	40/177	• • • of tables; using ruled lines
	schematics [LVS] or finite element methods	40/18	• • • of spreadsheets (form-filling G06F 40/174)
	[FEM] (optical proximity correction [OPC] design processes <u>G03F 1/36</u>)	40/183	Tabulation, i.e. one-dimensional positioning
	design processes door 1/30)	40/186	Templates
	<u>WARNING</u>	40/189	Automatic justification
	Group G06F 30/398 is incomplete pending	40/191	Automatic line break hyphenation
	reclassification of documents from groups	40/194	Calculation of difference between files
	$\underline{\text{G06F } 30/23}$ and $\underline{\text{G06F } 30/39}$.	40/197	• • Version control (for software <u>G06F 8/71</u>)
	Groups G06F 30/23, G06F 30/39 and	40/20	Natural language analysis (semantic analysis of
	G06F 30/398 should be considered in order		natural language <u>G06F 40/30</u>)
	to perform a complete search.	40/205	Parsing
40/00	Handling natural language data (speech analysis or	40/211	Syntactic parsing, e.g. based on context-free
40/00	synthesis, speech recognition G10L)	40/216	grammar [CFG] or unification grammars
40/10	Text processing (natural language analysis	40/216 40/221	using statistical methods
40/10	G06F 40/20; semantic analysis G06F 40/30;	40/221	• • • Parsing markup language streams (streaming G06F 40/149)
	processing or translation of natural language	40/226	Validation
	G06F 40/40)	40/232	Orthographic correction, e.g. spell checking or
40/103	• Formatting, i.e. changing of presentation of	40/232	vowelisation
	documents (automatic justification G06F 40/189;	40/237	Lexical tools
	automatic line break hyphenation G06F 40/191)	40/242	Dictionaries
40/106	Display of layout of documents; Previewing	40/247	Thesauruses; Synonyms
40/109	Font handling; Temporal or kinetic typography	40/253	Grammatical analysis; Style critique
40/111	 Mathematical or scientific formatting; 	40/258	Heading extraction; Automatic titling; Numbering
	Subscripts; Superscripts	40/263	Language identification
40/114	Pagination	40/268	Morphological analysis
40/117	• • • Tagging; Marking up (details of markup	40/274	Converting codes to words; Guess-ahead of
	languages <u>G06F 40/143</u>); Designating a	40/2/4	partial word inputs
	block; Setting of attributes (style sheets,	40/279	Recognition of textual entities
	e.g. eXtensible Stylesheet Language Transformation [XSLT], <u>G06F 40/154</u>)	40/284	Lexical analysis, e.g. tokenisation or collocates
40/12		40/289	• • Phrasal analysis, e.g. finite state techniques or
40/12	. Use of codes for handling textual entities Storage facilities	.0/209	chunking
40/123	Character encoding	40/295	Named entity recognition
40/126	Handling non-Latin characters, e.g. kana-to-	40/30	Semantic analysis
40/129	kanji conversion	40/35	Discourse or dialogue representation
40/131	• • • Fragmentation of text files, e.g. creating	40/40	Processing or translation of natural language
40/131	reusable text-blocks; Linking to fragments, e.g.		(natural language analysis G06F 40/20; semantic
	using XInclude; Namespaces		analysis <u>G06F 40/30</u>)
40/134	Hyperlinking	40/42	Data-driven translation
40/137	Hierarchical processing, e.g. outlines	40/44	• • • Statistical methods, e.g. probability models
40/14	Tree-structured documents (parsing	40/45	Example-based machine translation; Alignment
	<u>G06F 40/205</u> ; validation <u>G06F 40/226</u>)	40/47	• • • Machine-assisted translation, e.g. using
40/143	Markup, e.g. Standard Generalized Markup		translation memory
	Language [SGML] or Document Type	40/49	using very large corpora, e.g. the web
	Definition [DTD]	40/51	Translation evaluation
40/146	Coding or compression of tree-structured	40/53	• Processing of non-Latin text (kana-to-kanji
	data		conversion G06F 40/129; vowelisation
40/149	Adaptation of the text data for streaming	40/55	<u>G06F 40/232</u>)
	purposes, e.g. Efficient XML Interchange	40/55	Rule-based translation
40/151	[EXI] format	40/56	Natural language generation
40/151	Transformation	40/58	Use of machine translation, e.g. for multi-lingual retrieval, for server-side translation for client
40/154	Tree transformation for tree-structured or		devices or for real-time translation
	markup documents, e.g. XSLT, XSL-FO or stylesheets		devices of for real-time translation
40/157	• • • using dictionaries or tables	2101/00	Indexing scheme relating to the type of digital
40/15/	Automatic learning of transformation rules,		function generated
TU/ 1U	e.g. from examples	2101/02	• Linear multivariable functions, i.e. sum of products
40/163	Handling of whitespace	2101/04	Trigonometric functions
40/166	Editing, e.g. inserting or deleting	2101/06	Co-ordinate transformations
40/169	Annotation, e.g. comment data or footnotes	2101/08	• Powers or roots
40/171	by use of digital ink	2101/10	Logarithmic or exponential functions
· -	,	2101/12	Reciprocal functions

2101/14 • Probability distribution functions2101/16 • PCM companding functions

Indexing scheme associated with group G06F 30/00, relating to CAD techniques

2111/00 Details relating to CAD techniques

WARNING

Groups G06F 2111/00 - G06F 2111/20 are incomplete pending reclassification of documents from groups G06F 30/00, G06F 30/17, G06F 30/18, G06F 30/20, G06F 30/23, G06F 30/30, G06F 30/327, G06F 30/33, G06F 30/3312, G06F 30/34, G06F 30/36, G06F 30/39, G06F 30/392, and G06F 30/394.

All groups listed in this Warning should be considered in order to perform a complete search.

CAD in a network environment, e.g. collaborative CAD or distributed simulation
 Constraint-based CAD

2111/06 • Multi-objective optimisation, e.g. Pareto optimisation using simulated annealing [SA], ant colony algorithms or genetic algorithms [GA]

2111/08 • Probabilistic or stochastic CAD • Numerical modelling

2111/12 • Symbolic schematics 2111/14 • related to nanotechnology

2111/16 . Customisation or personalisation2111/18 . using virtual or augmented reality

2111/18 . using virtual or augmented reality
 2111/20 . Configuration CAD, e.g. designing by assembling

or positioning modules selected from libraries of predesigned modules

Indexing scheme associated with group G06F 30/00, relating to the application field

2113/00 Details relating to the application field

WARNING

Groups G06F 2113/00 - G06F 2113/28 are incomplete pending reclassification of documents from groups G06F 30/00, G06F 30/17, G06F 30/18, G06F 30/20, G06F 30/23, G06F 30/30, G06F 30/327, G06F 30/33, G06F 30/3312, G06F 30/34, G06F 30/36, G06F 30/39, G06F 30/392, and G06F 30/394.

All groups listed in this Warning should be considered in order to perform a complete search.

2113/02 • Data centres

2113/04 • Power grid distribution networks

2113/06 • Wind turbines or wind farms

2113/08 • Fluids

2113/10 • Additive manufacturing, e.g. 3D printing

2113/12 . Cloth 2113/14 . Pipes

2113/16 • Cables, cable trees or wire harnesses

2113/18 • Chip packaging

2113/20 • Packaging, e.g. boxes or containers

2113/22 . Moulding2113/24 . Sheet material

2113/26 . Composites

2113/28 • Fuselage, exterior or interior

Indexing scheme associated with group G06F 30/00, relating to the type of the circuit

2115/00 Details relating to the type of the circuit

WARNING

Groups G06F 2115/00 - G06F 2115/12 are incomplete pending reclassification of documents from groups G06F 30/00, G06F 30/17, G06F 30/18, G06F 30/20, G06F 30/23, G06F 30/30, G06F 30/327, G06F 30/33, G06F 30/3312, G06F 30/34, G06F 30/36, G06F 30/39, G06F 30/392, and G06F 30/394.

All groups listed in this Warning should be considered in order to perform a complete search.

2115/02 . System on chip [SoC] design
2115/04 . Micro electro-mechanical systems [MEMS]
2115/06 . Structured ASICs
2115/08 . Intellectual property [IP] blocks or IP cores
2115/10 . Processors

• Printed circuit boards [PCB] or multi-chip modules

Indexing scheme associated with group G06F 30/00, relating to the type or aim of the circuit design

2117/00 Details relating to the type or aim of the circuit design

WARNING

Groups G06F 2117/00 - G06F 2117/12 are incomplete pending reclassification of documents from groups G06F 30/00, G06F 30/17, G06F 30/18, G06F 30/20, G06F 30/23, G06F 30/30, G06F 30/327, G06F 30/33, G06F 30/3312, G06F 30/34, G06F 30/36, G06F 30/39, G06F 30/392, and G06F 30/394.

All groups listed in this Warning should be considered in order to perform a complete search.

2117/02 • Fault tolerance, e.g. for transient fault suppression

2117/04 . Clock gating

WARNING

Group G06F 2117/04 is incomplete pending reclassification of documents from groups G06F 30/00, G06F 30/17, G06F 30/18, G06F 30/20, G06F 30/23, G06F 30/30, G06F 30/327, G06F 30/33, G06F 30/312, G06F 30/34, G06F 30/36, G06F 30/39, G06F 30/392, G06F 30/394, and G06F 30/396.

All groups listed in this Warning should be considered in order to perform a complete search.

2117/06 • Spare resources, e.g. for permanent fault suppression

2117/08 . HW-SW co-design, e.g. HW-SW partitioning

2117/10 • Buffer insertion

2117/12 • Sizing, e.g. of transistors or gates

Indexing scheme associated with group G06F 30/00, relating to the purpose - mostly applicable to circuits - but also relevant for general CAD

2119/00 Details relating to the type or aim of the analysis or the optimisation

WARNING

Groups G06F 2119/00 - G06F 2119/22 are incomplete pending reclassification of documents from groups G06F 30/00, G06F 30/17, G06F 30/18, G06F 30/20, G06F 30/23, G06F 30/30, G06F 30/327, G06F 30/33, G06F 30/3312, G06F 30/34, G06F 30/36, G06F 30/39, G06F 30/392, and G06F 30/394.

All groups listed in this Warning should be considered in order to perform a complete search.

2119/02 • Reliability analysis or reliability optimisation; Failure analysis, e.g. worst case scenario performance, failure mode and effects analysis [FMEA]

2119/04 • Ageing analysis or optimisation against ageing 2119/06 Power analysis or power optimisation

Thermal analysis or thermal optimisation 2119/08

2119/10 Noise analysis or noise optimisation

2119/12 Timing analysis or timing optimisation

2119/14 • Force analysis or force optimisation, e.g. static or dynamic forces

 Equivalence checking 2119/16

2119/18 Manufacturability analysis or optimisation for manufacturability

2119/20 Design reuse, reusability analysis or reusability optimisation

2119/22 Yield analysis or yield optimisation

Indexing scheme associated with group G06F 18/00, relating to pattern recognition

2123/00	Data types
2123/02	• in the time domain, e.g. time-series data

2200/00	Indexing scheme relating to G06F 1/04 - G06F 1/32
2200/16	Indexing scheme relating to G06F 1/16 - G06F 1/18
2200/161	Indexing scheme relating to constructional details
	of the monitor
2200/1611	CRT monitor
2200/1612	Flat panel monitor
2200/1613	Supporting arrangements, e.g. for filters or
	documents associated to a laptop display
2200/1614	Image rotation following screen orientation,
	e.g. switching from landscape to portrait mode
2200/163	Indexing scheme relating to constructional details
	of the computer
2200/1631	Panel PC, e.g. single housing hosting PC and
	display panel
2200/1632	Pen holder integrated in the computer

(Frozen)

2200/1633 . . . Protecting arrangement for the entire housing of the computer

WARNING

Group G06F 2200/1633 is no longer used for the classification of documents as of January 1, 2025.

The content of this group is being reclassified into groups A45C 11/003, G06F 1/1629, G06F 1/1656 and H04M 1/0203.

All groups listed in this Warning should be considered in order to perform a complete search

2200/1634 . . . Integrated protective display lid, e.g. for touchsensitive display in handheld computer

2200/1635 . . . Stackable modules

2200/1636 . . . Sensing arrangement for detection of a tap gesture on the housing

2200/1637 . . .

Sensing arrangement for detection of housing movement or orientation, e.g. for controlling scrolling or cursor movement on the display of an handheld computer

2200/1638 . . . Computer housing designed to operate in both desktop and tower orientation

2200/1639 . . . Arrangements for locking plugged peripheral connectors

2200/20 • Indexing scheme relating to G06F 1/20 2200/201 . . Cooling arrangements using cooling fluid

2200/202 . . Air convective hinge

2200/203 . Heat conductive hinge

2200/26 • Indexing scheme relating to G06F 1/26

2200/261 . . PC controlled powerstrip

2201/00 Indexing scheme relating to error detection, to error correction, and to monitoring

2201/80 . Database-specific techniques

2201/805 Real-time

2201/81 Threshold

2201/815 Virtual

2201/82 Solving problems relating to consistency

. the problem or solution involving locking

2201/83 • the solution involving signatures

2201/835 . Timestamp

• Using snapshots, i.e. a logical point-in-time copy of

2201/845 • Systems in which the redundancy can be

transformed in increased performance 2201/85 • Active fault masking without idle spares

2201/855 Details of asynchronous mirroring using a journal to transfer not-yet-mirrored changes

2201/86 . Event-based monitoring

2201/865 • Monitoring of software

. Monitoring of transactions

. Monitoring of systems including the internet

2201/88 • Monitoring involving counting 2201/885 . Monitoring specific for caches

2203/00 Indexing scheme relating to

G06F 3/00 - G06F 3/048

2203/01 • Indexing scheme relating to G06F 3/01

2203/011 • Emotion or mood input determined on the basis of sensed human body parameters such as pulse, heart rate or beat, temperature of skin, facial expressions, iris, voice pitch, brain activity patterns	 2203/04105 . Pressure sensors for measuring the pressure or force exerted on the touch surface without providing the touch position 2203/04106 . Multi-sensing digitiser, i.e. digitiser using at least two different sensing technologies simultaneously
2203/012 . Walk-in-place systems for allowing a user to walk in a virtual environment while constraining him to a given position in the physical environment	or alternatively, e.g. for detecting pen and finger, for saving power or for improving position detection
 2203/013 . Force feedback applied to a game 2203/014 . Force feedback applied to GUI 2203/015 . Force feedback applied to a joystick 	2203/04107 • Shielding in digitiser, i.e. guard or shielding arrangements, mostly for capacitive touchscreens, e.g. driven shields, driven grounds
 2203/033 . Indexing scheme relating to G06F 3/033 2203/0331 . Finger worn pointing device 	2203/04108 • Touchless 2D- digitiser, i.e. digitiser detecting the X/Y position of the input means, finger or stylus, also when it does not touch, but is proximate to
 2203/0332 . Ergonomic shaped mouse adjustable to suit one of both hands 2203/0333 . Ergonomic shaped mouse for one hand 	the digitiser's interaction surface without distance measurement in the Z direction
2203/0334 • Ergonomic shaped mouse for vertical grip, whereby the hand controlling the mouse is resting or gripping it with an attitude almost vertical with respect of the working surface	2203/04109 • FTIR in optical digitiser, i.e. touch detection by frustrating the total internal reflection within an optical waveguide due to changes of optical properties or deformation at the touch location
 2203/0335 . Finger operated miniaturized mouse 2203/0336 . Mouse integrated fingerprint sensor 2203/0337 . Status LEDs integrated in the mouse to provide 	2203/04111 • Cross over in capacitive digitiser, i.e. details of structures for connecting electrodes of the sensing pattern where the connections cross each other, e.g. bridge structures comprising an insulating
visual feedback to the user about the status of the input device, the PC, or the user 2203/0338 • Fingerprint track pad, i.e. fingerprint sensor used	layer, or vias through substrate 2203/04112 • Electrode mesh in capacitive digitiser: electrode for touch sensing is formed of a mesh of very
as pointing device tracking the fingertip image 2203/0339 • Touch strips, e.g. orthogonal touch strips to control cursor movement or scrolling; single touch strip to adjust parameter or to implement a row of soft keys	fine, normally metallic, interconnected lines that are almost invisible to see. This provides a quite large but transparent electrode surface, without need for ITO or similar transparent conductive material
 2203/038	2203/04113 • Peripheral electrode pattern in resistive digitisers, i.e. electrodes at the periphery of the resistive sheet are shaped in patterns enhancing linearity of induced field
nature, e.g. voice plus gesture on digitizer 2203/0382 . Plural input, i.e. interface arrangements in which a plurality of input device of the same type are in communication with a PC	2203/04114 . Touch screens adapted for alternating or simultaneous interaction with active pens and passive pointing devices like fingers or passive pens
2203/0383 • Remote input, i.e. interface arrangements in which the signals generated by a pointing device are transmitted to a PC at a remote location, e.g. to a PC in a LAN	 2203/048 Indexing scheme relating to G06F 3/048 Cursor retrieval aid, i.e. visual aspect modification, blinking, colour changes, enlargement or other visual cues, for helping user
 Wireless input, i.e. hardware and software details of wireless interface arrangements for pointing devices 	do find the cursor in graphical user interfaces 2203/04802 . 3D-info-object: information is displayed on the internal or external surface of a three dimensional
 Indexing scheme relating to G06F 3/041 - G06F 3/045 2203/04101 . 2.5D-digitiser, i.e. digitiser detecting the X/Y 	manipulable object, e.g. on the faces of a cube that can be rotated by the user 2203/04803 • • Split screen, i.e. subdividing the display area or
position of the input means, finger or stylus, also when it does not touch, but is proximate to the digitiser's interaction surface and also measures	the window area into separate subareas 2203/04804 . Transparency, e.g. transparent or translucent
the distance of the input means within a short range in the Z direction, possibly with a separate measurement setup	windows 2203/04805 • Virtual magnifying lens, i.e. window or frame movable on top of displayed information to enlarge it for better reading or selection
2203/04102 • Flexible digitiser, i.e. constructional details for allowing the whole digitising part of a device to be flexed or rolled like a sheet of paper	2203/04806 Zoom, i.e. interaction techniques or interactors for controlling the zooming operation
2203/04103 • Manufacturing, i.e. details related to manufacturing processes specially suited for touch sensitive devices	 2203/04807 . Pen manipulated menu 2203/04808 . Several contacts: gestures triggering a specific function, e.g. scrolling, zooming, right-click,
2203/04104 Multi-touch detection in digitiser, i.e. details about the simultaneous detection of a plurality of touching locations, e.g. multiple fingers or pen and finger	when the user establishes several contacts with the surface simultaneously; e.g. using several fingers or a combination of fingers and pen

	Textured surface identifying touch areas, e.g. overlay structure for a virtual keyboard	2206/20	Indexing scheme related to audio interfaces for computers, indexing schema related to group G06F 3/16
2205/00	Indexing scheme relating to group G06F 5/00; Methods or arrangements for data conversion	2207/00	Indexing scheme relating to methods or
	without changing the order or content of the data handled		arrangements for processing data by operating upon the order or content of the data handled
2205/003	• Reformatting, i.e. changing the format of data representation	2207/02	G06F 7/02 - G06F 7/026
2205/06	• Indexing scheme relating to groups <u>G06F 5/06</u> - <u>G06F 5/16</u>	2207/025	• • String search, i.e. pattern matching, e.g. find identical word or best match in a string
2205/061	Adapt frequency, i.e. clock frequency at one side is adapted to clock frequency, or average clock	2207/22	G06F 7/22 - G06F 7/36
	frequency, at the other side; Not pulse stuffing only	2207/222	Binary data tree
2205/062	Allowing rewriting or rereading data to or from	2207/224 2207/226	External sortingPriority queue, i.e. 1 word in, 1 word out sorter;
	the buffer	2201/220	Output word, i.e. min or max of words in memory
2205/063	Dynamically variable buffer size	2207/228	Sorting or merging network
2205/064	allowing non-contiguous address segments in one	2207/38	• Indexing scheme relating to groups <u>G06F 7/38</u> - <u>G06F 7/575</u>
2205/065	logical buffer or dynamic buffer space allocation . With bypass possibility	2207/3804	Details
2205/065 2205/066	With bypass possionity User-programmable number or size of buffers,	2207/3808	concerning the type of numbers or the way they
2203/000	i.e. number of separate buffers or their size can be allocated freely	2207/3812	are handled Devices capable of handling different types of numbers
2205/067	Bidirectional FIFO, i.e. system allowing data transfer in two directions	2207/3816	
2205/10	• Indexing scheme relating to groups <u>G06F 5/10</u> - <u>G06F 5/14</u>	2207/382	Reconfigurable for different fixed word lengths
2205/102	• Avoiding metastability, i.e. preventing hazards, e.g. by using Gray code counters	2207/3824	Accepting both fixed-point and floating-point numbers
2205/104	Delay lines	2207/3828	Multigauge devices, i.e. capable of handling
2205/106	Details of pointers, i.e. structure of the address		packed numbers without unpacking them
••••	generators	2207/3832	Less usual number representations
2205/108	Reading or writing the data blockwise, e.g. using an extra end-of-block pointer	2207/3836	One's complement
2205/12	Indexing scheme relating to groups	2207/384	Octal
2203/12	G06F 5/12 - G06F 5/14	2207/3844	Hexadecimal
2205/123	Contention resolution, i.e. resolving conflicts	2207/3848	Unit distance code
	between simultaneous read and write operations	2207/3852 2207/3856	Calculation with most significant digit first
2205/126	Monitoring of intermediate fill level, i.e. with		Operand swapping
	additional means for monitoring the fill level, e.g.	2207/386 2207/3864	Special constructional features Clockless, i.e. asynchronous operation used
2206/00	half full flag, almost empty flag Indexing scheme related to dedicated interfaces for	2207/3804	as a design principle (G06F 2207/3888 takes precedence)
	computers	2207/3868	Bypass control, i.e. possibility to transfer an
2206/10	. Indexing scheme related to storage interfaces		operand unchanged to the output
	for computers, indexing schema related to group	2207/3872	Precharge of output to prevent leakage
	<u>G06F 3/06</u>	2207/3876	Alternation of true and inverted stages
2206/1004	Defragmentation	2207/388	Skewing
2206/1008	Graphical user interface [GUI]	2207/3884	Pipelining
2206/1012 2206/1014	Load balancingOne time programmable [OTP] memory, e.g. PROM, WORM	2207/3888	• • • • • • • • • • • • • • • • • • •
2206/15	Indexing scheme related to printer interfaces for	2207/3892	Systolic array
	computers, indexing schema related to group	2207/3896	Bit slicing
	G06F 3/12	2207/48	Indexing scheme relating to groups
2206/1504	Cost estimation		G06F 7/48 - G06F 7/575
2206/1506	Degraded mode, e.g. in view of consumables depleted, thresholds reached	2207/4802	Special implementations
2206/1508	Load balancing	2207/4804	Associative memory or processor
2206/151	Pre-printed media, e.g. media stock, forms, logos	2207/4806	Cascode or current mode logic
2206/1512	Print-to a presentation device other than a printer,	2207/4808	Charge transfer devices
	e.g. e-reader, e-paper, tablet	2207/481 2207/4812	Counters performing arithmetic operations Multiplexers
2206/1514		2207/4012	· · · · · · · · · · · · · · · · · · ·

2207/4814 Non-logic devices, e.g. operational	2207/4922 Multi-operand adding or subtracting
amplifiers	2207/4923 • Incrementer or decrementer
2207/4816 • • • • Pass transistors	2207/4924 . Digit-parallel adding or subtracting
2207/4818 Threshold devices	2207/506 • Indexing scheme relating to groups
2207/482 using capacitive adding networks	G06F 7/506 - G06F 7/508
2207/4822 Majority gates	2207/5063 • 2-input gates, i.e. only using 2-input logical gates,
2207/4824 Neural networks	e.g. binary carry look-ahead, e.g. Kogge-Stone or
2207/4826 using transistors having multiple	Ladner-Fischer adder
electrodes of the same type, e.g. multi-	2207/535 • Indexing scheme relating to groups
emitter devices, neuron-MOS devices	<u>G06F 7/535</u> - <u>G06F 7/5375</u>
2207/4828 Negative resistance devices, e.g. tunnel	2207/5351 • Multiplicative non-restoring division, e.g. SRT,
diodes, gunn effect devices	using multiplication in quotient selection
. Indexing scheme relating to group G06F 7/483	2207/5352 . Non-restoring division not covered by
2207/4835 Computations with rational numbers	G06F 7/5375
2207/491 • Indexing scheme relating to groups	2207/5353 . Restoring division
<u>G06F 7/491</u> - <u>G06F 7/4917</u>	2207/5354 . Using table lookup, e.g. for digit selection in
2207/49105 . Determining 9's or 10's complement	division by digit recurrence
2207/4911 Decimal floating-point representation	2207/5355 . Using iterative approximation not using digit
2207/49115 Duodecimal numbers	recurrence, e.g. Newton Raphson or Goldschmidt 2207/5356 • Via reciprocal, i.e. calculate reciprocal only, or
2207/4912 Non-specified BCD representation	calculate reciprocal first and then the quotient
2207/49125 Non-specified decimal representation	from the reciprocal and the numerator
2207/4913 • Sterling system, i.e. mixed radix with digit	2207/544 • Indexing scheme relating to group G06F 7/544
weights of 10-20-12	2207/5442 Absolute difference
2207/49135 . Using 036012 or 3612 code, i.e. binary coded	2207/552 • Indexing scheme relating to groups
decimal representation with digit weight of (0,) 3, 6, (0,) 1 and 2 respectively	G06F 7/552 - G06F 7/5525
2207/4914 • Using 2-out-of-5 code, i.e. binary coded decimal	2207/5521 . Inverse root of a number or a function, e.g. the
representation with digit weight of 2, 4, 2 and 1	reciprocal of a Pythagorean sum
respectively	2207/5523 Calculates a power, e.g. the square, of a number
2207/49145 Using 2421 code, i.e. non-weighted	or a function, e.g. polynomials
representation in which 2 out of 5 bits are "1" for	2207/5525 • Pythagorean sum, i.e. the square root of a sum of
each decimal digit	squares
2207/4915 . Using 4221 code, i.e. binary coded decimal	2207/5526 • Roots or inverse roots of single operands
representation with digit weight of 4, 2, 2 and 1	2207/5528 Non-restoring calculation, where each result
respectively	digit is either negative, zero or positive, e.g.
2207/49155 . Using 51111 code, i.e. binary coded decimal	SRT
representation with digit weight of 5, 1, 1, 1 and 1	2207/556 • Indexing scheme relating to group G06F 7/556
respectively	2207/5561 • Exponentiation by multiplication, i.e. calculating
2207/4916 Using 5211 code, i.e. binary coded decimal	Y**INT(X) by multiplying Y with itself or a power of itself, INT(X) being the integer part of
representation with digit weight of 5, 2, 1 and 1 respectively	power of itself, in I(X) being the integer part of X
2207/49165 • Using 5311 code, i.e. binary coded decimal	2207/58 • Indexing scheme relating to groups
representation with digit weight of 5, 3, 1 and 1	G06F 7/58 - G06F 7/588
respectively	2207/581 Generating an LFSR sequence, e.g. an m-
2207/4917 . Using 5321 or 543210 code, i.e. binary coded	sequence; sequence may be generated without
decimal representation with digit weight of 5,(4,)	LFSR, e.g. using Galois Field arithmetic
3, 2, 1 (and 0) respectively	2207/582 . Parallel finite field implementation, i.e. at least
2207/49175 Using 54321 code, i.e. binary coded decimal	partially parallel implementation of finite field
representation with digit weight of 5, 4, 3, 2 and 1	arithmetic, generating several new bits or trits per
respectively	step, e.g. using a GF multiplier
2207/4918 Using Aiken code, i.e. using both first and last	2207/583 Serial finite field implementation, i.e. serial
5 of 16 possible 4-bit values, rendering the code	implementation of finite field arithmetic,
symmetrical within the series of 16 values	generating one new bit or trit per step, e.g. using an LFSR or several independent LFSRs; also
2207/49185 . Using biquinary code, i.e. combination of 5-	includes PRNGs with parallel operation between
valued and 2-valued digits, having values 0, 1, 2, 3, 4 and 0, 5 or 0, 2, 4, 6, 8 and 0, 1 respectively	LFSR and outputs
2207/4919 • Using excess-3 code, i.e. natural BCD + offset	2207/72 • Indexing scheme relating to groups
of 3, rendering the code symmetrical within the	G06F 7/72 - G06F 7/729
series of 16 possible 4 bit values	2207/7204 Prime number generation or prime number testing
2207/49195 . Using pure decimal representation, e.g. 10-valued	2207/7209 • Calculation via subfield, i.e. the subfield being
voltage signal, 1-out-of-10 code	GF(q) with q a prime power, e.g. GF ((2**m)**n)
2207/492 • Indexing scheme relating to groups	via GF(2**m)
<u>G06F 7/492</u> - <u>G06F 7/496</u>	
2207/4921 Single digit adding or subtracting	

2207/7214	. Calculation via prime subfield, i.e. the subfield	2209/503	. Resource availability
	being GF(p) with p an integer prime > 3 ; e.g.	2209/504	Resource capping
2207/7210	GF(p**k) via GF(p)	2209/505	Clust
2207/7219	Countermeasures against side channel or fault	2209/506	Constraint
	attacks	2209/507	Low-level
2207/7223	Randomisation as countermeasure against side	2209/508	Monitor
	channel attacks	2209/509	Offload
2207/7228	Random curve mapping, e.g. mapping to an	2209/52	• Indexing scheme relating to G06F 9/52
	isomorphous or projective curve	2209/521	. Atomic
2207/7233	Masking, e.g. (A**e)+r mod n	2209/522	Manager
2207/7238	Operand masking, i.e. message blinding,	2209/523	Mode
	e.g. $(A+r)**e \mod n$; k. $(P+R)$		
2207/7242	Exponent masking, i.e. key masking, e.g.	2209/54	Indexing scheme relating to G06F 9/54
	A**(e+r) mod n; (k+r).P	2209/541	Client-server
2207/7247	Modulo masking, e.g. A**e mod (n*r)	2209/542	Intercept
	of operation order, e.g. starting to treat the	2209/543	Local
220111232	exponent at a random place, or in a randomly	2209/544	Remote
	chosen direction	2209/545	Gui
2207/7257	Random modification not requiring	2209/546	Xcast
2201/1231		2209/547	Messaging middleware
2207/7261	correction	2209/548	Queue
2207/7261	Uniform execution, e.g. avoiding jumps, or		_
	using formulae with the same power profile	2209/549	Remote execution
2207/7266	Hardware adaptation, e.g. dual rail logic;	2211/00	Indexing scheme relating to details of data-
	calculate add and double simultaneously		processing equipment not covered by groups
2207/7271	, , , ,		G06F 3/00 - G06F 13/00
	values which should be the same, unless a	2211/001	. In-Line Device
	computational fault occurred	2211/001	Bus
2207/7276	Additional details of aspects covered by group		
	G06F 7/723	2211/003	Mutual Authentication Bi-Directional
2207/728	using repeated square-and-multiply, i.e. right-		Authentication, Dialogue, Handshake
	to-left binary exponentiation	2211/004	Notarisation, Time-Stamp, Date-Stamp
2207/7285	using the window method, i.e. left-to-right k-	2211/005	Network, LAN, Remote Access, Distributed System
220777200	ary exponentiation	2211/006	E-Mail
2207/729	Sliding-window exponentiation	2211/007	• Encryption, En-/decode, En-/decipher, En-/
2207/7295	using an addition chain, or an addition-		decypher, Scramble, (De-)compress
2201/1293	using an addition chain, or an addition-		
	subtraction chain	2211/008	• Public Key, Asymmetric Key, Asymmetric
	subtraction chain	2211/008	• • • • • • • • • • • • • • • • • • • •
2209/00			Public Key, Asymmetric Key, Asymmetric Encryption Trust
2209/00 2209/46	Indexing scheme relating to G06F 9/00	2211/009	Encryption . Trust
2209/46	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46	2211/009 2211/10	Encryption Trust Indexing scheme relating to G06F 11/10
2209/46 2209/461	 Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Bridge 	2211/009 2211/10 2211/1002	Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076
2209/46 2209/461 2209/462	 Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Bridge Lookup 	2211/009 2211/10 2211/1002	Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to
2209/46 2209/461 2209/462 2209/463	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Bridge Lookup Naming	2211/009 2211/10 2211/1002	Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes
2209/46 2209/461 2209/462 2209/463 2209/48	 Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Bridge Lookup Naming Indexing scheme relating to G06F 9/48 	2211/009 2211/10 2211/1002 2211/1004	Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481	 Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Bridge Lookup Naming Indexing scheme relating to G06F 9/48 Exception handling 	2211/009 2211/10 2211/1002 2211/1004	Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID,
2209/46 2209/461 2209/462 2209/463 2209/48	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Exception handling Application	2211/009 2211/10 2211/1002 2211/1004 2211/1007	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481	 Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Bridge Lookup Naming Indexing scheme relating to G06F 9/48 Exception handling 	2211/009 2211/10 2211/1002 2211/1004 2211/1007	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Exception handling Application	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Bridge Lookup Naming Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/486	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/486 2209/50	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/486 2209/50 2209/501	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/50 2209/501 2209/5011	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Lookup Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/50 2209/501 2209/5011 2209/5012	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/50 2209/501 2209/5011	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Lookup Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g.
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/50 2209/501 2209/5011 2209/5012	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1014 2211/1016	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/485 2209/486 2209/50 2209/501 2209/5011 2209/5012 2209/5013	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1014 2211/1016	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/485 2209/486 2209/50 2209/501 2209/5011 2209/5013 2209/5014 2209/5015	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1014 2211/1016	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/50 2209/50 2209/501 2209/5011 2209/5013 2209/5015 2209/5016	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection Session	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1014 2211/1016	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk Different size blocks, i.e. mapping of blocks of
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/483 2209/484 2209/485 2209/486 2209/50 2209/501 2209/5011 2209/5012 2209/5013 2209/5015 2209/5016 2209/5016 2209/5017	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/46 Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection Session Task decomposition	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1014 2211/1016 2211/1019 2211/1021	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk Different size blocks, i.e. mapping of blocks of different size in RAID systems with parity
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/483 2209/484 2209/485 2209/486 2209/50 2209/501 2209/5011 2209/5012 2209/5013 2209/5016 2209/5016 2209/5017 2209/5018	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Lookup Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection Session Task decomposition Thread allocation	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1014 2211/1016 2211/1019 2211/1021	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk Different size blocks, i.e. mapping of blocks of different size in RAID systems with parity Different size disks, i.e. non uniform size of
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/50 2209/501 2209/5011 2209/5012 2209/5013 2209/5014 2209/5015 2209/5017 2209/5018 2209/5019	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Lookup Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection Sersion Task decomposition Thread allocation Workload prediction	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1016 2211/1019 2211/1021 2211/1021	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk Different size blocks, i.e. mapping of blocks of different size in RAID systems with parity Different size disks, i.e. non uniform size of disks in RAID systems with parity
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/483 2209/483 2209/485 2209/486 2209/50 2209/501 2209/5011 2209/5012 2209/5013 2209/5014 2209/5015 2209/5016 2209/5017 2209/5018 2209/5019 2209/502	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Lookup Naming Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection Session Task decomposition Thread allocation Workload prediction Proximity	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1016 2211/1019 2211/1021 2211/1021	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk Different size blocks, i.e. mapping of blocks of different size in RAID systems with parity Different size disks, i.e. non uniform size of disks in RAID systems with parity Different size groups, i.e. non uniform size of
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/482 2209/483 2209/484 2209/485 2209/50 2209/501 2209/5011 2209/5012 2209/5013 2209/5014 2209/5015 2209/5017 2209/5018 2209/5019	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Lookup Indexing scheme relating to G06F 9/48 Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection Sersion Task decomposition Thread allocation Workload prediction	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1016 2211/1019 2211/1021 2211/1021	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk Different size blocks, i.e. mapping of blocks of different size in RAID systems with parity Different size disks, i.e. non uniform size of disks in RAID systems with parity
2209/46 2209/461 2209/462 2209/463 2209/48 2209/481 2209/483 2209/483 2209/485 2209/486 2209/50 2209/501 2209/5011 2209/5012 2209/5013 2209/5014 2209/5015 2209/5016 2209/5017 2209/5018 2209/5019 2209/502	Indexing scheme relating to G06F 9/00 Indexing scheme relating to G06F 9/46 Lookup Naming Indexing scheme relating to G06F 9/48 Exception handling Application Multiproc Precedence Resource constraint Scheduler internals Indexing scheme relating to G06F 9/50 Performance criteria Pool Processor sets Request control Reservation Service provider selection Session Task decomposition Thread allocation Workload prediction Proximity	2211/009 2211/10 2211/1002 2211/1004 2211/1007 2211/1009 2211/1011 2211/1016 2211/1019 2211/1021 2211/1021	 Encryption Trust Indexing scheme relating to G06F 11/10 Indexing scheme relating to G06F 11/1076 Adaptive RAID, i.e. RAID system adapts to changing circumstances, e.g. RAID1 becomes RAID5 as disks fill up Addressing errors, i.e. silent errors in RAID, e.g. sector slipping and addressing errors Cache, i.e. caches used in RAID system with parity Clustered RAID, i.e. clustered or de-clustered RAID where data and parity are spread over more disks than blocks in a parity group Compression, i.e. RAID systems with parity using compression techniques Continuous RAID, i.e. RAID system that allows streaming or continuous media, e.g. VOD Fast writes, i.e. signaling the host that a write is done before data is written to disk Different size blocks, i.e. mapping of blocks of different size in RAID systems with parity Different size disks, i.e. non uniform size of disks in RAID systems with parity Different size groups, i.e. non uniform size of

2211/1028	Distributed, i.e. distributed RAID systems with		Compatibility, e.g. with legacy hardware
	parity	2212/1008	Correctness of operation, e.g. memory ordering
2211/103	Hybrid, i.e. RAID systems with parity	2212/1012	Design facilitation
2211/1022	comprising a mix of RAID types		Performance improvement
2211/1033	Inactive data in parity groups, i.e. RAID parity groups where parity is calculated on only	2212/1021	Hit rate improvement
	occupied or busy bits in the stripe		Latency reduction
2211/1035	Keeping track, i.e. keeping track of data and	2212/1028	Power efficiency
2211/1033	parity changes	2212/1032	J 1 , 1
2211/1038	LFS, i.e. Log Structured File System used in		degraded operation etc
2211,1000	RAID systems with parity		Life time enhancement
2211/104	Metadata, i.e. metadata associated with RAID		Resource optimization
	systems with parity		Space efficiency improvement
2211/1042	NanoRAID, i.e. RAID systems using		Scalability
	nanotechnology		Security improvement
2211/1045	Nested RAID, i.e. implementing a RAID	2212/1056	Simplification
	scheme in another RAID scheme	2212/15	Use in a specific computing environment
2211/1047	No striping, i.e. parity calculation on a RAID	2212/151	Emulated environment, e.g. virtual machine Virtualized environment, e.g. logically partitioned
	involving no stripes, where a stripe is an	2212/152	system
	independent set of data	2212/154	Networked environment
2211/105	On the fly coding, e.g. using XOR	2212/134	General purpose computing application
2211/1052	accumulators	2212/16	Portable computer, e.g. notebook
2211/1052	RAID padding, i.e. completing a redundancy group with dummy data	2212/163	Server or database system
2211/1054	Parity-fast hardware, i.e. dedicated fast	2212/165	Mainframe system
2211/1034	hardware for RAID systems with parity	2212/103	Embedded application
2211/1057	Parity-multiple bits-RAID6, i.e. RAID 6	2212/17	Portable consumer electronics, e.g. mobile phone
2211/1037	implementations	2212/171	Non-portable consumer electronics
2211/1059	• • Parity-single bit-RAID5, i.e. RAID 5	2212/1721	Home entertainment system, e.g. television set
	implementations	2212/1721	Vehicle or other transportation
2211/1061	Parity-single bit-RAID4, i.e. RAID 4	2212/174	Telecommunications system
	implementations	2212/175	Industrial control system
2211/1064	• • • Parity-single bit-RAID3, i.e. RAID 3	2212/177	. Smart card
	implementations	2212/178	Electronic token or RFID
2211/1066	Parity-small-writes, i.e. improved small or partial write techniques in RAID systems	2212/20	Employing a main memory using a specific memory
2211/1069	• • • Phantom write, i.e. write were nothing is	2212/202	technology
	actually written on the disk of a RAID system	2212/202	Non-volatile memory
2211/1071	Power loss, i.e. interrupted writes due to power	2212/2022	Flash memory
2211/1072	loss in a RAID system	2212/2024	• • Rewritable memory not requiring erasing, e.g. resistive or ferroelectric RAM
2211/1073	Problems due to wear-out failures in RAID systems	2212/2028	Battery-backed RAM
2211/1076	RAIP, i.e. RAID on platters	2212/205	Hybrid memory, e.g. using both volatile and non-
2211/1078	RAIR, i.e. RAID on removable media		volatile memory
2211/1076	RAIT, i.e. RAID on tape drive	2212/206	Memory mapped I/O
2211/1083	Reserve area on a disk of a RAID system	2212/21	Employing a record carrier using a specific
2211/1085	RMW, i.e. Read-Modify-Write method for		recording technology
2211,1003	RAID systems	2212/211	. Optical disk storage
2211/1088		2212/2112	• • • with a removable carrier, e.g. DVD
	Sector level checksum or ECC, i.e. sector or	2212/213	Tape storage
	• • • Sector level checksum of Lee, i.e. sector of	2212/214	
	stripe level checksum or ECC in addition to the		Solid state disk
		2212/2142	using write-once memory, e.g. OTPROM
2211/1092	stripe level checksum or ECC in addition to the RAID parity calculation	2212/2142 2212/2146	using write-once memory, e.g. OTPROMbeing detachable, e.g USB memory
2211/1092	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk	2212/2142	 using write-once memory, e.g. OTPROM being detachable, e.g USB memory Hybrid disk, e.g. using both magnetic and solid
	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the	2212/2142 2212/2146	using write-once memory, e.g. OTPROMbeing detachable, e.g USB memory
2211/1092 2211/1095	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the number of writes in a RAID array with parity	2212/2142 2212/2146 2212/217	using write-once memory, e.g. OTPROM being detachable, e.g USB memory . Hybrid disk, e.g. using both magnetic and solid state storage devices . Employing cache memory using specific memory technology
2211/1092 2211/1095 2211/1097	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the number of writes in a RAID array with parity . Boot, Start, Initialise, Power	2212/2142 2212/2146 2212/217	 using write-once memory, e.g. OTPROM being detachable, e.g USB memory Hybrid disk, e.g. using both magnetic and solid state storage devices Employing cache memory using specific memory
2211/1092 2211/1095	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the number of writes in a RAID array with parity	2212/2142 2212/2146 2212/217 2212/22 2212/221 2212/222	 using write-once memory, e.g. OTPROM being detachable, e.g USB memory Hybrid disk, e.g. using both magnetic and solid state storage devices Employing cache memory using specific memory technology Static RAM Non-volatile memory
2211/1092 2211/1095 2211/1097 2211/902	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the number of writes in a RAID array with parity . Boot, Start, Initialise, Power . Spectral purity improvement for digital function generators by adding a dither signal, e.g. noise	2212/2142 2212/2146 2212/217 2212/22 2212/221 2212/222 2212/222 2212/2228	 using write-once memory, e.g. OTPROM being detachable, e.g USB memory Hybrid disk, e.g. using both magnetic and solid state storage devices Employing cache memory using specific memory technology Static RAM Non-volatile memory Battery-backed RAM
2211/1092 2211/1095 2211/1097	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the number of writes in a RAID array with parity . Boot, Start, Initialise, Power . Spectral purity improvement for digital function generators by adding a dither signal, e.g. noise Indexing scheme relating to accessing, addressing	2212/2142 2212/2146 2212/217 2212/22 2212/221 2212/222 2212/2228 2212/224	 using write-once memory, e.g. OTPROM being detachable, e.g USB memory Hybrid disk, e.g. using both magnetic and solid state storage devices Employing cache memory using specific memory technology Static RAM Non-volatile memory Battery-backed RAM Disk storage
2211/1092 2211/1095 2211/1097 2211/902	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the number of writes in a RAID array with parity . Boot, Start, Initialise, Power . Spectral purity improvement for digital function generators by adding a dither signal, e.g. noise Indexing scheme relating to accessing, addressing or allocation within memory systems or	2212/2142 2212/2146 2212/217 2212/22 2212/221 2212/222 2212/222 2212/2228	 using write-once memory, e.g. OTPROM being detachable, e.g USB memory Hybrid disk, e.g. using both magnetic and solid state storage devices Employing cache memory using specific memory technology Static RAM Non-volatile memory Battery-backed RAM Disk storage Hybrid cache memory, e.g. having both volatile
2211/1092 2211/1095 2211/1097 2211/902	stripe level checksum or ECC in addition to the RAID parity calculation Single disk raid, i.e. RAID with parity on a single disk Writes number reduction, i.e. reducing the number of writes in a RAID array with parity . Boot, Start, Initialise, Power . Spectral purity improvement for digital function generators by adding a dither signal, e.g. noise Indexing scheme relating to accessing, addressing	2212/2142 2212/2146 2212/217 2212/22 2212/221 2212/222 2212/2228 2212/224	 using write-once memory, e.g. OTPROM being detachable, e.g USB memory Hybrid disk, e.g. using both magnetic and solid state storage devices Employing cache memory using specific memory technology Static RAM Non-volatile memory Battery-backed RAM Disk storage

2212/251	Local memory within processor subsystem	2212/50	. Control mechanisms for virtual memory, cache or
2212/2515	• • • being configurable for different purposes, e.g.		TLB
	as cache or non-cache memory	2212/502	using adaptive policy
2212/253	Centralized memory	2212/507	using speculative control
2212/2532	comprising a plurality of modules	2212/60	. Details of cache memory
2212/254	Distributed memory	2212/601	Reconfiguration of cache memory
2212/2542	Non-uniform memory access [NUMA]	2212/6012	of operating mode, e.g. cache mode or local
	architecture		memory mode
2212/26	Using a specific storage system architecture	2212/602	Details relating to cache prefetching
2212/261	Storage comprising a plurality of storage devices	2212/6022	Using a prefetch buffer or dedicated prefetch
2212/262	configured as RAID		cache
2212/263	Network storage, e.g. SAN or NAS	2212/6024	History based prefetching
2212/264	Remote server	2212/6026	Prefetching based on access pattern detection, e.g.
2212/27	Using a specific cache architecture		stride based prefetch
2212/271	Non-uniform cache access [NUCA] architecture	2212/6028	Prefetching based on hints or prefetch instructions
2212/272	Cache only memory architecture [COMA]	2212/603	of operating mode, e.g. cache mode or local
2212/28	Using a specific disk cache architecture		memory mode
2212/281	Single cache	2212/6032	Way prediction in set-associative cache
2212/282	Partitioned cache	2212/604	Details relating to cache allocation
2212/282	Plural cache memories	2212/6042	Allocation of cache space to multiple users or
2212/284	being distributed		processors
		2212/6046	Using a specific cache allocation policy other
2212/285	Redundant cache memory		than replacement policy
2212/286	Mirrored cache memory	2212/608	Details relating to cache mapping
2212/30	Providing cache or TLB in specific location of a	2212/6082	Way prediction in set-associative cache
2212/201	processing system	2212/62	Details of cache specific to multiprocessor cache
2212/301	. In special purpose processing node, e.g. vector		arrangements
2212/202	processor	2212/621	Coherency control relating to peripheral
2212/302	. In image processor or graphics adapter		accessing, e.g. from DMA or I/O device
2212/303	. In peripheral interface, e.g. I/O adapter or channel	2212/622	State-only directory, i.e. not recording identity of
2212/3035	. In peripheral device, e.g. printer		sharing or owning nodes
2212/304	. In main memory subsystem	2212/65	Details of virtual memory and virtual address
2212/3042	 being part of a memory device, e.g. cache		translation
2212/205	DRAM	2212/651	Multi-level translation tables
2212/305	being part of a memory device, e.g. cache DRAM	2212/652	Page size control
2212/306	. In system interconnect, e.g. between two buses	2212/653	Page colouring
2212/31	Providing disk cache in a specific location of a	2212/654	Look-ahead translation
2212/211	storage system	2212/655	Same page detection
2212/311	. In host system	2212/656	Address space sharing
2212/312	. In storage controller	2212/657	Virtual address space management
2212/313	. In storage device	2212/68	Details of translation look-aside buffer [TLB]
2212/314	. In storage network, e.g. network attached cache	2212/681	Multi-level TLB, e.g. microTLB and main TLB
2212/40	Specific encoding of data in memory or cache	2212/682	Multiprocessor TLB consistency
2212/401	Compressed data	2212/683	Invalidation
2212/402	Encrypted data	2212/684	TLB miss handling
2212/403	Error protection encoding, e.g. using parity or	2212/70	Details relating to dynamic memory management
	ECC codes	2212/702	Conservative garbage collection
2212/45	Caching of specific data in cache memory	2212/702	Details relating to flash memory management
2212/451	Stack data		
2212/452	Instruction code	2212/7201	Logical to physical mapping or translation of blocks or pages
2212/453	Microcode or microprogram	2212/7202	Allocation control and policies
2212/454	Vector or matrix data		Temporary buffering, e.g. using volatile buffer or
2212/455	Image or video data	2212/7203	dedicated buffer blocks
2212/46	Caching storage objects of specific type in disk	2212/7204	Capacity control, e.g. partitioning, end-of-life
	cache	2212/7204	degradation
2212/461	Sector or disk block	2212/7205	Cleaning, compaction, garbage collection, erase
2212/462	Track or segment	2212/7205	control
2212/463	File	2212/7206	
2212/464	Multimedia object, e.g. image, video		Reconfiguration of flash memory system management of metadata or control data
2212/465	Structured object, e.g. database record	2212/7207	-
2212/466	Metadata, control data	2212/7208	Multiple device management, e.g. distributing data over multiple flash devices
2212/468	. The specific object being partially cached	2212/7200	Validity control, e.g. using flags, time stamps or
		2212/7209	sequence numbers
			sequence numbers

2212/7211	Wear leveling	2213/3604	Coding information on multiple lines
2212/7211	Wear leveling	2213/38	Universal adapter
2213/00	Indexing scheme relating to interconnection	2213/3802	Harddisk connected to a computer port
	of, or transfer of information or other signals	2213/3804	Memory card connected to a computer port
	between, memories, input/output devices or central	2210,000.	directly or by means of a reader/writer
2212/0002	processing units	2213/3806	Mobile device
2213/0002	Serial port, e.g. RS232C	2213/3808	Network interface controller
2213/0004	Parallel ports, e.g. centronics	2213/3812	USB port controller
2213/0006	Extension to the industry standard architecture [EISA]	2213/3814	Wireless link with a computer system port
2213/0008	High speed serial bus, e.g. Fiber channel	2213/3852	Converter between protocols
2213/0012	High speed serial bus, e.g. IEEE P1394	2213/3854	Control is performed at the peripheral side
2213/0014	Futurebus	2213/40	Bus coupling
2213/0014	Inter-integrated circuit (I2C)	2213/4002	Universal serial bus hub with a single upstream
2213/0018	Industry standard architecture [ISA]		port
2213/0022	• Multibus	2213/4004	Universal serial bus hub with a plurality of
2213/0024	Peripheral component interconnect [PCI]		upstream ports
2213/0026	• PCI express	2216/00	Indexing scheme relating to additional aspects
2213/0028	Serial attached SCSI [SAS]	2210/00	of information retrieval not explicitly covered by
2213/0032	• Serial ATA [SATA]		G06F 16/00 and subgroups
2213/0034	Sun microsystems bus [SBus]	2216/01	Automatic library building
2213/0036	Small computer system interface [SCSI]	2216/03	Data mining
2213/0038	System on Chip	2216/05	Energy-efficient information retrieval
2213/0042	Universal serial bus [USB]	2216/07	. Guided tours
2213/0044	Versatile modular eurobus [VME]	2216/09	. Obsolescence
2213/0052	Assignment of addresses or identifiers to the	2216/11	• Patent retrieval
	modules of a bus system	2216/13	• Prefetching
2213/0054	Split transaction bus	2216/15	Synchronised browsing
2213/0056	Use of address and non-data lines as data lines for	2216/17	. Web printing
	specific data transfers to temporarily enlarge the	Indovina ach	ome associated with group COSE 18/00 valating to
2212/0059	data bus and increase information transfer rate		eme associated with group G06F 18/00, relating to entition specially adapted for signal processing
2213/0058	Bus-related hardware virtualisation	patterniceog	sittion speciarly adapted for signal processing
	Dandryidth consumption adviction during transform		
2213/0062	Bandwidth consumption reduction during transfers Latency reduction in handling transfers	2218/00	Aspects of pattern recognition specially adapted
2213/0064	Latency reduction in handling transfers		for signal processing
2213/0064 2213/16	Latency reduction in handling transfers Memory access	2218/02	for signal processing . Preprocessing
2213/0064 2213/16 2213/1602	 Latency reduction in handling transfers Memory access Memory access type 	2218/02 2218/04	for signal processingPreprocessingDenoising
2213/0064 2213/16 2213/1602 2213/24	 Latency reduction in handling transfers Memory access Memory access type Interrupt 	2218/02	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using
2213/0064 2213/16 2213/1602 2213/24 2213/2402	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation 	2218/02 2218/04 2218/06	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis
2213/0064 2213/16 2213/1602 2213/24 2213/2402	 Latency reduction in handling transfers Memory access Memory access type Interrupt 	2218/02 2218/04 2218/06 2218/08	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction
2213/0064 2213/16 2213/1602 2213/24 2213/2402	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts 	2218/02 2218/04 2218/06	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g.
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts 	2218/02 2218/04 2218/06 2218/08 2218/10	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching by matching peak patterns
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12	for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching by matching peak patterns by matching signal segments
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406 2213/2408	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16	 for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching by matching peak patterns
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406 2213/2408 2213/2412	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16	for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching by matching peak patterns by matching signal segments by plotting the signal segments against each
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406 2213/2408	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18	for signal processing Preprocessing Denoising Lenoising Feature straction Feature extraction Feature
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406 2213/2412 2213/2412	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22	for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching by matching peak patterns by matching signal segments by plotting the signal segments against each other, e.g. analysing scattergrams by applying autoregressive analysis Source localisation; Inverse modelling
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406 2213/2408 2213/2412	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18	for signal processing Preprocessing Denoising By applying a scale-space analysis, e.g. using wavelet analysis Feature extraction By analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching By matching peak patterns By matching signal segments By matching signal segments By plotting the signal segments against each other, e.g. analysing scattergrams By applying autoregressive analysis Source localisation; Inverse modelling Indexing scheme relating to application aspects of
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00	for signal processing Preprocessing Denoising Peace analysis Feature extraction Peace analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching Peace analysing signal segments Peace analysing segments Peace analysing segments Peace analysing segments Peace analysing scattergrams Peace analysing scattergram
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22	for signal processing Preprocessing Denoising Peature extraction Feature extraction Polyanalysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching Polymatching peak patterns Polymatching signal segments Polymatching signal segments Polymatching the signal segments against each other, e.g. analysing scattergrams Polymatching signal segments Polymatching signal segments against each other, e.g. analysing scattergrams Polymatching scattergrams Polymatching signal segments against each other, e.g. analysing scattergrams Polymatching signal segments analysis Polymatching segments analys
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00	for signal processing Preprocessing Denoising by applying a scale-space analysis, e.g. using wavelet analysis Feature extraction by analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching by matching peak patterns by matching signal segments by matching signal segments by plotting the signal segments against each other, e.g. analysing scattergrams by applying autoregressive analysis Source localisation; Inverse modelling Indexing scheme relating to application aspects of data processing equipment or methods
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00 2219/10	for signal processing Preprocessing Denoising By applying a scale-space analysis, e.g. using wavelet analysis Feature extraction By analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching By matching peak patterns By matching signal segments By matching signal segments By plotting the signal segments against each other, e.g. analysing scattergrams By applying autoregressive analysis Bource localisation; Inverse modelling Indexing scheme relating to application aspects of data processing equipment or methods Environmental application, e.g. waste reduction, pollution control, compliance with environmental legislation
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406 2213/2412 2213/2412 2213/2414 2213/2416 2213/2418 2213/2422	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00	for signal processing Preprocessing Denoising By applying a scale-space analysis, e.g. using wavelet analysis Feature extraction By analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching By matching peak patterns By matching signal segments By matching signal segments By plotting the signal segments against each other, e.g. analysing scattergrams By applying autoregressive analysis Bource localisation; Inverse modelling Indexing scheme relating to application aspects of data processing equipment or methods Environmental application, e.g. waste reduction, pollution control, compliance with environmental legislation Indexing scheme relating to security arrangements
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2406 2213/2412 2213/2412 2213/2414 2213/2416 2213/2418 2213/2422 2213/2424	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources Interrupt packet, e.g. event 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00 2219/10	for signal processing Preprocessing Denoising Preprocessing Denoising Preprocessing Prepro
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418 2213/2422 2213/2424 2213/2424	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources Interrupt packet, e.g. event DMA DMA using DMA transfer descriptors Systems and methods for controlling the DMA 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00 2219/10	for signal processing Preprocessing Denoising Preprocessing Pr
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418 2213/2422 2213/2422 2213/2422 2213/2802 2213/2804	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources Interrupt packet, e.g. event DMA DMA using DMA transfer descriptors Systems and methods for controlling the DMA frequency on an access bus 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00 2219/10	for signal processing Preprocessing Denoising Preprocessing Denoising Preprocessing Prepro
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418 2213/2422 2213/2422 2213/2424 2213/2802 2213/2804	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources Interrupt packet, e.g. event DMA DMA using DMA transfer descriptors Systems and methods for controlling the DMA frequency on an access bus Space or buffer allocation for DMA transfers 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00 2219/10	for signal processing Preprocessing Denoising Preprocessing Pr
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418 2213/2418 2213/2422 2213/2424 2213/2802 2213/2804 2213/2806 2213/2808	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources Interrupt packet, e.g. event DMA DMA using DMA transfer descriptors Systems and methods for controlling the DMA frequency on an access bus Space or buffer allocation for DMA transfers Very long instruction word DMA 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/18 2218/20 2218/22 2219/00 2219/10	for signal processing Preprocessing Denoising Denoising Feature straction By analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching By matching peak patterns By matching signal segments By matching signal segments By plotting the signal segments against each other, e.g. analysing scattergrams By applying autoregressive analysis Bource localisation; Inverse modelling Indexing scheme relating to application aspects of data processing equipment or methods Environmental application, e.g. waste reduction, pollution control, compliance with environmental legislation Indexing scheme relating to security arrangements for protecting computers, components thereof, programs or data against unauthorised activity Indexing scheme relating to G06F 21/50, monitoring users, programs or devices to maintain the integrity of platforms Protect user input by software means
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418 2213/2418 2213/2422 2213/2424 2213/2802 2213/2804 2213/2808 2213/2808 2213/2808 2213/2808	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources Interrupt packet, e.g. event DMA DMA using DMA transfer descriptors Systems and methods for controlling the DMA frequency on an access bus Space or buffer allocation for DMA transfers Very long instruction word DMA Arbitration 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/20 2218/22 2219/00 2219/10 2221/00	for signal processing Preprocessing Denoising Feature straction Feature extraction By analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching By matching peak patterns By matching signal segments By plotting the signal segments against each other, e.g. analysing scattergrams By applying autoregressive analysis Source localisation; Inverse modelling Indexing scheme relating to application aspects of data processing equipment or methods Environmental application, e.g. waste reduction, pollution control, compliance with environmental legislation Indexing scheme relating to security arrangements for protecting computers, components thereof, programs or data against unauthorised activity Indexing scheme relating to G06F 21/50, monitoring users, programs or devices to maintain the integrity of platforms
2213/0064 2213/16 2213/1602 2213/24 2213/2402 2213/2404 2213/2408 2213/2412 2213/2414 2213/2416 2213/2418 2213/2418 2213/2422 2213/2424 2213/2802 2213/2804 2213/2806 2213/2808	 Latency reduction in handling transfers Memory access Memory access type Interrupt Avoidance of interrupt starvation Generation of an interrupt or a group of interrupts after a predetermined number of interrupts Generation of an interrupt or a group of interrupts after a fixed or calculated time elapses Reducing the frequency of interrupts generated from peripheral to a CPU Dispatching of interrupt load among interrupt handlers in processor system or interrupt controller Routing of interrupt among interrupt handlers in processor system or interrupt controller Determination of the interrupt source among a plurality of incoming interrupts Signal interruptions by means of a message Sharing of interrupt line among a plurality of interrupt sources Interrupt packet, e.g. event DMA DMA using DMA transfer descriptors Systems and methods for controlling the DMA frequency on an access bus Space or buffer allocation for DMA transfers Very long instruction word DMA 	2218/02 2218/04 2218/06 2218/08 2218/10 2218/12 2218/14 2218/16 2218/20 2218/22 2219/00 2219/10 2221/03	for signal processing Preprocessing Denoising Denoising Feature straction By analysing the shape of a waveform, e.g. extracting parameters relating to peaks Classification; Matching By matching peak patterns By matching signal segments By matching signal segments By plotting the signal segments against each other, e.g. analysing scattergrams By applying autoregressive analysis Bource localisation; Inverse modelling Indexing scheme relating to application aspects of data processing equipment or methods Environmental application, e.g. waste reduction, pollution control, compliance with environmental legislation Indexing scheme relating to security arrangements for protecting computers, components thereof, programs or data against unauthorised activity Indexing scheme relating to G06F 21/50, monitoring users, programs or devices to maintain the integrity of platforms Protect user input by software means

Indexing scheme associated with group G06F18/00, relating to pattern recognition specially adapted for signal processing

organia process	6
2221/034	Test or assess a computer or a system
2221/21	• Indexing scheme relating to G06F 21/00 and subgroups addressing additional information or applications relating to security arrangements for protecting computers, components thereof, programs or data against unauthorised activity
2221/2101	Auditing as a secondary aspect
2221/2103	Challenge-response
2221/2105	Dual mode as a secondary aspect
2221/2107	File encryption
2221/2109	Game systems
2221/2111	• Location-sensitive, e.g. geographical location, GPS
2221/2113	Multi-level security, e.g. mandatory access control
2221/2115	Third party
2221/2117	User registration
2221/2119	Authenticating web pages, e.g. with suspicious links
2221/2121	• Chip on media, e.g. a disk or tape with a chip embedded in its case
2221/2123	Dummy operation
2221/2125	Just-in-time application of countermeasures, e.g., on-the-fly decryption, just-in-time obfuscation or de-obfuscation
2221/2127	Bluffing
2221/2129	Authenticate client device independently of the user
2221/2131	Lost password, e.g. recovery of lost or forgotten passwords
2221/2133	Verifying human interaction, e.g., Captcha
2221/2135	Metering
2221/2137	Time limited access, e.g. to a computer or data
2221/2139	Recurrent verification
2221/2141	Access rights, e.g. capability lists, access control lists, access tables, access matrices
2221/2143	• Clearing memory, e.g. to prevent the data from being stolen
2221/2145	• Inheriting rights or properties, e.g., propagation of permissions or restrictions within a hierarchy
2221/2147	Locking files
2221/2149	Restricted operating environment
2221/2151	Time stamp
2221/2153	Using hardware token as a secondary aspect